

1

2

3

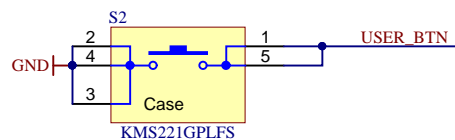
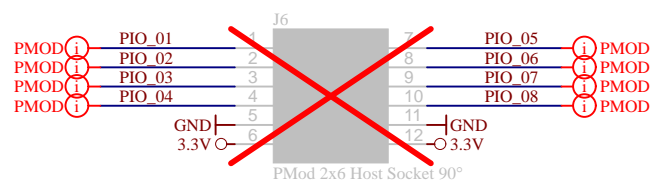
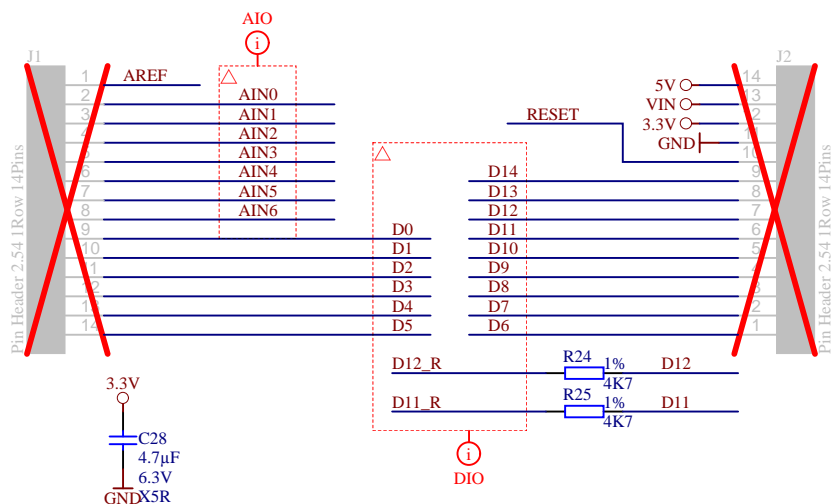
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A

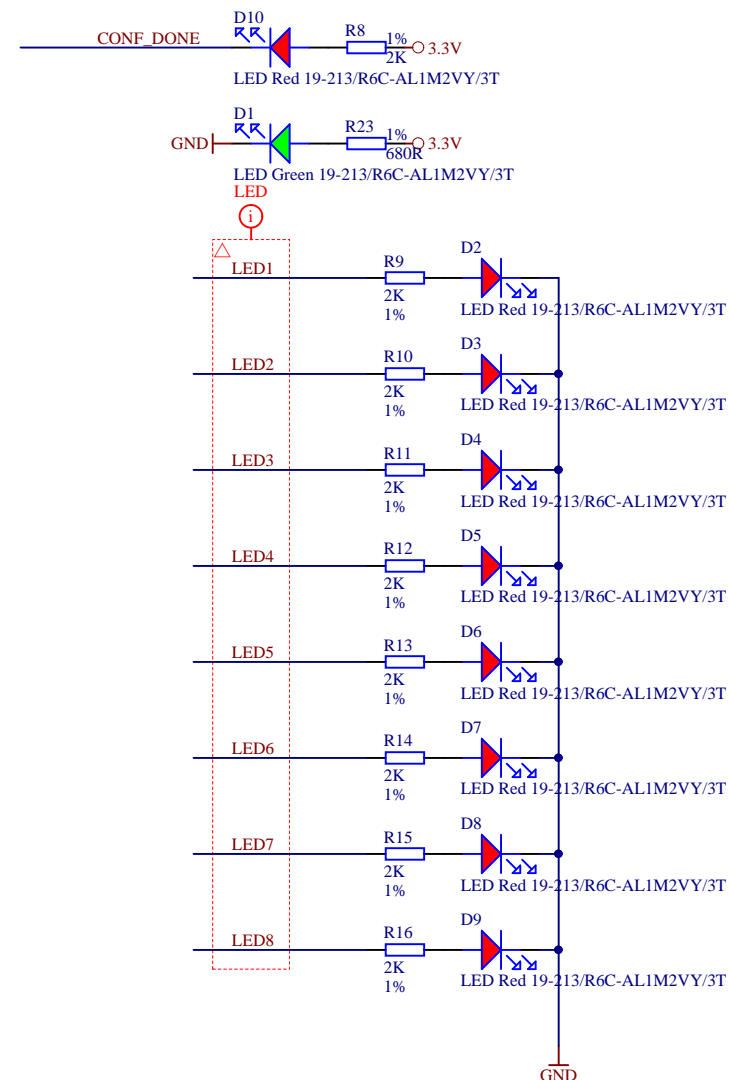
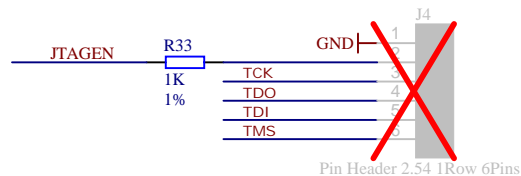
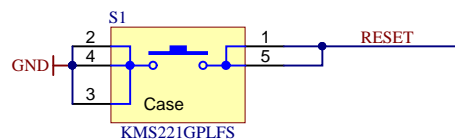
B

C

D



DUAL Function: JTAG or USER I/O if FT232H not installed



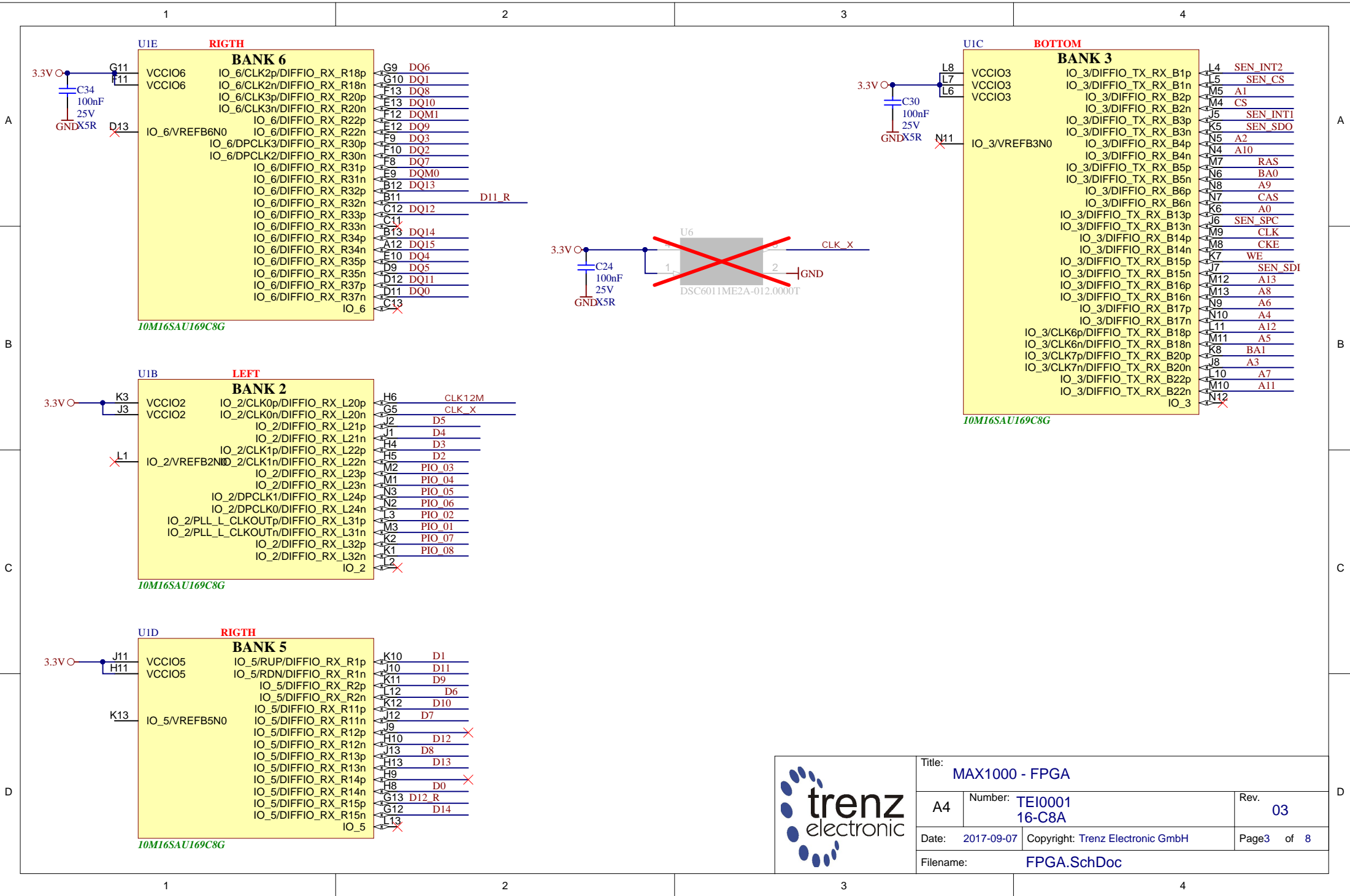
Title: MAX1000 - Headers		
A4	Number: TEI0001 16-C8A	Rev. 03
Date: 2017-09-07	Copyright: Trenz Electronic GmbH	Page2 of 8
Filename: A-Headers.SchDoc		


1

2

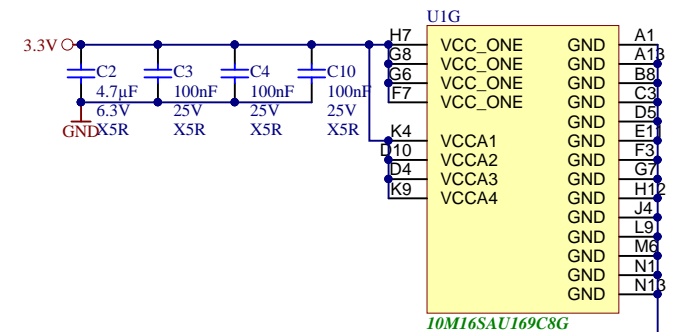
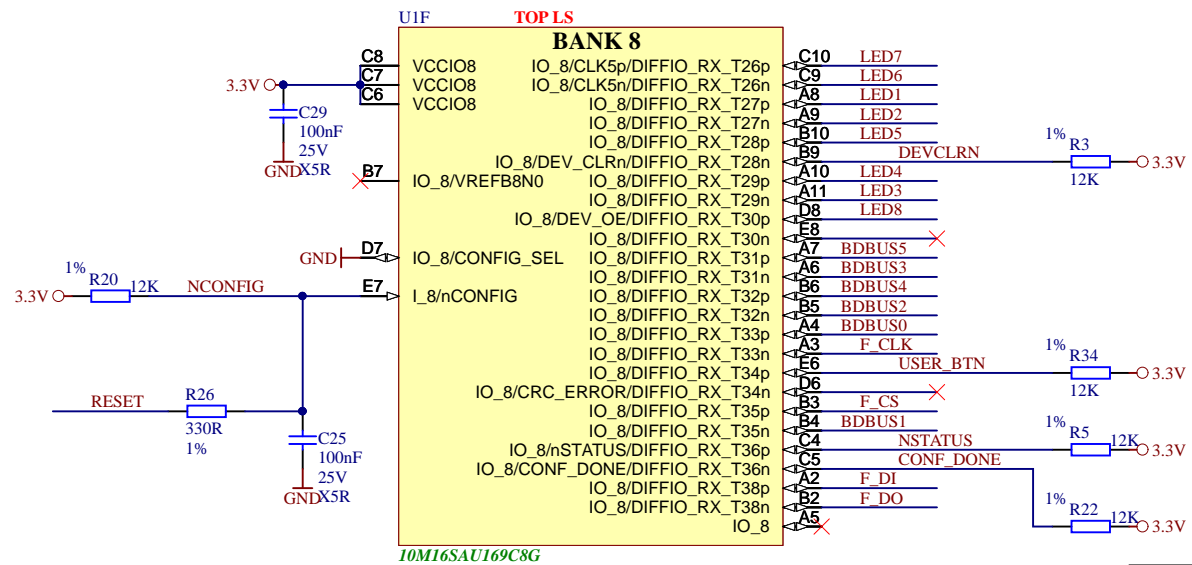
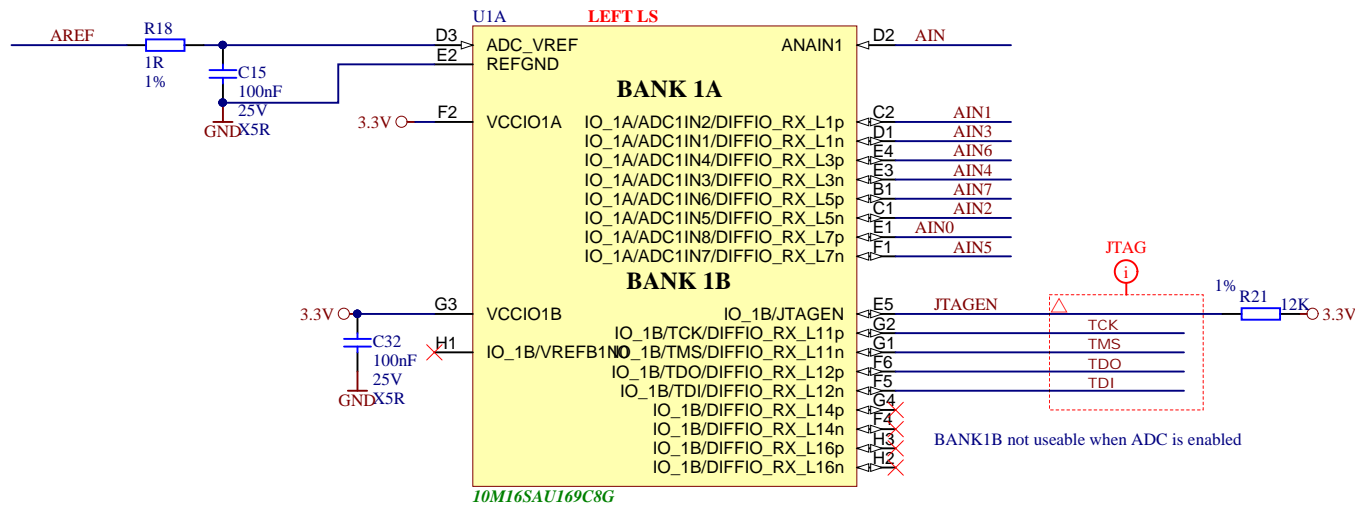
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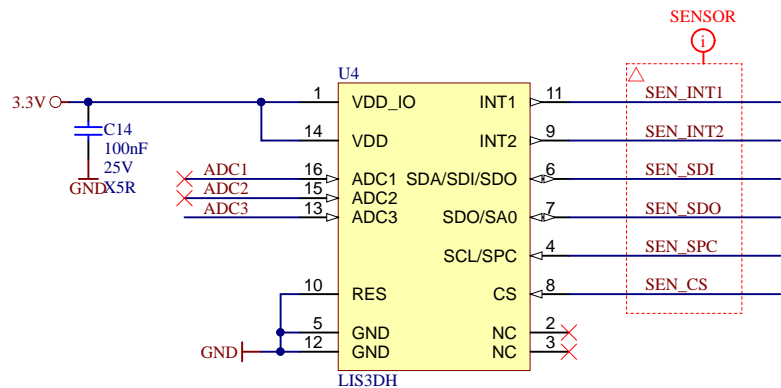
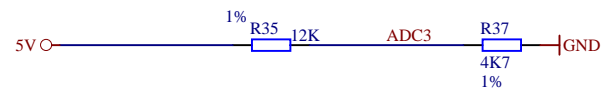
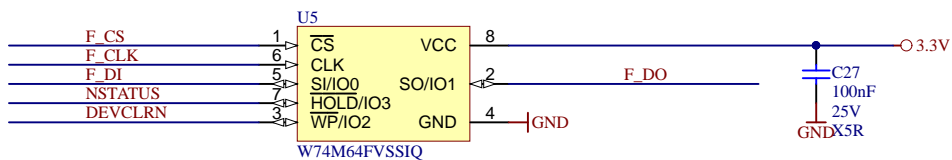
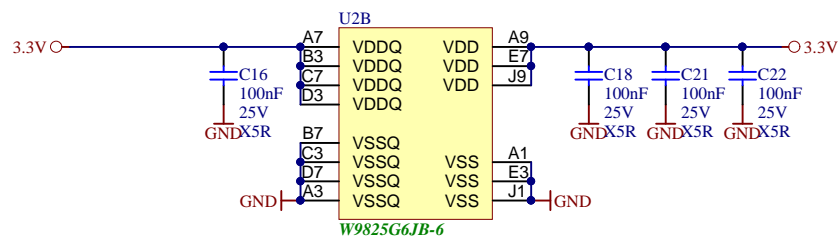
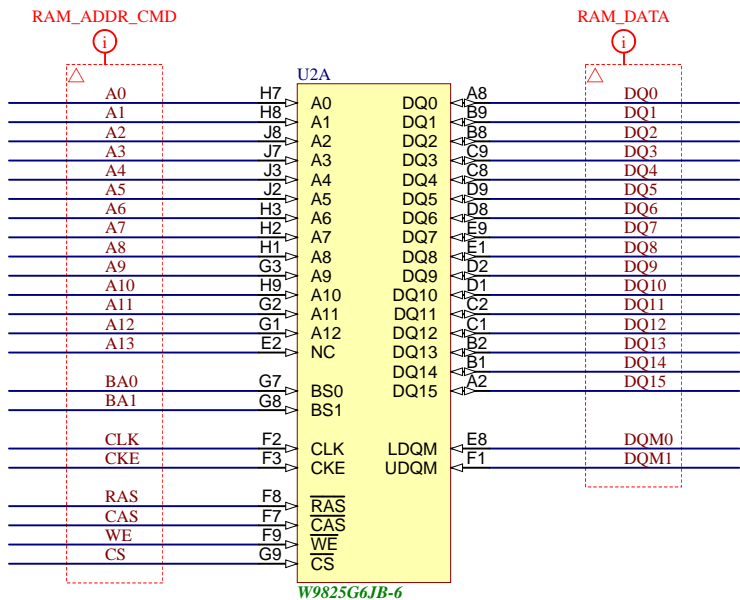




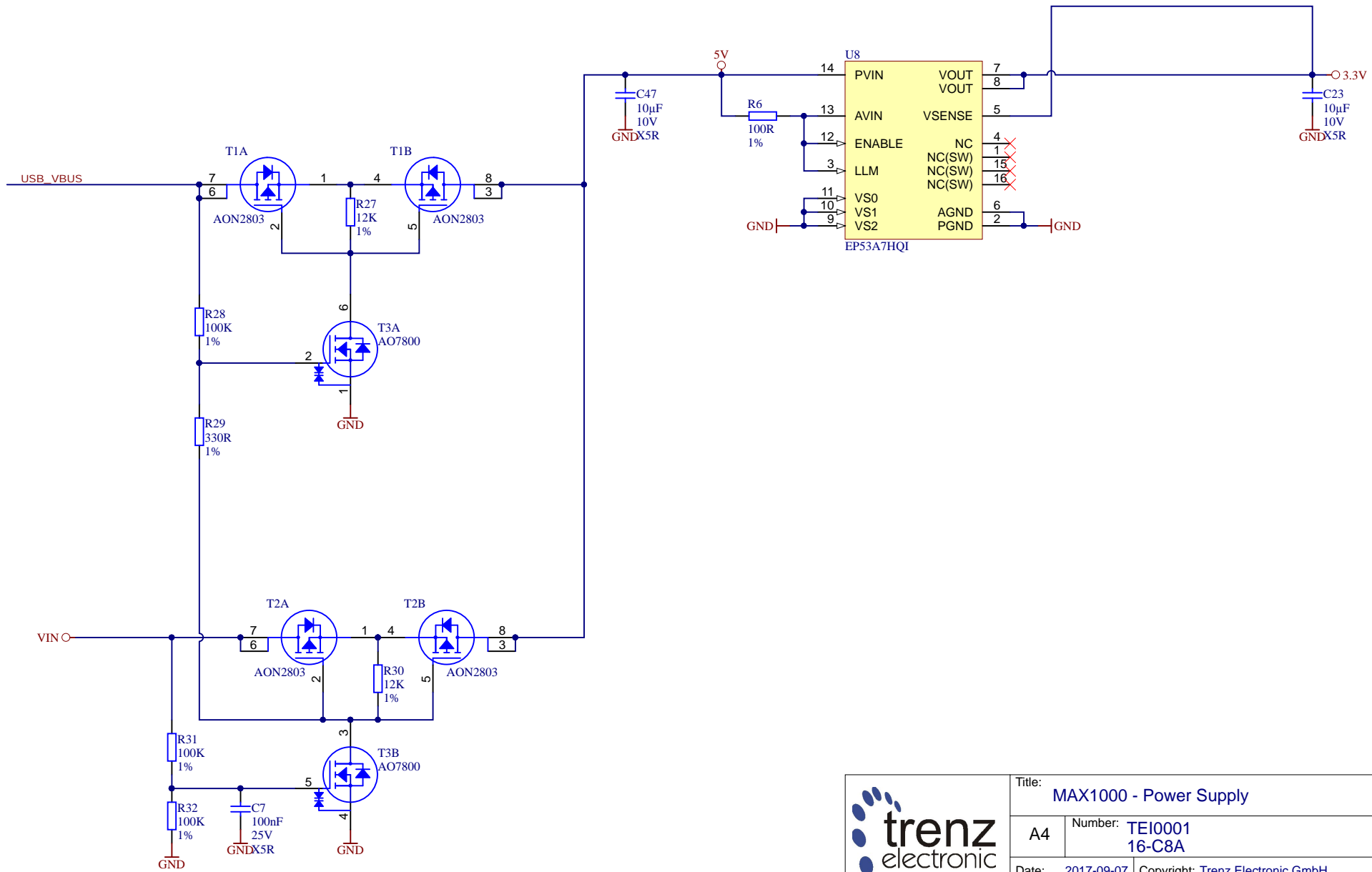
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A4	Number: TEI0001 16-C8A	Rev. 03
Date: 2017-09-07	Copyright: Trenz Electronic GmbH	Page3 of 8
Filename: FPGA.SchDoc		




Title: MAX1000 - FPGA_MISC			
A4	Number: TEI0001 16-C8A	Rev. 03	
Date: 2017-09-07	Copyright: Trenz Electronic GmbH		Page4 of 8
Filename: FPGA_MISC.SchDoc			



Title: MAX1000 - Sensor_Flash_SDRAM		
A4	Number: TEI0001 16-C8A	Rev. 03
Date: 2017-09-07	Copyright: Trenz Electronic GmbH	
Filename: SENSOR_Flash_RAM.SchDoc	Page 6 of 8	



	Title: MAX1000 - Power Supply		
	A4	Number: TEI0001 16-C8A	Rev. 03
	Date: 2017-09-07	Copyright: Trenz Electronic GmbH	Page7 of 8
	Filename: PowerSupply.SchDoc		

CHANGES REV01 to REV02

- 1) Added resistors R35, R37
- 2) Changed SPI-flash on W74M64FVSSIQ (populated in REV02)
- 3) Moved nets D0,D1,D4,D5 from bank1B to banks 5 and 2
- 4) Optimized connections into banks 1A, 1B, 2, 5, 6
- 5) Changed pin-out on a connector J4
- 6) Added a pin-header J3
- 7) Changed a LEDs

CHANGES REV02a (29.08.2017)

- 1) Updated schematic symbol of U1

CHANGES REV03

- 1) Buttons S1 and S2 replaced from KMS221GLFS to KMS221GPLFS



Title:	MAX1000
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A4

Number: TEI0001
16-C8A

Rev.	03
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Date: 2017-09-07	Copyright: Trenz Electronic GmbH
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