Maciej Paweł **Kopeć**, MSc

● Electronics Engineer ● Embedded software and FPGA enthusiast ● DIY advocate ●

⌂ Na Popielówkę 75P/2, 32-087 Zielonki, Poland | ✉ maciej.p.kopec@gmail.com
☐ +48 515 083 938 | ⌱ mpkopec | ⊞ Maciej Kopeć | ⓘD Maciej P. Kopeć

Education

Mar 2014 – Sep 2015	**Technical Physics** | Faculty of Physics and Applied Computer Science | AGH-UST **Master of Science** | Thesis: *Control interface for SALT ASIC for LHCb detector tracker upgrade*
Oct 2010 – Feb 2014	**Technical Physics** | Faculty of Physics and Applied Computer Science | AGH-UST **Bachelor of Science** | Thesis: *Demo application for colour touchscreen working in embedded system*

Work experience

For more detailed information about my experience (detailed job description, etc.), please visit my LinkedIn profile, to which a link can be found in my contact data.

Jan 2020 – present	**Research Assistant** | AGH University of Science and Technology
Mar 2020 – Jul 2020	**Advanced Electronics Engineer** | Merit Poland sp. z o.o.
Apr 2019 – Mar 2020	**Electronics Engineer** | Merit Poland sp. z o.o.
Jan 2018 – Mar 2019	**Electronics Engineer** | Merit Poland sp. z o.o. as Sii Poland's contractor
Dec 2014 – Dec 2017	**Beam diagnostics and Instrumentation Specialist** | National Synchrotron Radiation Centre 'Solaris'
May 2016 – Aug 2016	**Embedded systems developer** | Conseil Européen pour la Recherche Nucléaire (CERN)
Jan 2014 – Jun 2014	**Hardware development intern** | Woodward Poland sp. z o.o.
Jul 2013 – Aug 2013	**Electrical engineering trainee** | Faculty of Physics and Applied Computer Science
2008 – 2013	**Web-developer, freelancer** | Back-end and front-end web development

Skills

Programming languages	C, Python, Verilog, SQL (basic), MATLAB (basic), Bash
Design software	KiCad, Altium Designer, Mentor Xpedition, LTspice IV, Simulink (basic), Xilinx Vivado, Xilinx Vitis (basic), Eclipse
Other technologies	Linux, LaTeX, git, Wordpress, MS Office, Vim
Additional qualifications	SEP 1 kV license (30 kV for laboratory testing), driving license, good knowledge of circuits and signals theory, analytical thinking

Courses and certificates

20 Feb 2020	**CCES** | High speed design: the 25+ Gbps challenge
6, 8, 13 Nov 2018	**EMC for automotive** | EMC Solution
Oct 2017 (online)	**Advanced Hardware Design** | FEDEVEL Academy
12–16 Jun 2017	**Accelerator Power Electronics Engineering** | US Particle Accelerator School
	**	** The course was concluded with an exam graded *excellent*.
19–23 Jun 2017	**Pulsed Power Engineering** | US Particle Accelerator School
	**	** The course was concluded with an exam graded *outstanding*.
Dec 2016 (online)	**Learn Altium Essentials** | FEDEVEL Academy
28–29 Apr 2016	**PCB design at RF — multi-Gigabit transmission, EMI control and PCB materials** | Cadence
25–27 Apr 2016	**Essential high-speed PCB design for signal integrity** | Cadence

Languages

English	Fluent, *FCE passed in 2007*
Russian	Basic
Japanese	Pre-intermediate, *currently learning*

Disclaimer