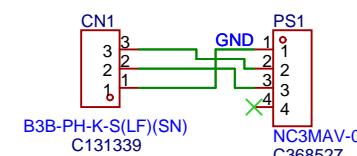


A

A



Intentionally not pin-to-pin for Main Board V2.4

B

B

C

C

D

D

Schematic	Schematic1			Update Date	2024-11-15	
Page	P1			Create Date	2024-11-15	
Drawn	EasyEDA	XLR BAL OUT			Part Number	JLCPCB-002
Reviewed	EasyEDA					
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