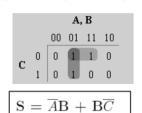
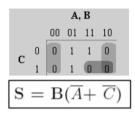
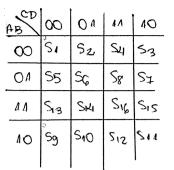
Mapa de Karnaugh

• Agrupamento por múltiplos de potência 2.







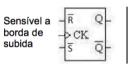
Forma disjuntiva

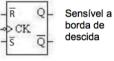
Forma conjuntiva

Flip-Flop

• RS

CK	$\overline{R} \ \overline{S}$	$\mathbf{Q} \; \overline{Q}$
0	ХХ	Qa \overline{Qa}
1	X X	Qa \overline{Qa}
F	0 0	Qa \overline{Qa}
不	0 1	0 1
1	1 0	1 0
1	11	1 1

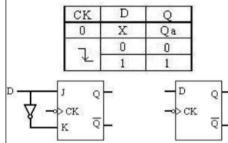




• JK

$\mathbf{C}\mathbf{K}$	J K	$\mathbf{Q} \ \overline{Q}$
0	ХХ	$\mathbf{Qa}\ \overline{Qa}$
1	XX	$\mathbf{Qa}\ \overline{Qa}$
T	0 0	Qa \overline{Qa}
L	0 1	0 1
T	1 0	1 0
T	1 1	\overline{Qa} Qa





• T

T	Q	1	
X	Qa	1	
0	Qa		
1	Qa		
1 Q		Т	Q
CK _		→> CK	
	T X 0 1	T Q X Qa 0 Qa 1 Qa	T Q X Qa 0 Qa 1 Qa

• JK Master Slave

PR	CL	CK	J	K	Q
1	0	X	X	X	0
0	1	X	X	X	1
1	1	1 1	0	0	Qa
			0	1	0
			1	0	1
			1	1	Qa

Contadores

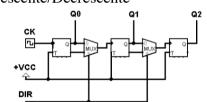
- 1. Diagrama de Estados
- 2. Tabela de Transição de estados
- 3. Karnaugh
- 4. Circuito Lógico

• Síncronos

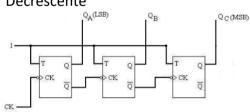
Transição	Possibilidades	Conclusão
	J К	J К
$0 \rightarrow 0$	0 0	0 X
, ,	0 1	0 X
$0 \rightarrow 1$	1 1	1 X
0 , 1	1 0	1 X
1 → 0	1 1	X 1
_ , .	0 1	X 1
1 → 1	0 0	X 0
	1 0	X 0

Assíncronos

Crescente/Decrescente



Decrescente

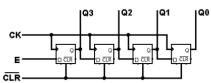


Registradores

Q3 Q2 Q1 Q0 CLK CLR D3 D2 D1 D0

Shift-Registers

Pulsos De Clock	\overline{CLR} E	$\mathrm{Q3}\ \mathrm{Q2}\ \mathrm{Q1}\ \mathrm{Q0}$
0	0 X	0000
1	11	4,600
2	1 1	1/1/0 0
3	1 0	0 1/1/0
4	1 1	1 0 1 1
5	1 0	0101
6	1 0	0 0 1 0
7	1 0	0 0 0 1
8	1 0	0 0 0 0



CLR CKA CKB D3~D0 (DBUS) Q3~Q0 (RA) Q3~Q0 (RB) S3~S0 Co