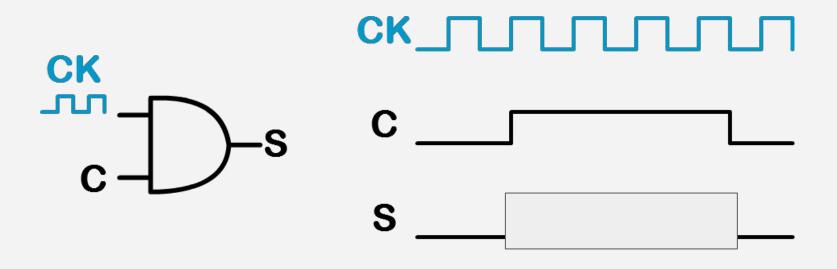
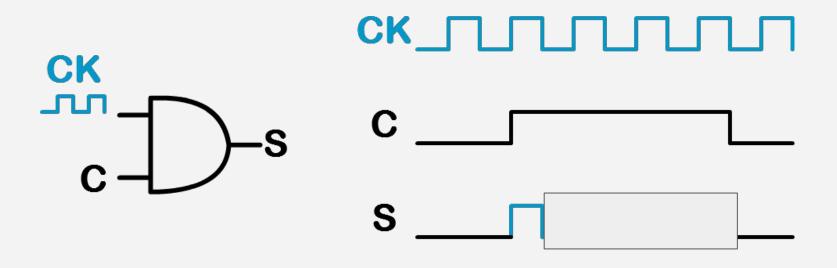


Consequências interessantes com o uso de Portas Lógicas

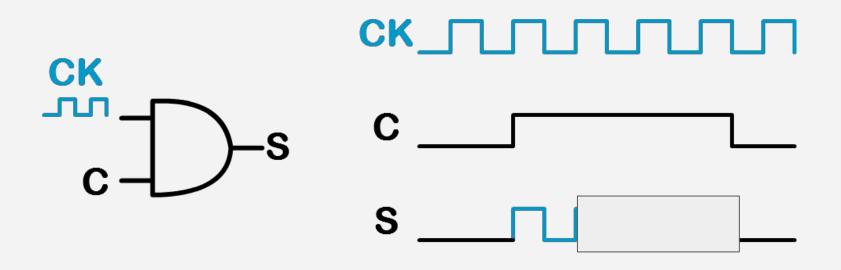




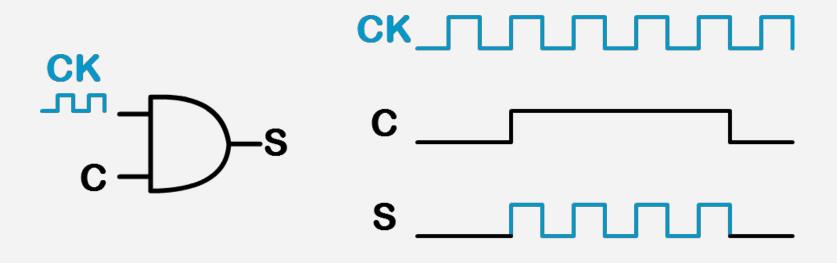




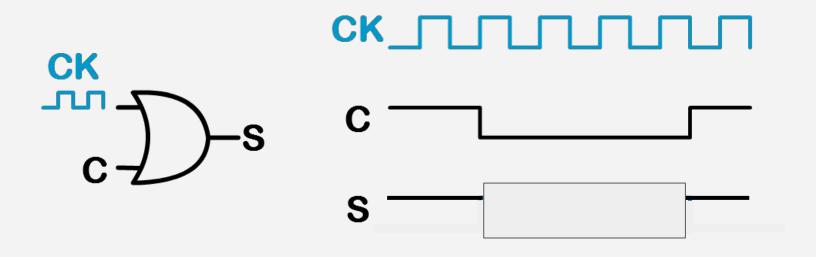




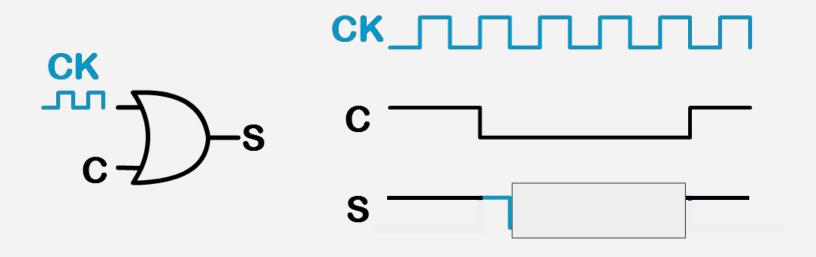




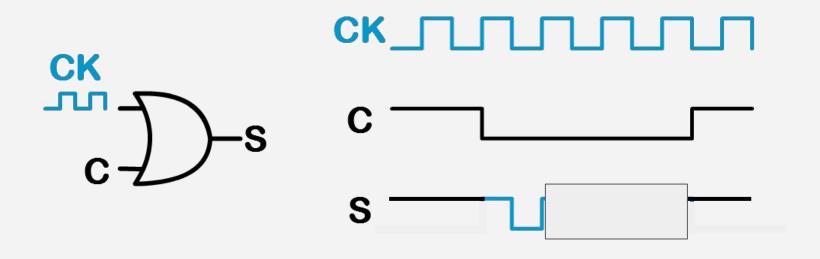




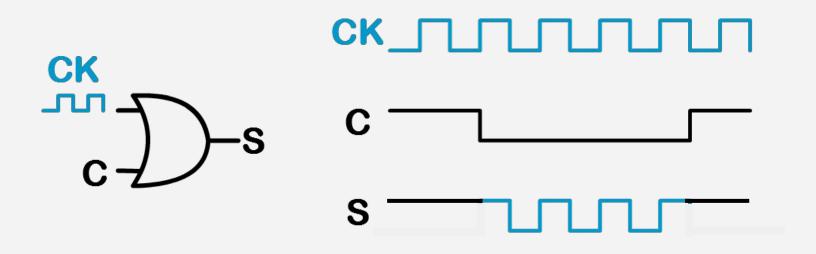






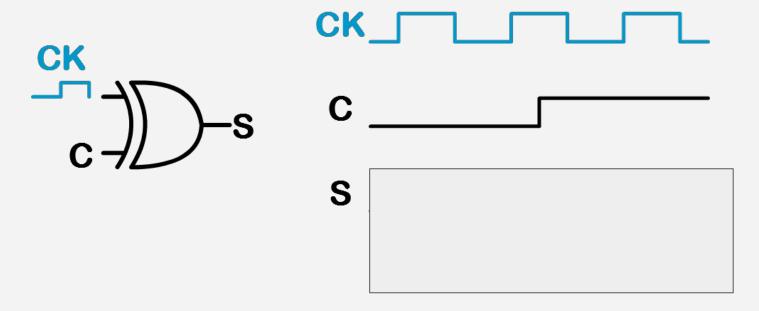








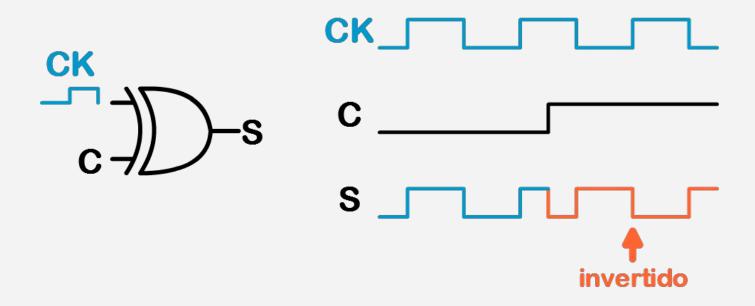
Inversão controlada de sinal



→ Inverte o sinal quando C está em nível alto



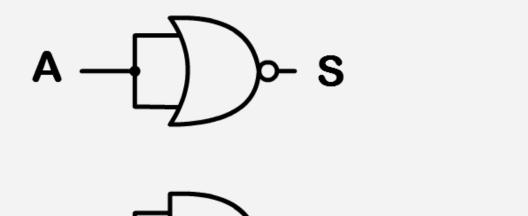
Inversão controlada de sinal



→ Inverte o sinal quando C está em nível alto



Portas inversoras alternativas



$$S = \overline{A}$$

 $S = \overline{A}$