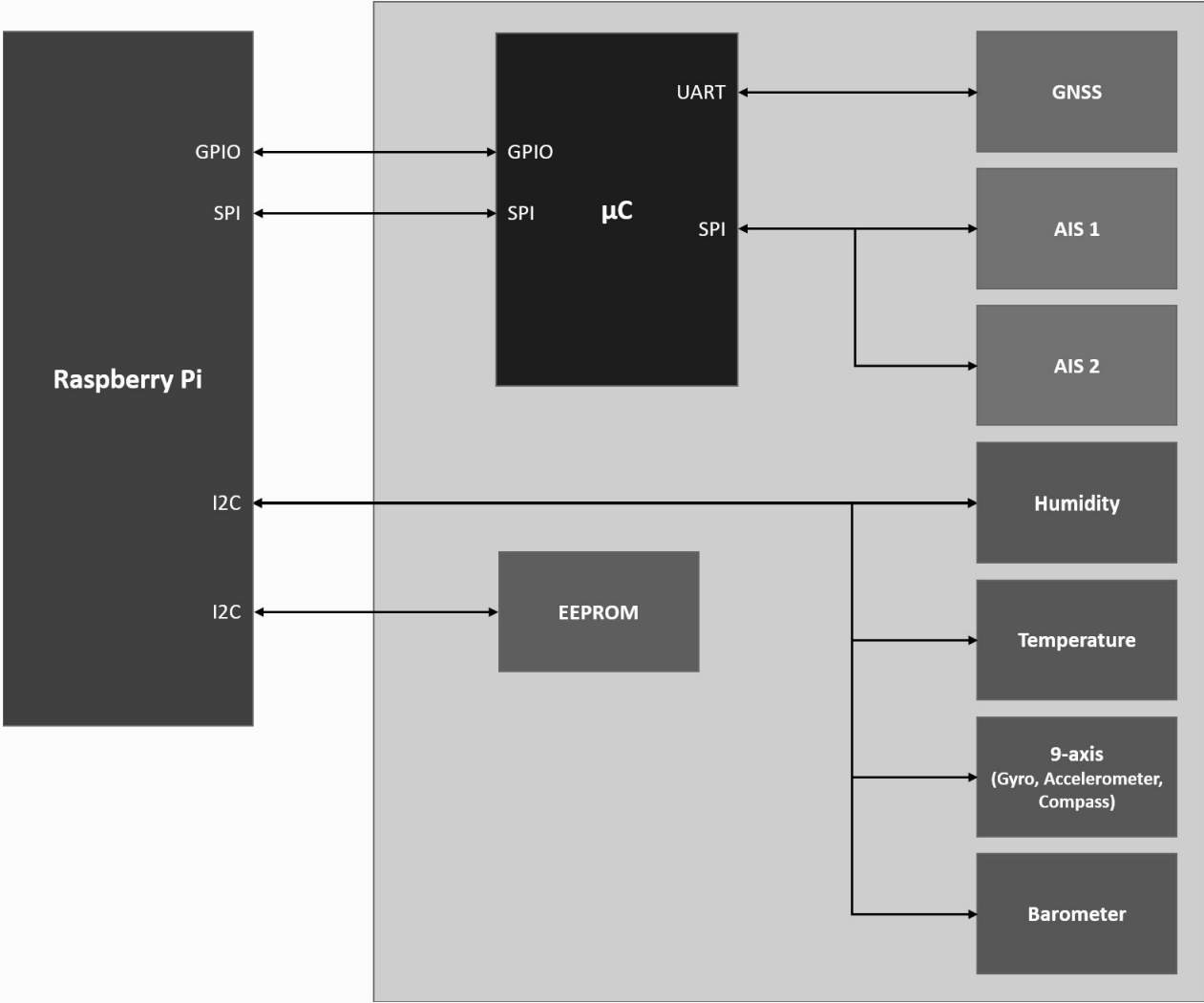


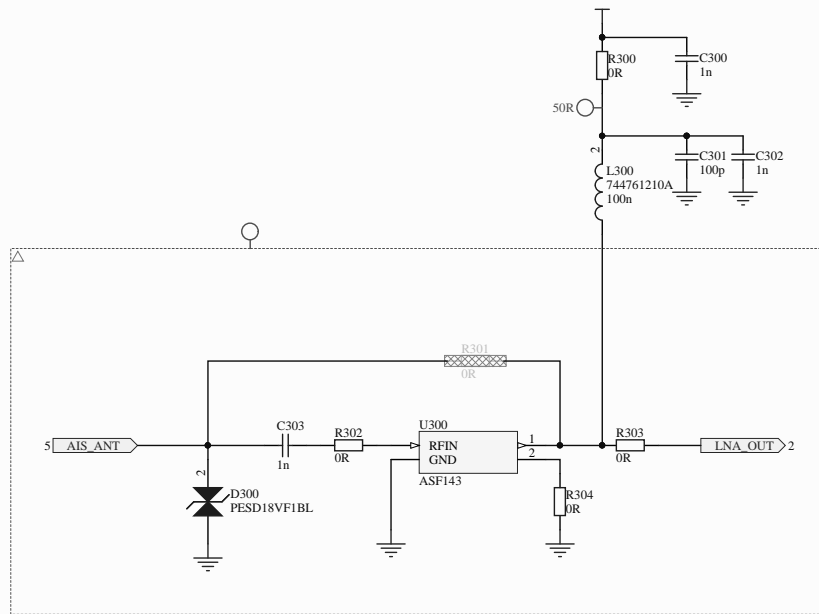
Project Name: Rooco Moitesssier HAT  
Drawing Number: P770010E05-Xx-SH-x  
Revision: E05  
Drawn by: Thomas POMS  
Variant: X1



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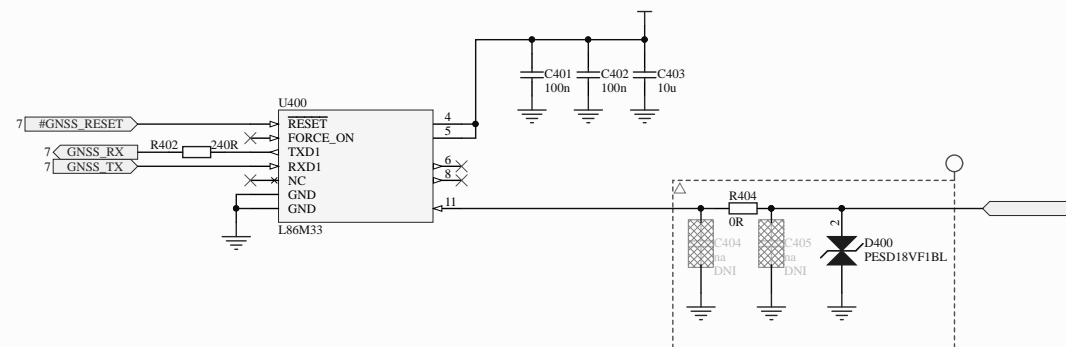


R302 and R303 might be used for impedance matching purpose.

If no LNA is required R301 is installed and C303, R302, U300, R303, R304, L300, R300, C300, C301 and C302 are not assembled.

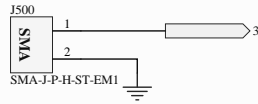
Alternate parts:  
BGA427 --> R304 = 2p2, reverse pinning compared to ASF143  
BGA612  
BGA616  
ASF143 --> R304 = 0R, reverse pinning compared to BGA427

#GNSS\_RESET must not actively driven high. The microcontroller should use open collector output.

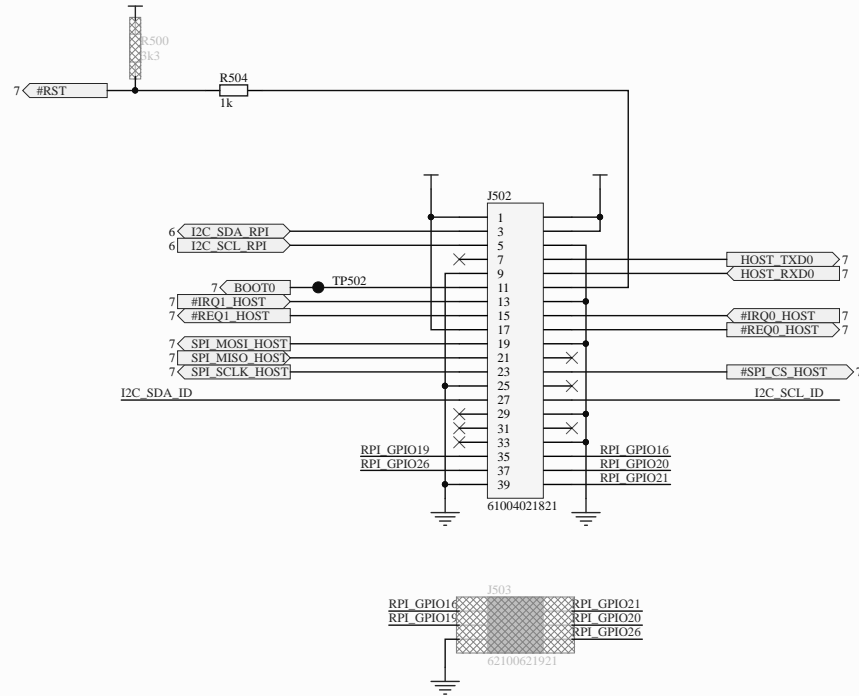
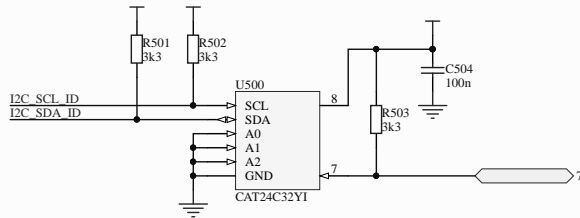
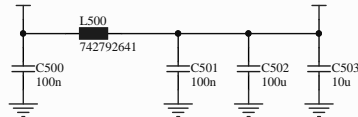
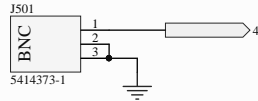


If C404 and C405 are required, appropriate values must be evaluated/calculated.

Alternate parts:  
RF Solutions CON-SMA-EDGE-S  
Cinch 142-0711-821  
RS Pro 526-5785



Alternate parts:  
0731000105

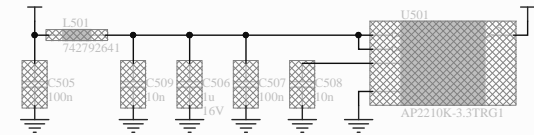


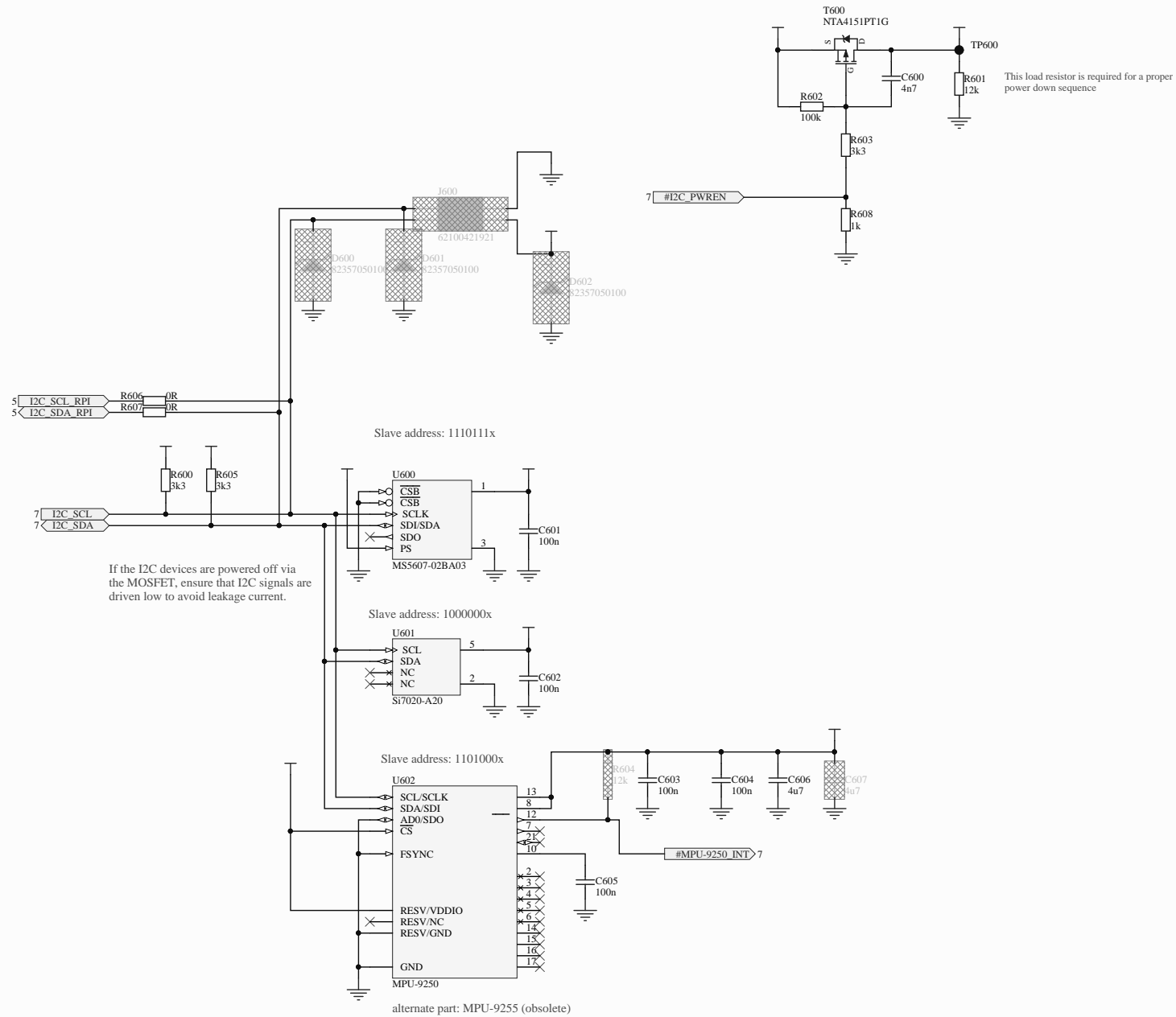
Raspberry Pi 3 GPIO Header			
Pin#	NAME	NAME	Pin#
01	3.3v DC Power	DC Power 5v	02
03	GPIO02 (SDA1 , I2C)	DC Power 5v	04
05	GPIO03 (SCL1 , I2C)	Ground	06
07	GPIO04 (GPIO_GCLK)	(TXD0) GPIO14	08
09	Ground	(RXD0) GPIO15	10
11	GPIO17 (GPIO_GEN0)	(GPIO_GEN1) GPIO18	12
13	GPIO27 (GPIO_GEN2)	Ground	14
15	GPIO22 (GPIO_GEN3)	(GPIO_GEN4) GPIO23	16
17	3.3v DC Power	(GPIO_GEN5) GPIO24	18
19	GPIO10 (SPI_MOSI)	Ground	20
21	GPIO09 (SPI_MISO)	(GPIO_GEN6) GPIO25	22
23	GPIO11 (SPI_CLK)	(SPI_CE0_N) GPIO08	24
25	Ground	(SPI_CE1_N) GPIO07	26
27	ID_SD (I2C ID EEPROM)	(I2C ID EEPROM) ID_SC	28
29	GPIO05	Ground	30
31	GPIO06	GPIO12	32
33	GPIO13	Ground	34
35	GPIO19	GPIO16	36
37	GPIO26	GPIO20	38
39	Ground	GPIO21	40

Rev. 2  
29/02/2016

www.element14.com/RaspberryPi

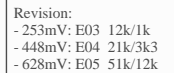
REF500  
PCB\_Reference\_Point  
REF501  
PCB\_Reference\_Point  
REF502  
PCB\_Reference\_Point







boot configuration bits nBOOT1 and BOOT\_SEL must be set to 1 in the user option byte



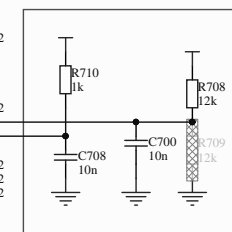
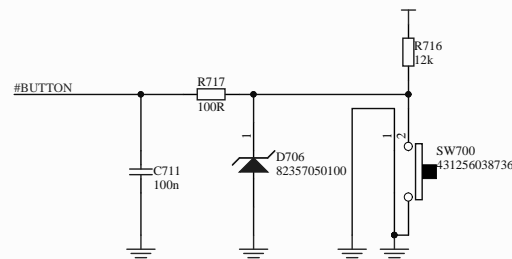
Enable pull-up on PA15 and check if logic '0' --> revision can be read with analog input. If logic '1' --> board is either E01 or E02.

EXTI0\_1\_IRQn  
EXTI2\_3\_IRQn  
EXTI4\_15\_IRQn

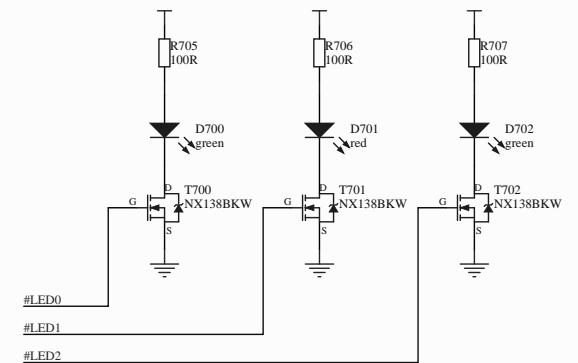
```
EXTI0_1_IRQn      --> #REQ0_HOST
EXTI0_IRQn         --> #REQ1_HOST
EXTI2_3_IRQn       -->
EXTI2_IRQn         --> DEBUG_GPIO1
EXTI3_IRQn         --> #IRQ_AIS1
EXTI4_15_IRQn      -->
EXTI4_IRQn         -->
EXTI5_IRQn         -->
EXTI6_IRQn         -->
EXTI7_IRQn         -->
EXTI8_IRQn         --> #IRQ_AIS0
EXTI9_IRQn         -->
EXTI10_IRQn        -->
EXTI11_IRQn        --> #IRQ_AIS1
EXTI12_IRQn        --> #MPU-9250_INT
EXTI13_IRQn        -->
EXTI14_IRQn        -->
EXTI15_IRQn        -->
```


Alternate parts:

STM32F030CC, Cortex-M0, 48 MHz, 256 kB FLASH, 32 kB RAM  
STM32F091CB, Cortex-M0, 48 MHz, 128 kB FLASH, 32 kB RAM  
STM32F091CC, Cortex-M0, 48 MHz, 256 kB FLASH, 32 kB RAM  
STM32F071CB, Cortex-M0, 48 MHz, 128 kB FLASH, 32 kB RAM



if both inputs (PB10 and PB11) are high, the HAT is attached to the Raspberry Pi, otherwise it is running in standalone mode



<b>Microcontroller</b>				csoft - Web and IT Solutions Wiener Straße 2 A-8665 Langenwang www.csoft-it.at			
Drawing Number: P770010E05-Xx-SH-x				Rooco Moitessier H			
Size: A3		Number: 7		Revision: E05			
Drawn by: Thomas POMs		Approved by:					
Date: 19.07.2021		Time: 08:23:25		Sheet 7 of 7			Variant X1
File: P770010E05-Xx-SH-7_Microcontroller.SchDoc							
