

CSE 237D: SoC Security

Milestone 2 Report

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Deliverable for Milestone 2:

Milestone 2

May 2 - May 13

Developing & Integrating AXI Master

Deliverable:

1. Modified the md5_top (hardware accelerator top) file to instantiate an AXI master module. Also developed internal registers into the hardware accelerator and implemented a register access interface for AXI master access.

https://github.com/mr103/SoC-Security/blob/master/md5_top_axi4lite.v

2. Developed a testbench to act like AXI slave for proper handshaking with the AXI master within the hardware accelerator core.

https://github.com/mr103/SoC-Security/blob/master/tb_axi_master_md5.sv

3. Simulation Snapshot

