Chip Scale Review

2016 Editorial Calendar

(Editorial close date: 11/20)	January • February	(* denotes show distribution)	
Packaging trends		SEMI European 3D Summit Grenoble, France (Jan 18-20) * SMTA Pan Pac Microelectronics Symposium * Kohala Coast, Hawaii (Jan 25-28) SEMICON Korea Seoul, Korea (January 27-29) BiTS Workshop * Mesa, AZ (March 6-9) APEX Expo Las Vegas, NV (March 15-17) IMAPS Device Packaging (DPC) * Fountain Hills, AZ (March 15-17) SEMICON China* Productronica China	
Assembly materials			
Cu TSV stress analysis			
Wafer carrier solutions			
Recent advances in 3D package reliability			
Metrology for 3D integration			
Silicon photonics packaging			
Advances in wafer probing		Shanghai China (March 15-17)	
Socket contact technologies			

International Directory of Test and Burn-in Socket Suppliers

Ad Space Close Jan 22 - Ad Materials Deadline Jan 29

(Editorial close date: 12/19)	March • April		
MEMS in IoT devices		SEMICON South East Asia Penang, Malaysia (Apr 26-28) MEPTEC MEMS Technology Symposium * San Jose, CA (TBD) loT Symposium * San Jose, CA (TBD) ECTC * Las Vegas, NV (May 31- June 3)	
Cu pillar technology			
FOWLP			
Die-to-wafer stacking			
Flexible substrates			
Packaging of high-power devices			
Glass interposers			
Lithography for TSVs			
TSV technologies			

Ad Space Close Feb 19 - Materials Close Feb 26

(Editorial close date: 3/11)	May • June		
Industry market update		IMAPS Advanced Technology Workshop * Dearborn, MI (TBD) IEEE/SW Test Workshop (SWTW) San Diego, CA (June 5-8) SEMI Europe Packaging Tech Seminar * Porto, Portugal (TBD) SEMICON West *	
Package optimization & failure analysis with 3D X-ray			
Failure analysis methodologies for advanced packaging			
Multi-chip packaging technology advances			
Ultra-thin embedded packaging		San Francisco, CA (July 12-14)	
Plasma dicing singulation			
Packaging materials update			
3D integration for tomorrow's devices			
Reducing the cost of test			

Ad Space Close May 20 - Ad Materials Close May 27