

## 2016 Editorial Calendar

## July • August

Materials development of SiP & IoT	• ICEPT 2016 • Wuhan, China (Aug 16-19)
Silicon 3D Integration for small power devices	• SEMICON Taiwan Taipei, Taiwan (Sept 7-9)
Solder reliability	BiTS China 2016     Suzhou, China (Sept 13)
Environmental impact of packaging processes	• ESTC Grenoble, France (Sept 13-16)
MEMS challenges in the IoT era	MEPTEC     Medical Electronics Symposium
Materials for test sockets	Portland, OR (Sept 14-16)  • SEMI European MEMS Summit
Direct bonding technology	Stuttgart, Germany (Sept 15-16)
Warpage in FCBGAs	

International Directory of Bonding Equipment for 2.5D and 3D Assembly

Ad Space Close Jun 24 - Ad Materials Close Jul 1

September • October	
Panel Level Packaging for FO-WLP	• IMAPS 2016 Pasadena, CA (Oct 11-13)
Probe card inspection	ICPT 2016 Beijing, China (Oct 17-19) IWLPC-International Wafer-Level Packaging Conference & Exhibition San Jose, CA (Oct 18-20) SEMICON Europa Grenoble, France (Oct 25-27)
Trends in TSV technologies	
Market update on multi-die ICs	
Bonding / debonding for heterogeneous integration	
Hermetic CSP for MEMS	
LED packaging	
Micro testing thin die	
SiP	

Ad Space Close Sep 9 - Ad Materials Close Sep 16

November • December		
FOWLP in the foundry	MEMS Executive Congress     Scottsdale, AZ (Nov 10-11)	
3D TSV assembly	MEPTEC     Semiconductor Packaging Roadmap     Symposium     San Jose, CA (Nov 14)     International Test Conference (ITC)     Fort Worth, TX (Nov 15-17)     SEMICON Japan     Tokyo, Japan (Dec 14-16)     3D ASIP Conference     Burlingame, CA (Dec 13-15)	
Die stacking		
High-frequency test sockets		
Wafer-level test		
High performance photoresist for advanced packaging		
Dicing of silicon carbide power devices		
Glass for semiconductor/optoelectronics packaging		
Interconnects for MEMS & sensors		

Ad Space Close Nov 4 - Materials Close Nov 11