The results are the following:

- Switches=0 → Zero = 0x3fb54000 (algorithm based on the FPU)
- Switches=1 -> Cycles = 1672 (algorithm based on the FPU)
- Switches=2 → Instructions = 264 (algorithm based on the FPU)
- Switches=3 \rightarrow Zero = 0x3fb54000 (SW algorithm)
- Switches=4 → Cycles = 5780 (SW algorithm)
- Switches=5 → Instructions = 3718 (SW algorithm)

As expected, the SW algorithm needs much more instructions than the algorithm based on the FPU. Consequently, it also needs much more cycles.