* ***mpc\_ eqcond\_e / eqcond\_e***

SOLUTION:

* + Let´s analyze these signals for the specific case of a *beq* instruction (***br\_eq*** = 1 and ***cnd\_eq\_en***=1).





* + - If the equality compare result, computed at *m14k\_edp* as explained above, is 1 (***edp\_cndeq\_e***=1), then ***eqcond\_e*** and ***mpc\_eqcond\_e*** are set to 1, given that ***cnd\_eq\_en***=1 for a *beq* instruction (omitting other signals also involved such as ***mpr\_run\_ie***, ***mpc\_irval\_e***, etc.).
    - If the equality compare result is 0 (***edp\_cndeq\_e***=0), then ***eqcond\_e*** and ***mpc\_eqcond\_e*** are set to 0.



