Developing Models on Alveo U200

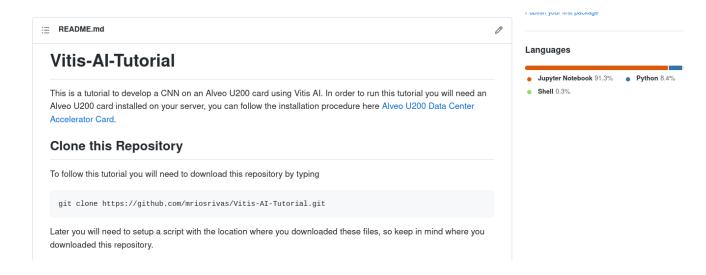
Manuel Rios

GitHub Repository

Clone GitHub Repository

You can download all the source material from here:

git clone https://github.com/mriosrivas/Vitis-AI-Tutorial.git



Alveo U200

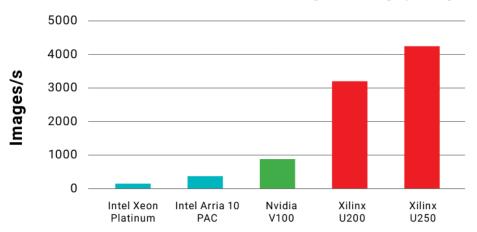
Alveo U200



- Xilinx 16nm UltraScale™ architecture
- Adaptable to any workload
 - Database Search & Analytics
 - Financial Computing
 - Machine Learning
 - Storage Compression
 - Video Processing/Transcoding
 - Genomics

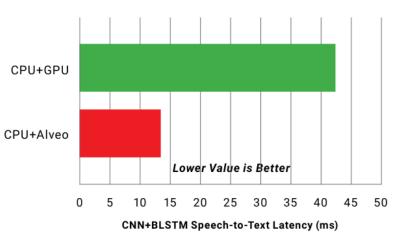
Alveo U200

Increase Real-Time Machine Learning* Throughput by 20X



*GoogleNet V1: Accelerating DNNs with Xilinx Alveo Accelerator Cards White Paper

Reduce ML Inference Latency by 3X



CPU+GPU: Nvidia P4 + Xeon CPU E5-2690 v4 @2.60GHz (56 Cores)
CPU+Alveo: Alveo U200 or U250 + Xeon CPU E5-2686 v4 @2.3GHz (8 Cores)

The Xilinx® **Deep Learning Processor Unit** (DPU) is a series of soft IP dedicated for convolutional neural networks acceleration.

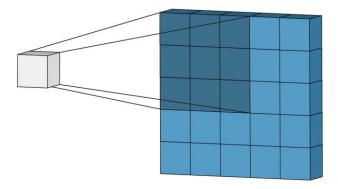
DPUCADF8H is a high throughput CNN inference IP for Alveo cards.

The IP is optimized for **high-resolution image networks** and featured with high efficiency.

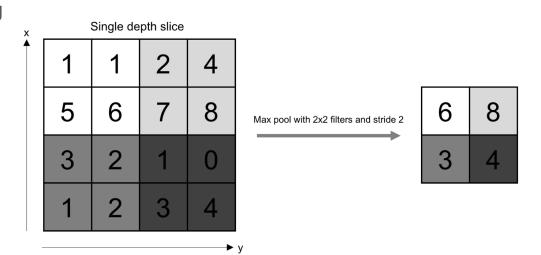
It runs with a set of efficiently optimized instructions and it can support most **convolutional neural networks**, such as VGG, ResNet, GoogLeNet, YOLO, SSD, MobileNet, and FPN.

- o Convolution, dilated convolution, and deconvolution
- Maximum and average pooling
- Element wise sum
- o ReLU
- Data split and concat
- Data reorganization
- Fully connected layer
- Batch normalization

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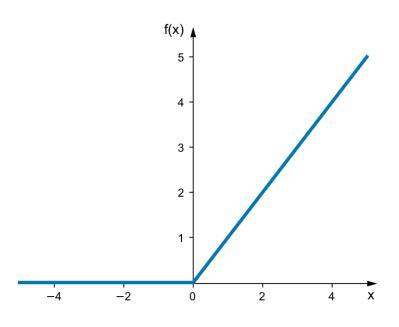


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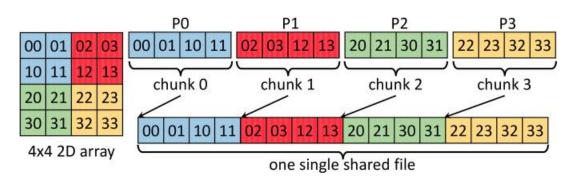
$$\begin{bmatrix} 0 & 1 \\ 2 & 3 \end{bmatrix} + \begin{bmatrix} 1 & 2 \end{bmatrix} = \begin{bmatrix} 1 & 3 \\ 3 & 5 \end{bmatrix}$$
 Add to each column

$$\begin{bmatrix} 0 & 1 \\ 2 & 3 \end{bmatrix} + \begin{bmatrix} 1 \\ 2 \end{bmatrix} = \begin{bmatrix} 1 & 2 \\ 4 & 5 \end{bmatrix}$$
 Add to each row

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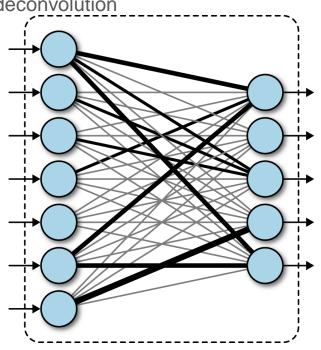
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Highlights of DPUCADF8H functionality include:

Convolution, dilated convolution, and deconvolution

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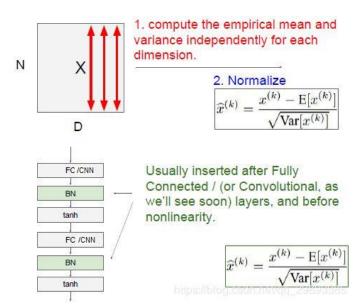
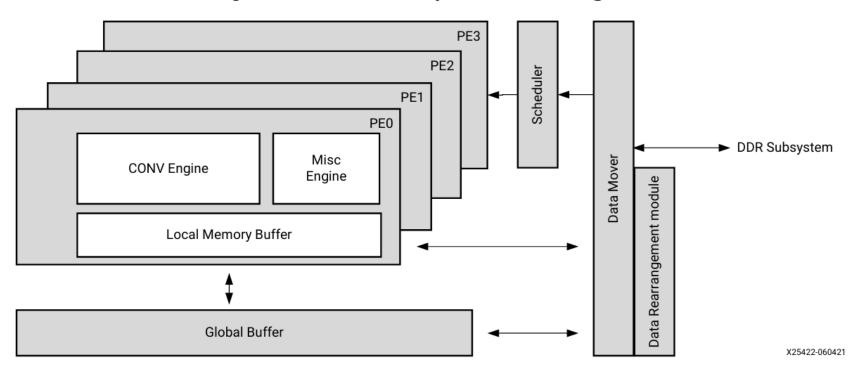


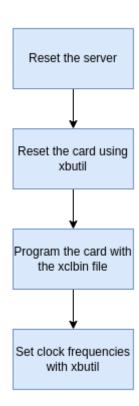
Figure 1: DPUCADF8H Top-Level Block Diagram



Server Setup

Frequency Setup of Alveo U200 Card

- If we let the Alveo card to work with the default settings, it will overheat.
- Temperature higher than 91°C will shutdown the card.
- To solve this issue we change the clock frequency.



Frequency Setup of Alveo U200 Card

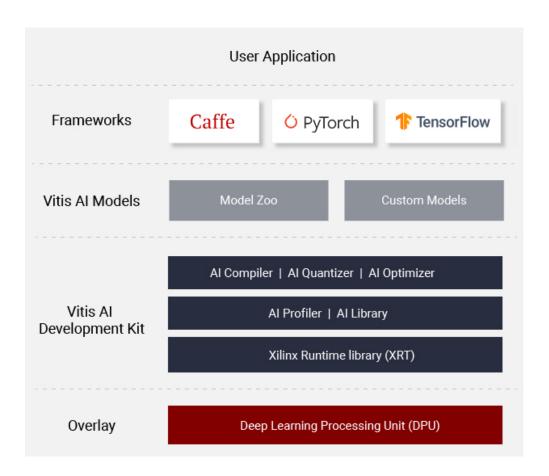
```
# Run this in the host machine (not in docker)
# Reset the card with id 0000:17:00.1
xbutil reset -d 0000:17:00.1
# Program the card with the xclbin file
xbutil program -d 0000:17:00.1 -u
/opt/xilinx/overlaybins/DPUCADF8H/dpdpuv3_wrapper.hw.xilinx_u200_gen3x16
_xdma_1_202110_1.xclbin
# Set the clock frequency
xbutil --legacy clock -d 0000:17:00.1 -f 70 -g 70
```

Vitis Al

Vitis AI

Development stack for Al inference on Xilinx hardware platforms, including both edge devices and Alveo cards.





Setup Vitis AI

1. Clone Vitis AI repository

```
git clone --recurse-submodules https://github.com/Xilinx/Vitis-AI
cd Vitis-AI
```

1. Pull Docker image

docker pull xilinx/vitis-ai-cpu:latest

Note: These two steps are already done in the server.

Setup Vitis AI

3. Edit docker_run.sh to add your project as a volume.

```
docker_run_params=$(cat <<-END
    -v /opt/xilinx/dsa:/opt/xilinx/dsa \
    -e USER=$user -e UID=$uid -e GID=$gid \
    -e VERSION=$VERSION \
    -v $DOCKER_RUN_DIR:/vitis_ai_home \
   -v /home/manuel/tutorial:/tutorial \
    ${DETACHED} \
    ${RUN MODE}
    $IMAGE NAME \
    $DEFAULT_COMMAND
END
```

Run Docker and setup environment

1. Run your Docker container

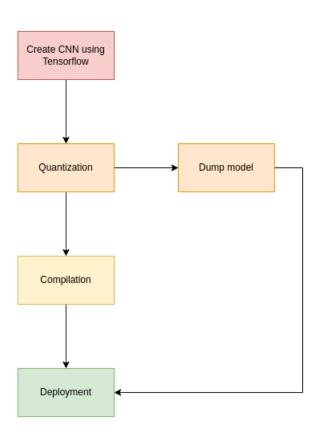
```
cd Vitis-AI
./docker_run.sh
```

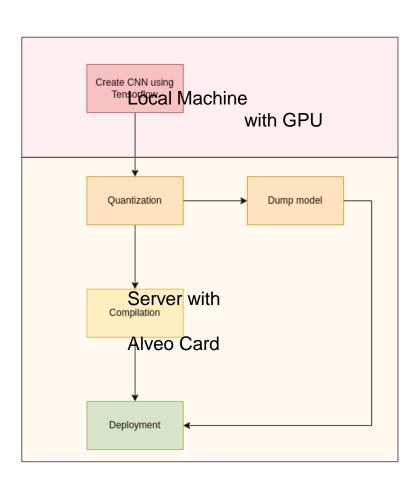
1. Activate the Tensorflow 2 environment

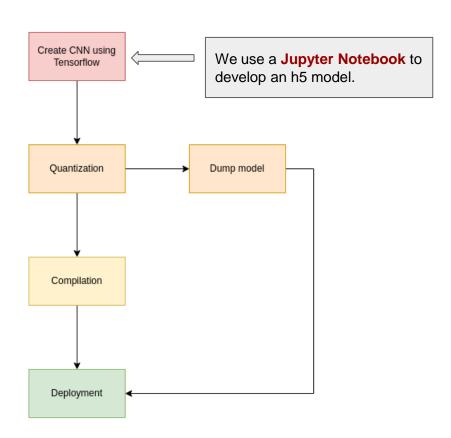
conda activate vitis-ai-tensorflow2

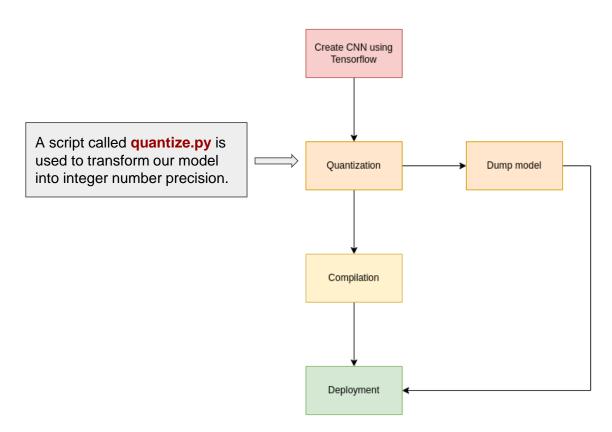
1. Source the Xilinx environment variables

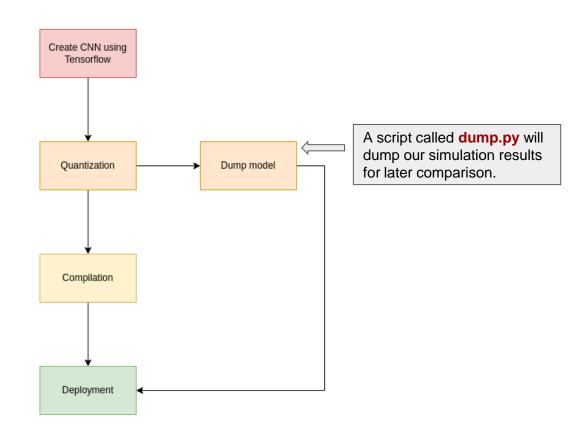
source /workspace/setup/alveo/setup.sh DPUCADF8H

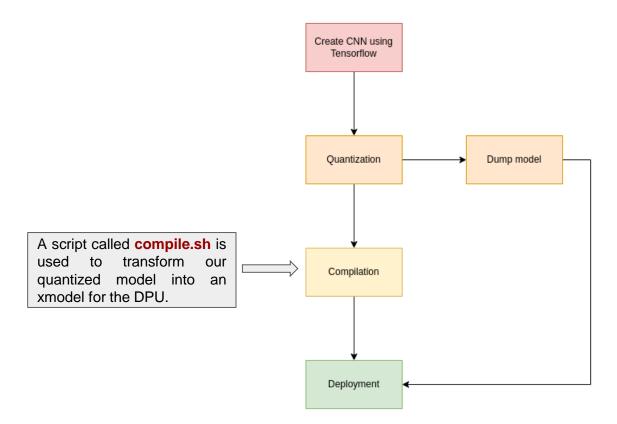


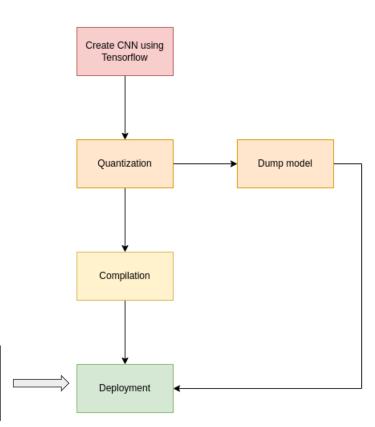












A script called **inference.py** will perform inference using the developed model.

- Create and train a cat-dog classifier.
- Use Tensorflow 2.8.0 and Keras.
- Save the model as an H5 file for later use.





Basic architecture:

- Convolutional Neural Network
- Max Pooling
- Batch Normalization
- Dense Layers

Optimizer:

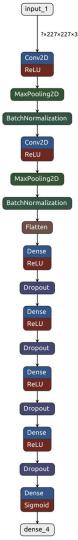
RMSprop

Loss:

Binary Cross Entropy

Metric:

Binary Accuracy



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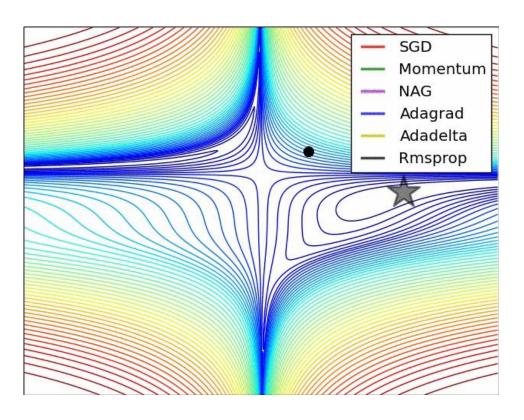
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Metric:

Binary Accuracy

$$H_p(q) = -\frac{1}{N} \sum_{i=1}^{N} y_i \cdot log(p(y_i)) + (1 - y_i) \cdot log(1 - p(y_i))$$

1. Create CNN Using Tensorflow

Basic architecture:

- Convolutional Neural Network
- Max Pooling
- Batch Normalization
- Dense Layers

Optimizer:

RMSprop

Loss:

Binary Cross Entropy

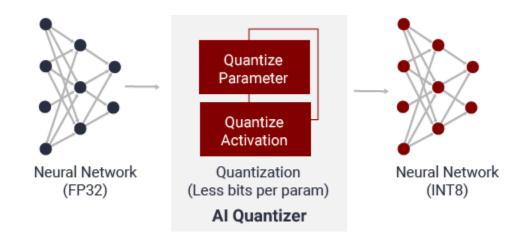
Metric:

Binary Accuracy

	Predicted O	Predicted 1
Actual O	TN	FP
Actual 1	FN	TP

2. Quantize model

Quantization is the process of converting a **32-bit floating point model** into an **INT8 representation**.



2. Quantize model

There are two different approaches to quantize a deep learning model:

- Post-training quantization (PTQ)
 - Convert a pre-trained float model into a quantized model with little degradation in model accuracy.
 - A representative dataset is needed to run a few batches of inference on the float model.
- Quantization aware training (QAT)
 - Models the quantization errors in both the forward and backward passes during model quantization.

2. Post-training quantization (PTQ)

```
## Load model trained in Keras (Tensorflow 2)
float model=tf.keras.models.load model('float model.h5')
## Ouantize the model
quantizer = vitis quantize.VitisQuantizer(float model)
quantized model = quantizer.quantize model(calib dataset=train ds,
                                            calib batch size=4,
                                            replace sigmoid=True,
                                            input shape="?,227,227,3",
                                           weight bit=8,
                                            bias bit=8)
## Save quantized model
quantized model.save('quantized model.h5')
```

2. Post-training quantization (PTQ)

3. Dump Model

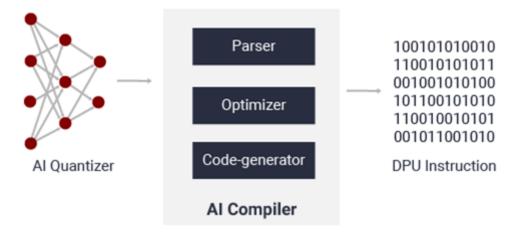
- When deploying a model, debugging becomes difficult, therefore dumping the simulation results can help.
- Dumping results can be:
 - Inputs and outputs
 - Weights and bias
- Results can be integer and floating point for reference.

3. Dump Model

4. Compile model

Vitis™ Al compiler (VAI_C):

- Interface to a compiler family targeting the optimization of neural-network computations to different DPUs.
- Maps a network model to a highly optimized DPU instruction sequence.



4. Compile model

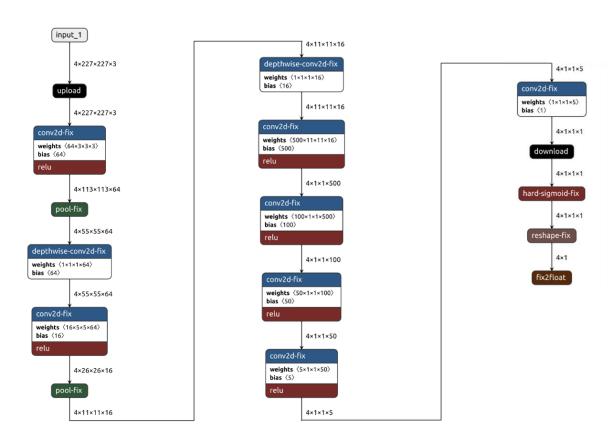
```
vai_c_tensorflow2 \
    --model quantized_model.h5\
    --arch /opt/vitis_ai/compiler/arch/DPUCADF8H/U200/arch.json \
    --output_dir compile_model \
    --net_name deploy \
    --options '{"input_shape": "4,227,227,3"}'
```

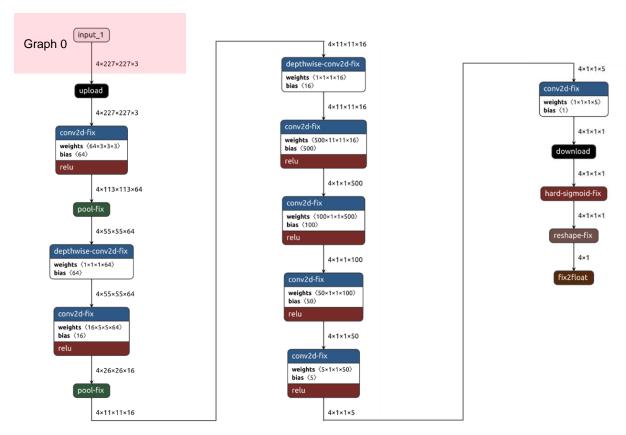
4. Compile model

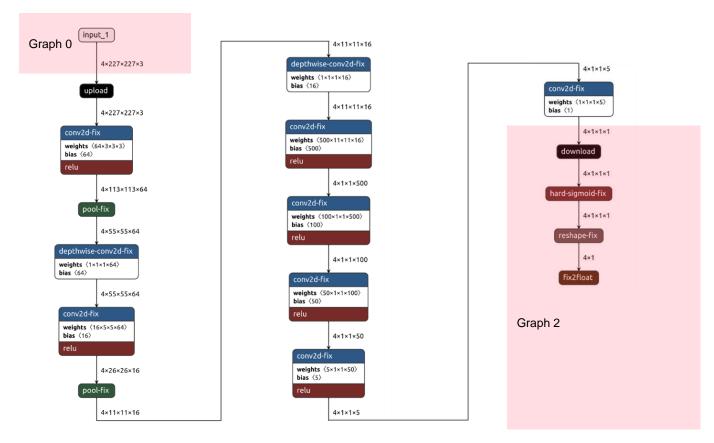
We can plot the model graph using

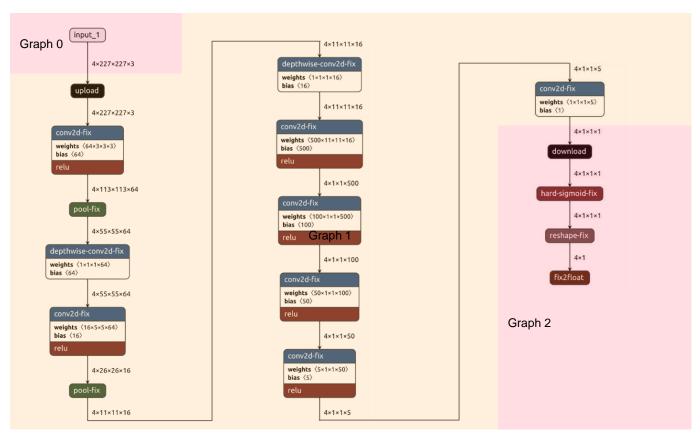
```
xdputil xmodel deploy.xmodel -s xmodel_graph.png
```

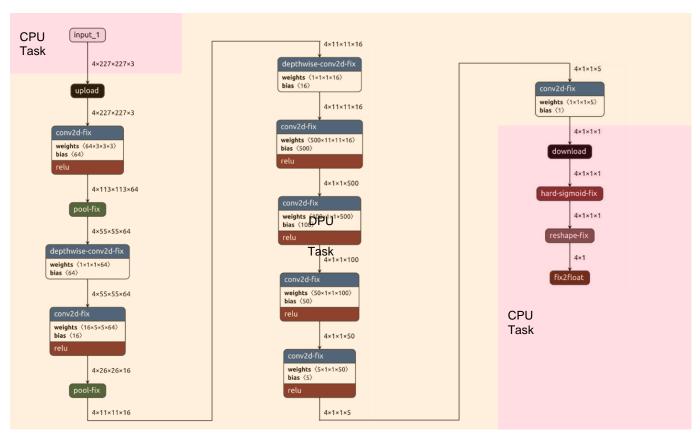
5. Deployment: Graph

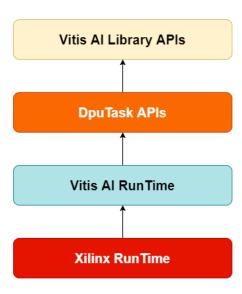


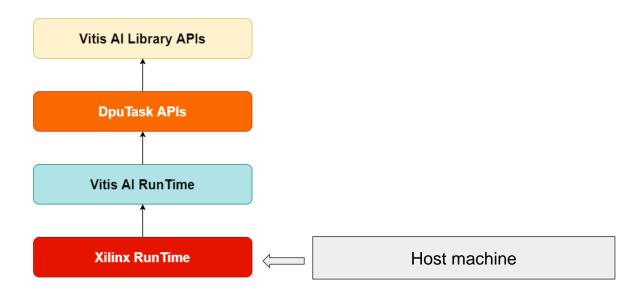


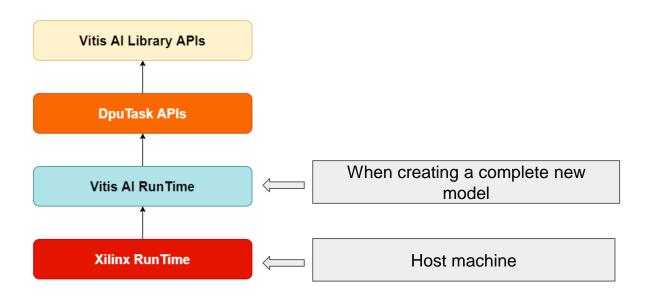


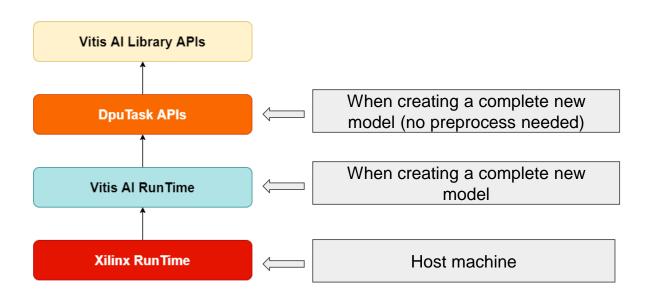


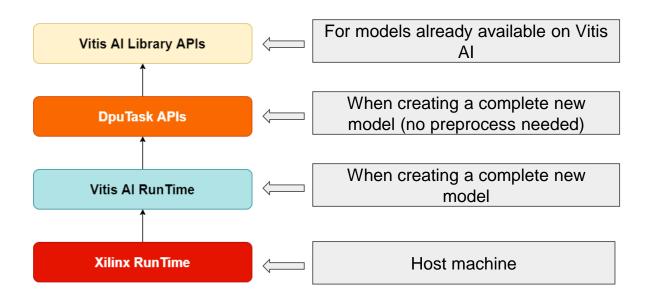


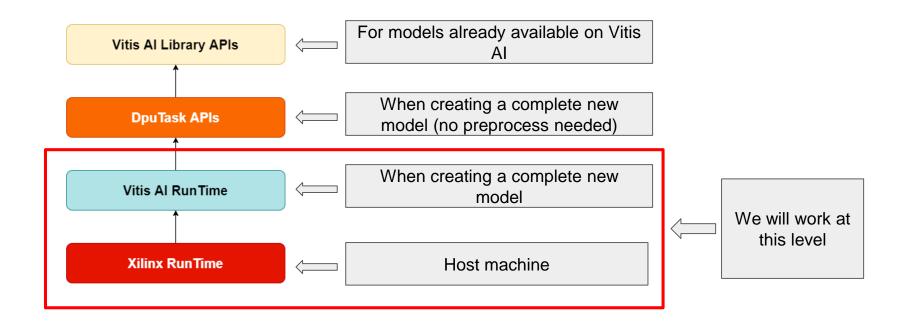


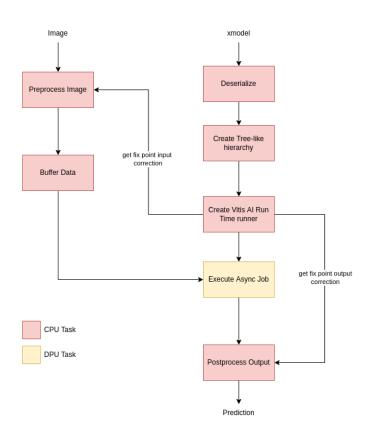


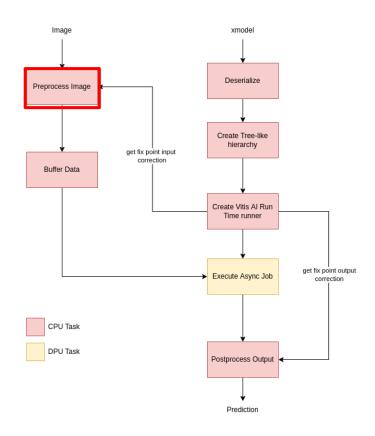




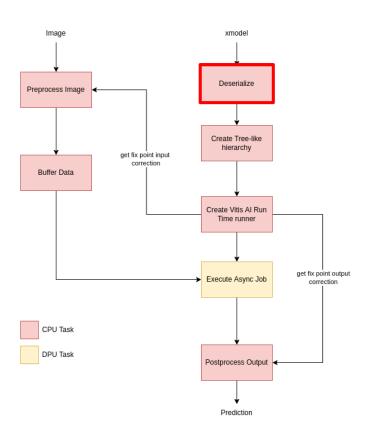




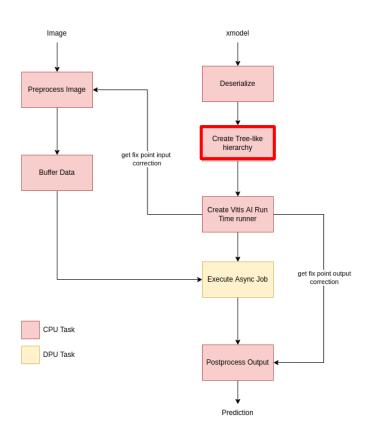




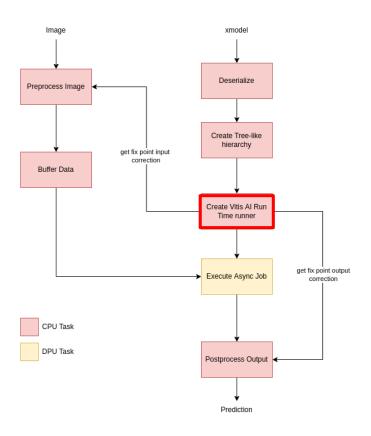
```
1 from PIL import Image
 2 import vart
 3 import xir
 4 import numpy as np
 6 def preprocess fn(image path):
       with Image.open(image path) as img:
          if img.mode != 'RGB':
               img = img.convert('RGB')
           img = img.resize((227, 227), Image.NEAREST)
      data = np.asarray(img, dtype="float32" )
      data = data/255.0
      return data
 6 x0 = preprocess fn('../dataset/dump/cat/cat.10046.jpg')
 7 x1 = preprocess fn('../dataset/dump/cat/cat.11175.jpg')
 8 x2 = preprocess fn('../dataset/dump/dog/dog.10048.jpg')
 9 x3 = preprocess fn('../dataset/dump/dog/dog.11175.jpg')
21 model = '../compilation/compiled model/deploy.xmodel'
23 g = xir.Graph.deserialize(model)
25 root subgraph = g.get root subgraph()
27 child subgraph = root subgraph.toposort child subgraph()
29 dpu = vart.Runner.create runner(child subgraph[1], 'run')
31 inputTensors = dpu.get input tensors()
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33 input ndim = tuple(inputTensors[0].dims)
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37 input fixpos = inputTensors[0].get attr("fix point")
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56 result = outputData[0]*(2**(output fixpos)-1)
57 print(result.astype('uint8'))
```



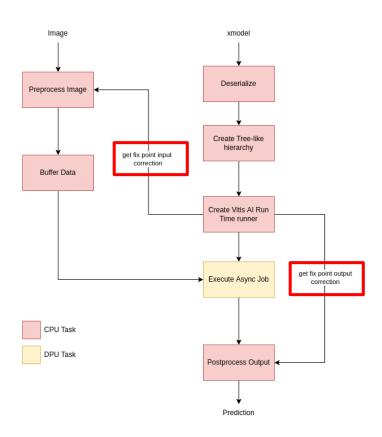
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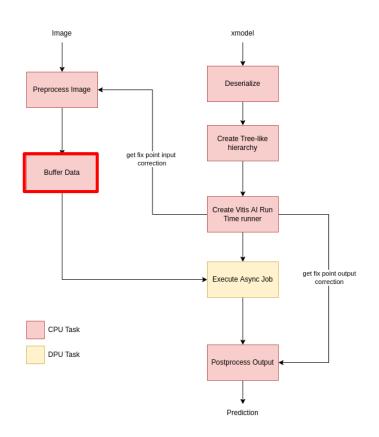
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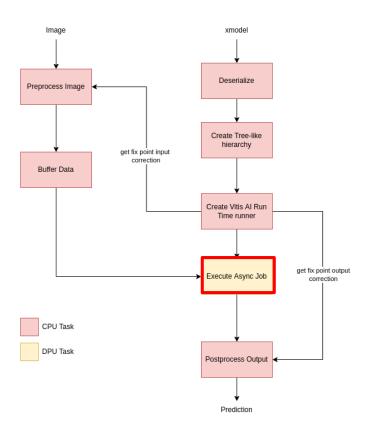
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57 print(result.astype('uint8'))
```



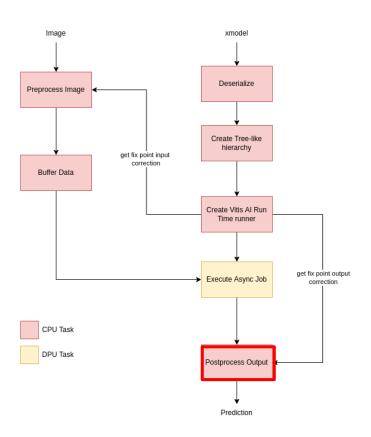
```
1 from PIL import Image
 2 import vart
 3 import xir
 4 import numpy as np
 6 def preprocess fn(image path):
      with Image.open(image path) as img:
          if img.mode != 'RGB':
               img = img.convert('RGB')
          img = img.resize((227, 227), Image.NEAREST)
      data = np.asarray(img, dtype="float32" )
13
      data = data/255.0
14
      return data
15
16 x0 = preprocess fn('../dataset/dump/cat/cat.10046.jpg')
17 x1 = preprocess fn('../dataset/dump/cat/cat.11175.jpg')
18 x2 = preprocess fn('../dataset/dump/dog/dog.10048.jpg')
19 x3 = preprocess fn('../dataset/dump/dog/dog.11175.jpg')
21 model = '../compilation/compiled model/deploy.xmodel'
23 g = xir.Graph.deserialize(model)
25 root subgraph = g.get root subgraph()
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29 dpu = vart.Runner.create runner(child subgraph[1], 'run')
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```

5. Deployment: Complete Application

To run a complete implementation

```
cd /tutorial/inference
python inference.py -d ../dataset/test -t 1 -m ../compilation/compiled_model/deploy.xmodel
```

6. Accuracy Check

To check your model implementation accuracy run

cd /tutorial/inference

python accuracy_calc.py

Thank you!