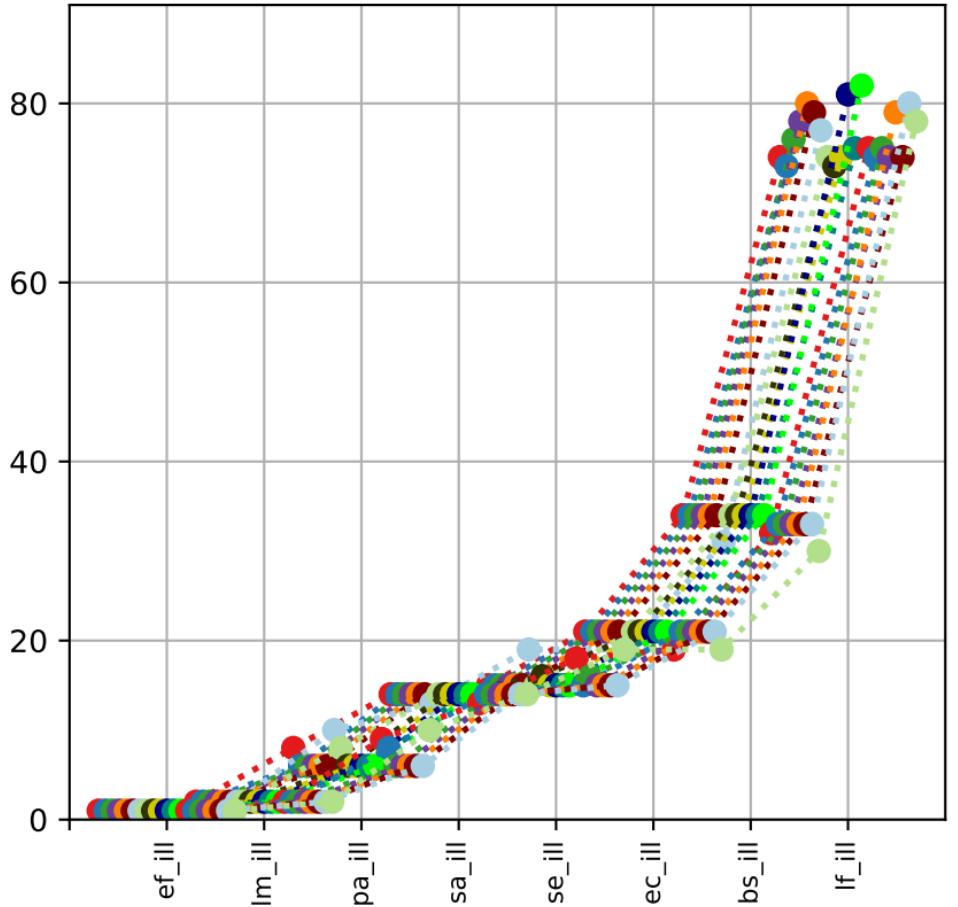


# misassemblies



- r10\_1bin\_v2\_1\_MP
- r10\_1bin\_v2\_1\_MP\_helen
- r10\_1bin\_v2\_1\_r1\_medaka
- r10\_1bin\_v2\_1\_r2\_medaka
- r10\_1bin\_v2\_1\_racon\_r1
- r10\_1bin\_v2\_1\_racon\_r2
- r10\_1bin\_v2\_1\_raw
- r10\_1bin\_v2\_2\_MP
- r10\_1bin\_v2\_2\_MP\_helen
- r10\_1bin\_v2\_2\_r1\_medaka
- r10\_1bin\_v2\_2\_r2\_medaka
- r10\_1bin\_v2\_2\_racon\_r1
- r10\_1bin\_v2\_2\_racon\_r2
- r10\_1bin\_v2\_2\_raw
- r10\_1bin\_v2\_3\_MP
- r10\_1bin\_v2\_3\_MP\_helen
- r10\_1bin\_v2\_3\_r1\_medaka
- r10\_1bin\_v2\_3\_r2\_medaka
- r10\_1bin\_v2\_3\_racon\_r1
- r10\_1bin\_v2\_3\_racon\_r2
- r10\_1bin\_v2\_3\_raw