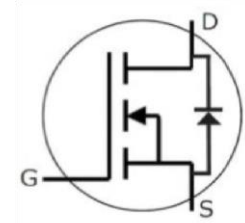
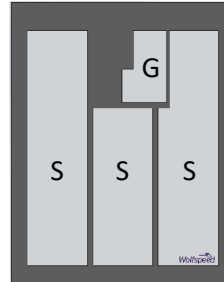


CPM3-1200-0021A

Wolfspeed SiC Gen 3 MOSFET

Description

This is the Wolfspeed's 3rd generation of high performance silicon carbide MOSFET in a packageless bare die format to be implemented into any custom module design. The high blocking voltage with low on-resistance, high speed switching with low capacitance make this MOSFET ideal for high frequency switching application including solar inverters and EV chargers.



Package Types: Bare Die
PN's: CPM3-1200-0021A

Features

- Enhanced 3rd Generation SiC MOSFET
- High blocking voltage with low on-resistance
- High speed switching with low capacitance
- Fast intrinsic diode with low reverse recovery

Applications

- Motor Drive
- Solar Inverters
- SMPS
- High voltage DC/DC converters

Absolute Maximum Ratings

Stress beyond those listed under absolute maximum ratings may damage the device.

| Parameter | Symbol | Rating | Unit |
|---|-------------------|---------------------|------------|
| Drain-Source Voltage, across T_{vj} | $V_{DS(max)}$ | 1200 | V |
| Maximum Gate-Source Voltage, Peak Transient Capability | $V_{GS(max)}$ | -8/+19 | V |
| Continuous Drain Current, $V_{GS} = 15V$, assumes die packaged in TO-247 package with $R_{th(j-c)} < 0.32$ K/W | I_D | $T_c = 25^\circ C$ | 100 |
| | | $T_c = 100^\circ C$ | 74.5 |
| Pulsed Drain Current, t_p limited by $T_{vj(max)}$ | $I_{D(pulse)}$ | 200 | A |
| Virtual Junction and Storage Temperature | T_{vj}, T_{stg} | -55 to 175 | $^\circ C$ |
| Maximum Processing Temperature, in non-reactive ambient | T_{proc} | 325 | $^\circ C$ |

Recommended Operating Conditions

| Parameter | Symbol | Rating | Unit |
|---|--------------|--------|------|
| Recommended Operating Gate - Source Voltage | $V_{GS(op)}$ | -4/+15 | V |

Electrical Characteristics ($T_{VJ} = 25^{\circ}\text{C}$)

| Characteristics | Symbol | Min. | Typ. | Max. | Unit | Test Conditions |
|----------------------------------|---------------|------|------|------|---------------|--|
| Drain-Source Breakdown Voltage | $V_{(BR)DSS}$ | 1200 | | | V | $V_{GS} = 0\text{ V}$, $I_D = 100\text{ }\mu\text{A}$ |
| Gate Threshold Voltage | $V_{GS(th)}$ | 1.8 | 2.5 | 3.6 | V | $V_{DS} = V_{GS}$, $I_{DS} = 16.8\text{ mA}$ |
| | | | 2 | | V | $V_{DS} = V_{GS}$, $I_{DS} = 16.8\text{ mA}$, $T_{VJ} = 175^{\circ}\text{C}$ |
| Zero Gate Voltage Drain Current | I_{DSS} | | 1 | 25 | μA | $V_{DS} = 1200\text{ V}$, $V_{GS} = 0\text{ V}$ |
| Gate-Source Leakage Current | I_{GSS} | | 10 | 100 | nA | $V_{GS} = 15\text{ V}$, $V_{DS} = 0\text{ V}$ |
| Drain-Source On-State Resistance | $R_{DS(on)}$ | 14.5 | 21 | 27 | m Ω | $V_{GS} = 15\text{ V}$, $I_D = 61\text{ A}$ |
| | | | 38 | | | $V_{GS} = 15\text{ V}$, $I_D = 61\text{ A}$, $T_{VJ} = 175^{\circ}\text{C}$ |
| Transconductance | g_{fs} | | 35 | | S | $V_{DS} = 20\text{ V}$, $I_{DS} = 61\text{ A}$ |
| | | | 33 | | | $V_{DS} = 20\text{ V}$, $I_{DS} = 61\text{ A}$, $T_{VJ} = 175^{\circ}\text{C}$ |
| Input Capacitance | C_{iss} | | 4818 | | pF | $V_{GS} = 0\text{ V}$, $V_{DS} = 1000\text{ V}$ $f = 100\text{ kHz}$ $V_{AC} = 25\text{ mV}$ |
| Output Capacitance | C_{oss} | | 180 | | | |
| Reverse Transfer Capacitance | C_{rss} | | 12 | | | |
| C_{oss} Stored Energy | E_{oss} | | 99 | | μJ | $V_{DS} = 1000\text{ V}$, $f = 100\text{ kHz}$ |
| Internal Gate Resistance | $R_{G(int)}$ | | 3.3 | | Ω | $f = 100\text{ kHz}$, $V_{AC} = 25\text{ mV}$ |
| Gate to Source Charge | Q_{gs} | | 49 | | nC | $V_{DS} = 800\text{ V}$, $V_{GS} = -4\text{ V}/15\text{ V}$ $I_{DS} = 61\text{ A}$ Per IEC60747-8-4 pg 21 |
| Gate to Drain Charge | Q_{gd} | | 50 | | | |
| Total Gate Charge | Q_g | | 162 | | | |

Reverse Diode Characteristics ($T_{VJ} = 25^{\circ}\text{C}$)

| Characteristics | Symbol | Typ. | Max. | Unit | Test Conditions |
|-------------------------------|-----------|------|------|------|---|
| Diode Forward Voltage | V_{SD} | 4.6 | | V | $V_{GS} = -4\text{ V}$, $I_{SD} = 30.5\text{ A}$ |
| | | 4.2 | | V | $V_{GS} = -4\text{ V}$, $I_{SD} = 30.5\text{ A}$, $T_{VJ} = 175^{\circ}\text{C}$ |
| Reverse Recovery Time | t_{rr} | 34 | | ns | $V_{GS} = -4\text{ V}$, $I_{SD} = 61\text{ A}$, $V_R = 800\text{ V}$ $\text{dif}/\text{dt} = 2600\text{ A}/\mu\text{s}$, $T_{VJ} = 175^{\circ}\text{C}$ |
| Reverse Recovery Charge | Q_{rr} | 928 | | nC | |
| Peak Reverse Recovery Current | I_{rrm} | 42 | | A | |

Typical Performance

All the graphs are based on a die placed in a TO-247-4L package.

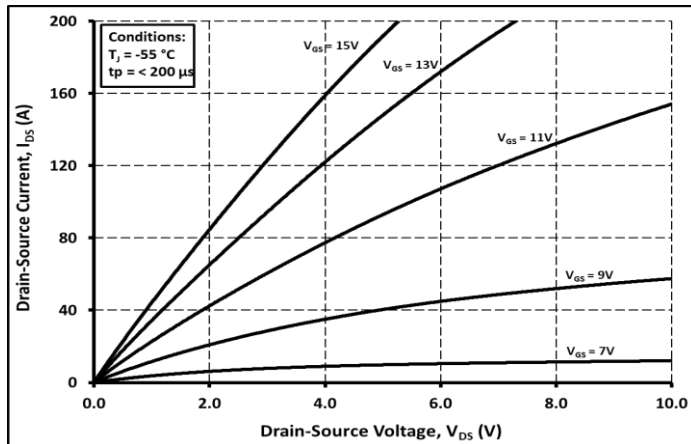


Figure 1.

Output Characteristics $T_{vj} = -55\text{ }^{\circ}\text{C}$

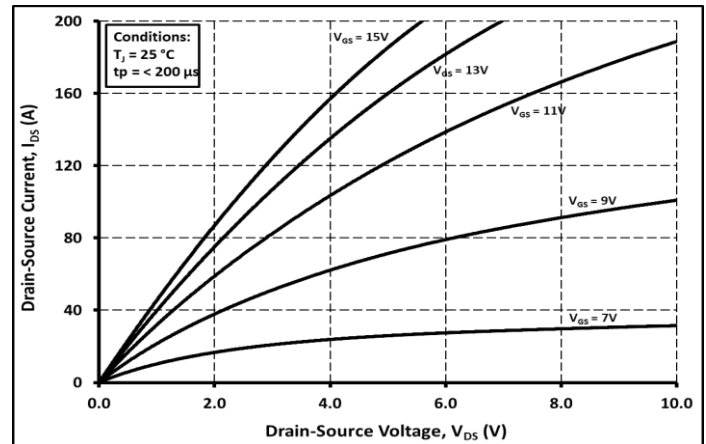


Figure 2.

Output Characteristics $T_{vj} = 25\text{ }^{\circ}\text{C}$

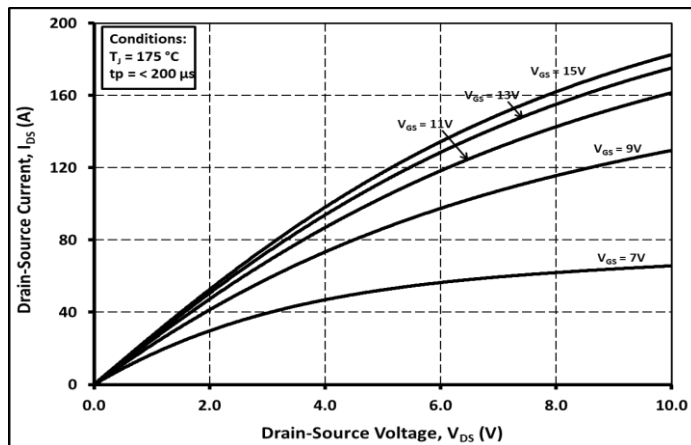


Figure 3.

Output Characteristics $T_{vj} = 175\text{ }^{\circ}\text{C}$

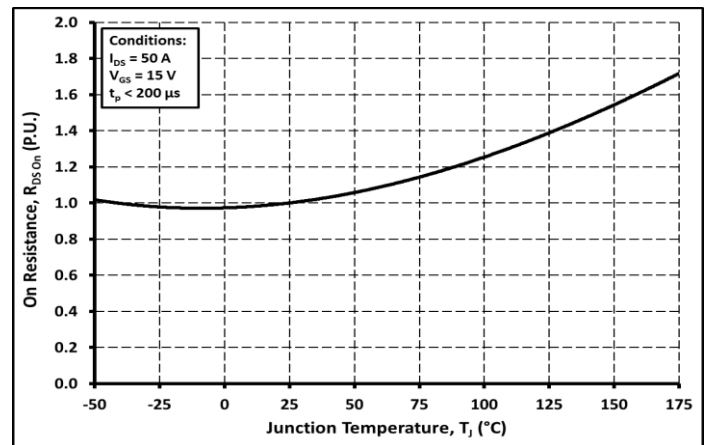


Figure 4.

Normalized On-Resistance vs. Temperature

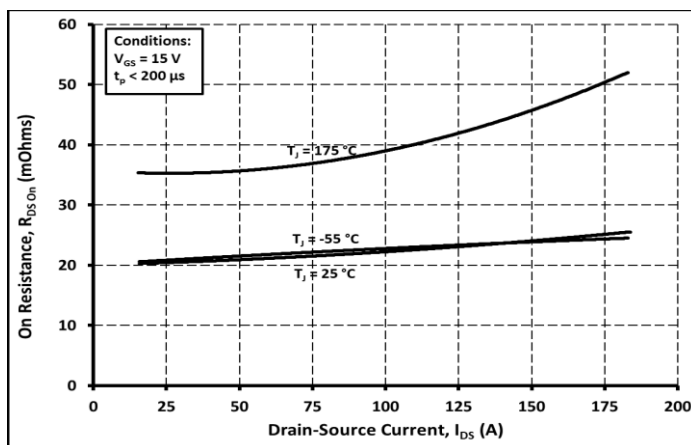


Figure 5.

On-Resistance vs. Drain Current For Various Temperatures

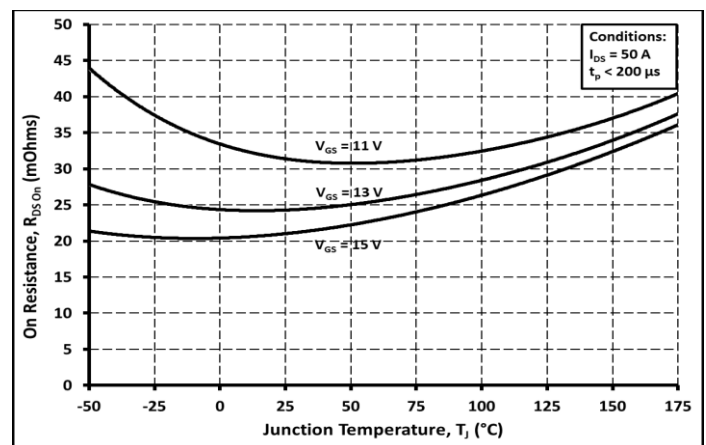


Figure 6.

On-Resistance vs. Temperature For Various Gate Voltages

Typical Performance

All the graphs are based on a die placed in a TO-247-4L package.

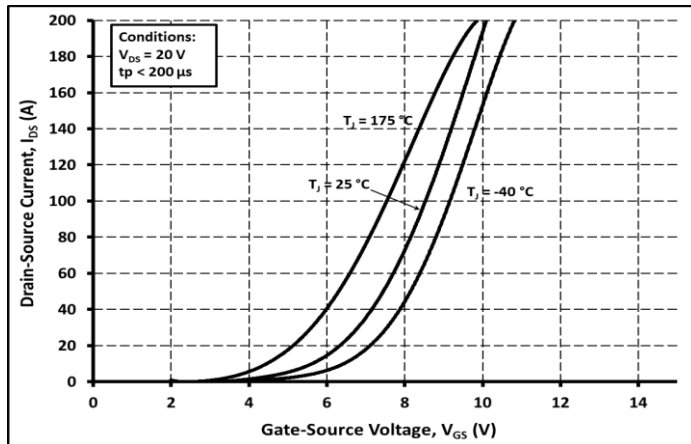


Figure 7.

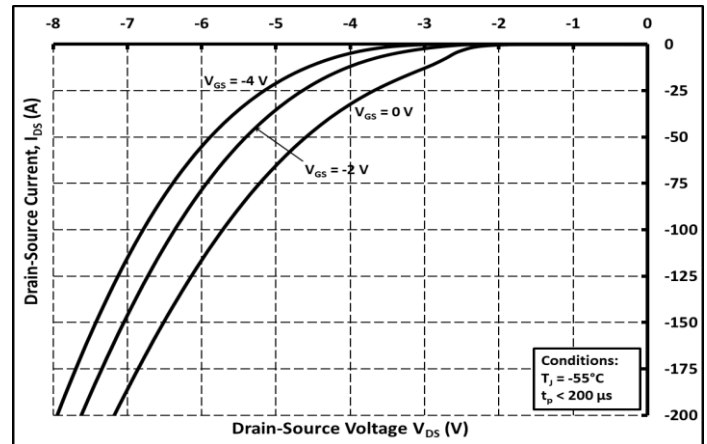


Figure 8.

Transfer Characteristic For Various Junction Temperatures

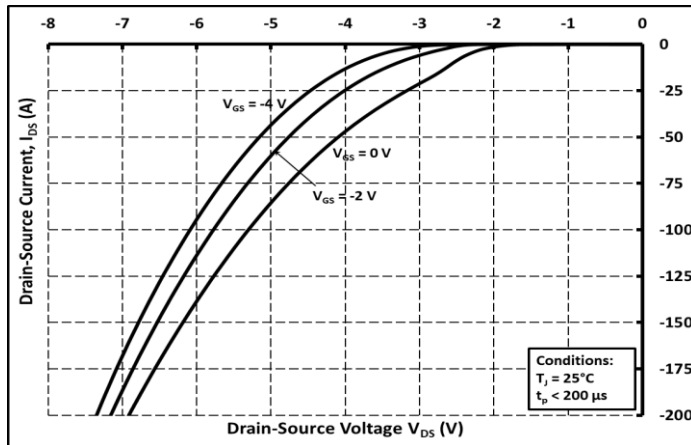


Figure 9.

Body Diode Characteristic at $T_{vj} = -55\text{ }^{\circ}\text{C}$

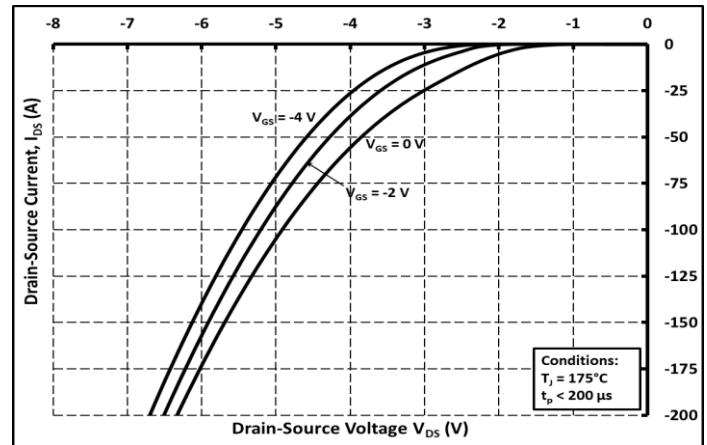


Figure 10.

Body Diode Characteristic at $T_{vj} = 25\text{ }^{\circ}\text{C}$

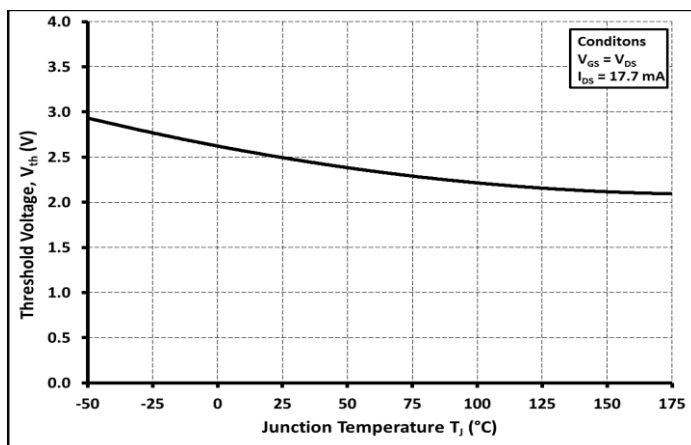


Figure 11.

Body Diode Characteristic at $T_{vj} = 175\text{ }^{\circ}\text{C}$

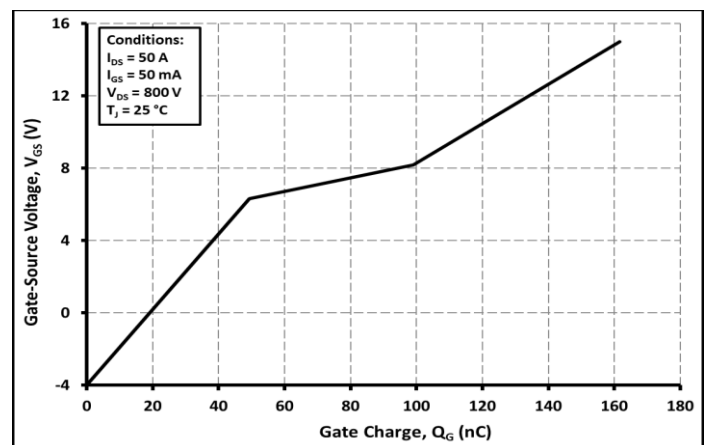


Figure 12.

Threshold Voltage vs. Temperature

Gate Charge Characteristics

Typical Performance

All the graphs are based on a die placed in a TO-247-4L package.

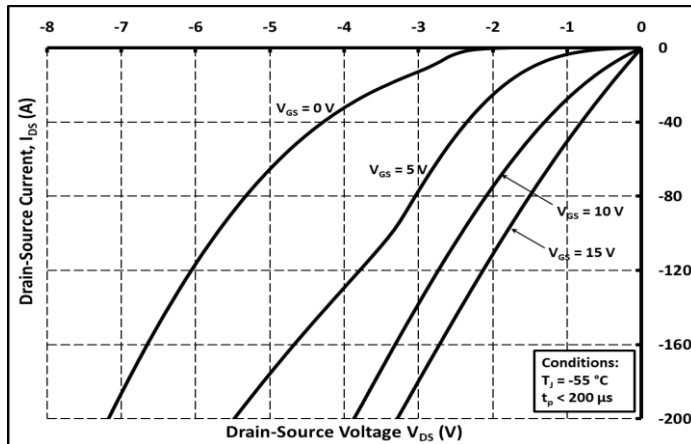


Figure 13.

3rd Quadrant Characteristic at $T_{vj} = -55\text{ }^{\circ}\text{C}$

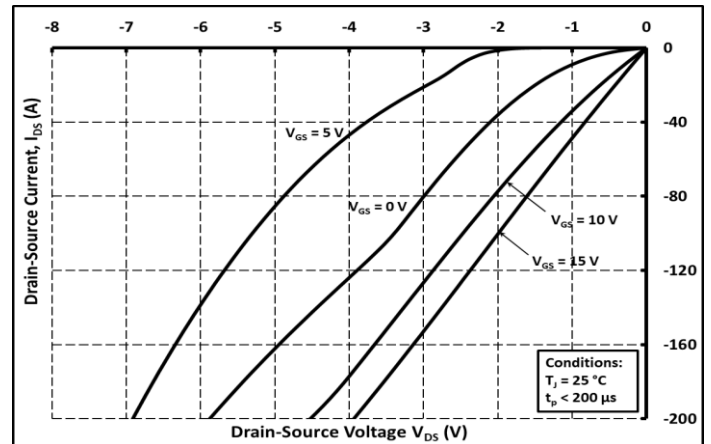


Figure 14.

3rd Quadrant Characteristic at $T_{vj} = 25\text{ }^{\circ}\text{C}$

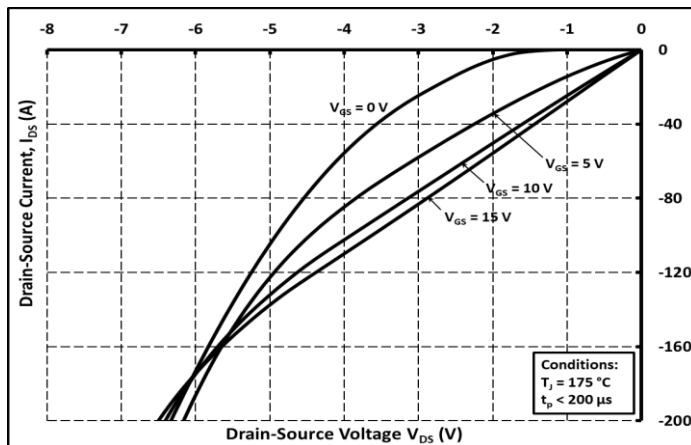


Figure 15.

3rd Quadrant Characteristic at $T_{vj} = 175\text{ }^{\circ}\text{C}$

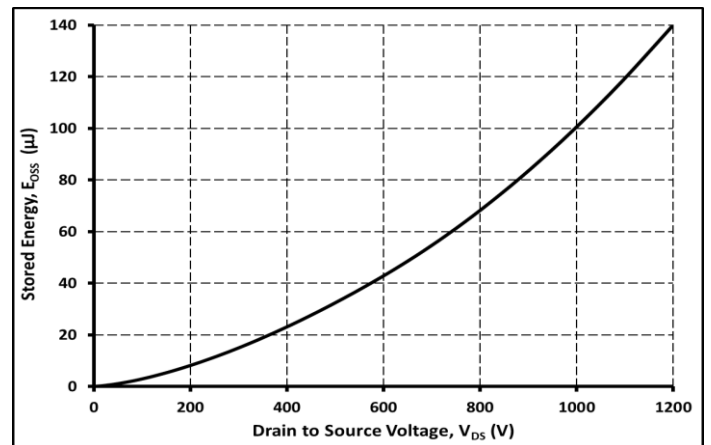


Figure 16.

Output Capacitor Stored Energy

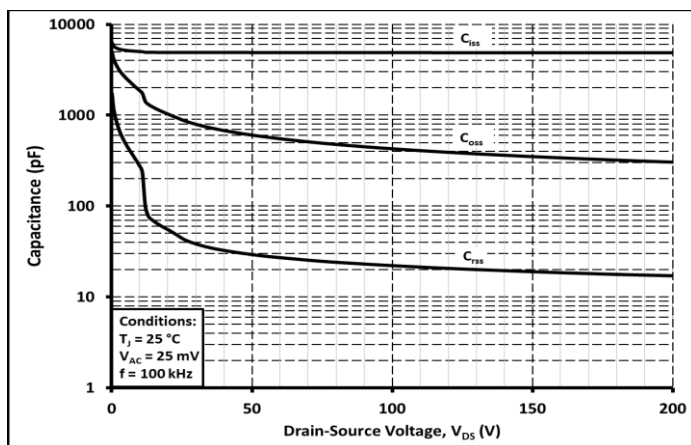


Figure 17.

Capacitances vs. Drain-Source Voltage (0-200V)

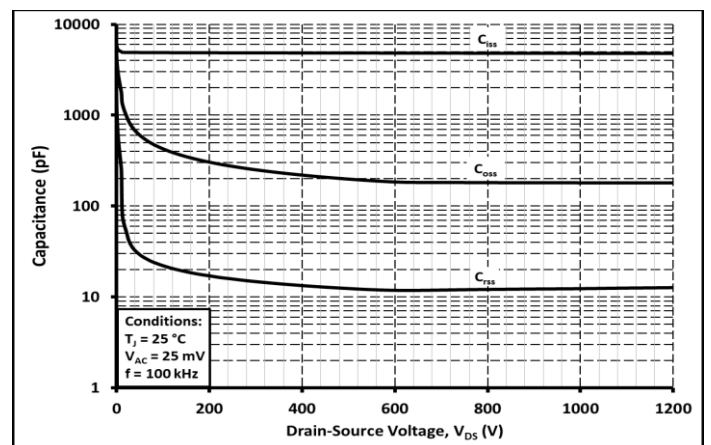


Figure 18.

Capacitances vs. Drain-Source Voltage (0-1200V)



Product Ordering Information

| Order Number | Description | Package |
|---------------------|--|------------------|
| CPM3-1200-0021A-FY6 | SIC MOSFET G3 IND 1200V/21mO UV MLT | Bare Die Product |
| CPM3-1200-0021A-GQ8 | SIC MOSFET G3 IND 1200V/21mO UV MVF | Bare Die Product |

Revision History

| Revision History | Date of Change | Brief Summary |
|------------------|----------------|--|
| - | 04/04/2019 | Initial Release |
| 1 | 01/09/2020 | <ul style="list-style-type: none">Removed test conditions and note section from the Maximum Ratings TableUpdated description for all the parameters in the Maximum Ratings TableUpdated footnotesTemperature note removed and embedded into every test conditionUpdated test conditions for gate threshold voltage, drain-source on-state resistance, transconductance, gate to source charge, gate to drain charge, total gate charge, diode forward voltage, reverse recovery time, reverse recovery charge and peak reverse recovery currentUpdated typical values for continuous drain current, zero gate voltage drain current, gate-source leakage current, drain-source on-state resistance, transconductance, input capacitance, reverse transfer capacitance, Coss stored energy, gate to source charge, gate to drain charge, total gate charge, reverse recovery time and reverse recovery chargeAll junction temperatures changed to virtual junction temperaturesAll graphs updated to reflect the most recent test data |
| 2 | 07/30/2023 | <ul style="list-style-type: none">Document format updated |
| 3 | 07/09/2024 | <ul style="list-style-type: none">Updated die image on the first page |



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