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Test Name:	**MBA: General Computer Science**
Taken On:	2 Aug 2019 11:24:04 PDT
Time Taken:	8 min 39 sec/ 10 min
Work Experience:	1 years
Invited by:	Jeff
Invited on:	2 Aug 2019 11:21:35 PDT
Tags Score:	CS Fundamentals 42.5/105
	IT 0/5
	Threads 0/5
	Virtual Memory 0/5

41.3%

47/115

scored in **MBA: General Computer Science** in 8 min 39 sec on 2 Aug 2019 11:24:04 PDT

Recruiter/Team Comments:

No Comments.

	Question Description	Time Taken	Score	Status
Q1	**Compiler** > **Multiple Choice**	17 sec	5/ 5	✓
Q2	**Transpiler** > **Multiple Choice**	19 sec	5/ 5	✓
Q3	**The kernel is primarily responsible for which of the following? (Select multiple** > **Multiple Choice**	31 sec	5/ 5	✓
Q4	**Select the term that best matches the following definition:** > **Multiple Choice**	8 sec	0/ 5	✗
Q5	**Assembly** > **Multiple Choice**	19 sec	5/ 5	✓
Q6	**Virtual Memory** > **Multiple Choice**	50 sec	0/ 5	✗
Q7	**Memory** > **Multiple Choice**	38 sec	0/ 5	✗
Q8	**Virtual Memory** > **Multiple Choice**	16 sec	5/ 5	✓
Q9	**Mutex** > **Multiple Choice**	44 sec	0/ 5	✗
Q10	**Interrupt** > **Multiple Choice**	8 sec	0/ 5	✗
Q11	**Memory Addresses** > **Multiple Choice**	18 sec	0/ 5	✗
Q12	**Memory Addresses** > **Multiple Choice**	5 sec	5/ 5	✓

1/14

Q13	**Memory Leak** › **Multiple Choice**	11 sec	5/ 5	✅
Q14	**Basic Computing** › **Multiple Choice**	9 sec	5/ 5	✅
Q15	**Router** › **Multiple Choice**	19 sec	0/ 5	❌
Q16	**Switch** › **Multiple Choice**	26 sec	0/ 5	❌
Q17	**Networking** › **Multiple Choice**	53 sec	0/ 5	❌
Q18	**Addressing** › **Multiple Choice**	10 sec	2.5/ 5	🟡
Q19	**OSI Model** › **Multiple Choice**	10 sec	0/ 5	❌
Q20	**OSI Model** › **Multiple Choice**	13 sec	0/ 5	❌
Q21	**OSI Model** › **Multiple Choice**	6 sec	0/ 5	❌
Q22	**Logic Gates** › **Multiple Choice**	18 sec	0/ 5	❌
Q23	**Logic Gates** › **Multiple Choice**	17 sec	5/ 5	✅

QUESTION 1

✅

Correct Answer

Score 5

Compiler › Multiple Choice [CS Fundamentals]

QUESTION DESCRIPTION

Which of the following best defines the purpose of a compiler?

CANDIDATE ANSWER

Options: (Expected answer indicated with a tick)

✅ ⦿ Translate source code from a high-level programming language to a lower-level programming language.

○ Translate source code from a lower-level programming language to a high-level programming language.

○ Executes source code.

○ Compresses source code into a single file.

No Comments

QUESTION 2

✓ **Correct Answer**

Score 5

Transpiler > Multiple Choice   CS Fundamentals

QUESTION DESCRIPTION

Which of the following best defines the purpose of a **transpiler**?

CANDIDATE ANSWER

Options: (Expected answer indicated with a tick)

- ○ Compresses and optimizes source code.
- ✓ ● Translate source code from one high-level language to another high-level language.
- ○ Translates source code from a high-level language to machine code.
- ○ Interprets and executes source code.

No Comments

QUESTION 3

✓ **Correct Answer**

Score 5

Multiple Choice

QUESTION DESCRIPTION

The **kernel** is primarily responsible for which of the following? (**Select multiple**)

CANDIDATE ANSWER

Options: (Expected answer indicated with a tick)

- ✓ ● Managing program access to the CPU.
- ○ Providing a graphical user interface (GUI) for users to interact with the system.
- ✓ ● Managing access to Random-access memory (RAM) and the allocation of RAM.
- ✓ ● Providing methods for applications to access Input/Output devices.
- ○ Handling application-level thread locking

No Comments

QUESTION 4

❌

Wrong Answer

Score 0

Multiple Choice   Threads

QUESTION DESCRIPTION

Select the term that best matches the following definition:

The smallest sequence of programmed instructions that can be managed independently by a scheduler.

CANDIDATE ANSWER

Options: (Expected answer indicated with a tick)

- 🔘 process
- ⚪ program
- ✅ ⚪ thread
- ⚪ context

No Comments

QUESTION 5

✅

Correct Answer

Score 5

Assembly › Multiple Choice   CS Fundamentals

QUESTION DESCRIPTION

An **assembly language** differs from machine code in what way?

CANDIDATE ANSWER

Options: (Expected answer indicated with a tick)

- ⚪ Assembly languages are machine code.
- ⚪ Assembly languages are not specific to computer architecture.
- ⚪ Machine code is assembled into an assembly language prior to its execution.
- ✅ 🔘 Assembly languages use mnemonic to represent machine code instruction, making it easier to read.

No Comments

QUESTION 6

Virtual Memory › Multiple Choice   [CS Fundamentals]   [Virtual Memory]

QUESTION DESCRIPTION

Which of the following best defines **virtual memory**?

CANDIDATE ANSWER

Options: (Expected answer indicated with a tick)

- ✓ ○ The illusion of large main memory
- ○ A large main memory
- ◉ A large secondary memory
- ○ A read-only memory

No Comments

QUESTION 7

Memory › Multiple Choice   [CS Fundamentals]

QUESTION DESCRIPTION

Main memory (as opposed to secondary storage) is most commonly stored in which area?

CANDIDATE ANSWER

Options: (Expected answer indicated with a tick)

- ○ Mass Storage Device (Hard Disk Drive/Solid State Drive)
- ◉ Memory Bus
- ○ Central Processing Unit (CPU) Cache
- ✓ ○ Random Access Memory (RAM)

No Comments

QUESTION 8

✅
Correct Answer

Score 5

Virtual Memory › Multiple Choice   [CS Fundamentals]

QUESTION DESCRIPTION

If a computer does not need virtual memory, there is also no need for which of the following?

CANDIDATE ANSWER

Options: (Expected answer indicated with a tick)

- ⭕ Random Access Memory (RAM)
- ⭕ Read Only Memory (ROM)
- ✅ 🔘 Memory Management Unit (MMU)
- ⭕ Central Processing Unit (CPU)

No Comments

QUESTION 9

❌
Wrong Answer

Score 0

Mutex › Multiple Choice   [CS Fundamentals]

QUESTION DESCRIPTION

A **mutex** is meant to do which of the following?

CANDIDATE ANSWER

Options: (Expected answer indicated with a tick)

- ⭕ Signal that a particular block of memory can be modified, deleted, or overwritten.
- ⭕ Convert a virtual memory address to a physical memory address.
- 🔘 Allow a resource to be accessed by multiple threads simultaneously.
- ✅ ⭕ Limit the access to a resource in an environment where there are multiple threads of execution.

No Comments

QUESTION 10

Interrupt > Multiple Choice   CS Fundamentals

QUESTION DESCRIPTION

When the processor encounters a **trap** or **exception**, or when the processor receives a signal requesting its immediate attention, the signal is called which of the following?

CANDIDATE ANSWER

Options: (Expected answer indicated with a tick)

- ○ Peripheral Message
- ⦿ Subroutine Call
- ✓ ○ Interrupt
- ○ Buffer

No Comments

QUESTION 11

Memory Addresses > Multiple Choice   CS Fundamentals

QUESTION DESCRIPTION

A logical address maps to a location in the _____ .

CANDIDATE ANSWER

Options: (Expected answer indicated with a tick)

- ✓ ○ virtual memory
- ○ secondary memory
- ○ main memory
- ⦿ CPU cache

No Comments

QUESTION 12

✅ **Correct Answer**

Score 5

Memory Addresses > Multiple Choice [CS Fundamentals]

QUESTION DESCRIPTION

A **physical address** maps directly to the _____ .

CANDIDATE ANSWER

Options: (Expected answer indicated with a tick)

- ✅ ⦿ Main Memory
- ◯ Virtual Memory
- ◯ CPU Cache
- ◯ Secondary Memory

No Comments

QUESTION 13

✅ **Correct Answer**

Score 5

Memory Leak > Multiple Choice [CS Fundamentals]

QUESTION DESCRIPTION

Which of the following represent a memory leak?

CANDIDATE ANSWER

Options: (Expected answer indicated with a tick)

- ✅ ⦿ Memory is allocated for a specific purpose but never deallocated.
- ◯ More memory is allocated than is necessary for a program to run.
- ◯ Two separate processes attempt to access the same memory address.
- ◯ Multiple pointers reference the same data.

No Comments

Correct Answer

Score 5

Basic Computing › Multiple Choice CS Fundamentals

QUESTION DESCRIPTION

The process of automatically reclaiming memory that is filled with objects that are no longer in use is called _____ .

CANDIDATE ANSWER

Options: (Expected answer indicated with a tick)

○ Pruning

○ Dissassembly

○ Profiling

✓ ● Garbage Collection

No Comments

Wrong Answer

Score 0

Router › Multiple Choice CS Fundamentals IT

QUESTION DESCRIPTION

Which of the following best defines the purpose of a router?

CANDIDATE ANSWER

Options: (Expected answer indicated with a tick)

✓ ○ Receive frames and broadcast those frames to all connected ports.

○ Receive frames and forward those frames only to intended addresses.

● Receive packets and redirect those packets between networks.

○ Act as a gateway for data packets based on pre-determined or user-configured rules.

No Comments

Switch > Multiple Choice   CS Fundamentals

QUESTION DESCRIPTION

Which of the following best defines the purpose of a switch?

CANDIDATE ANSWER

Options: (Expected answer indicated with a tick)

○ Receive packets and redirect them between networks.

✓ ○ Receive frames and forward those frames only to the intended addresses.

⦿ Act as a gateway for data packets based on pre-determined or user-configured rules.

○ Receive frames and broadcast those frames to all connected ports.

No Comments

Networking > Multiple Choice   CS Fundamentals

QUESTION DESCRIPTION

Given two small **separate** networks (each consisting of 5 computers, connected through a 10/100mbps hub), which device would be most appropriate to allow each network to communicate with the other, while allowing the networks to remain separate?

CANDIDATE ANSWER

Options: (Expected answer indicated with a tick)

⦿ 10/100Mbps Bridge

○ 10/100Mbps Hub

○ 10/100Mbps Switch

✓ ○ 10/100Mbps Router

No Comments

QUESTION 18

✓ Correct Answer

Score 2.5

Addressing › Multiple Choice   CS Fundamentals

QUESTION DESCRIPTION

Which of the following is/are a benefit to using IPv6 addresses over IPv4? (**Select Multiple**)

CANDIDATE ANSWER

Options: (Expected answer indicated with a tick)

- ◯ Smaller packet size
- ◯ No need to change IP address when connecting to different networks
- ✓ ◯ More efficient packet routing
- ✓ ◉ Lower chance of IP address conflicts

No Comments

QUESTION 19

✗ Wrong Answer

Score 0

OSI Model › Multiple Choice   CS Fundamentals

QUESTION DESCRIPTION

Select the layer responsible for handling physical addressing.

CANDIDATE ANSWER

Options: (Expected answer indicated with a tick)

- ◯ L7: Application
- ◯ L6: Presentation
- ◯ L5: Session
- ◯ L4: Transport
- ◯ L3: Network
- ✓ ◯ L2: Data Link
- ◉ L1: Physical

No Comments

❌

Wrong Answer

Score 0

OSI Model › Multiple Choice   CS Fundamentals

QUESTION DESCRIPTION

Which OSI layer is responsible for end-to-end connections and reliability?

CANDIDATE ANSWER

Options: (Expected answer indicated with a tick)

- ◯ L7: Application
- ◯ L6: Presentation
- ◯ L5: Session
- ✅ ◯ L4: Transport
- ◯ L3: Network
- ⦿ L2: Data Link
- ◯ L1: Physical

No Comments

QUESTION 21

❌

Wrong Answer

Score 0

OSI Model › Multiple Choice   CS Fundamentals

QUESTION DESCRIPTION

Which of the following layers is responsible for defining protocols for software (HTTP, FTP, SMTP, etc.)?

CANDIDATE ANSWER

Options: (Expected answer indicated with a tick)

- ✅ ◯ L7: Application
- ◯ L6: Presentation
- ◯ L5: Session
- ⦿ L4: Transport
- ◯ L3: Network
- ◯ L2: Data Link
- ◯ L1: Physical

No Comments

Logic Gates > Multiple Choice　　CS Fundamentals

QUESTION DESCRIPTION

Given the following inputs, what will the output be for an **AND** gate?

Input 1	Input 2	Output
0	0	x
0	1	x
1	0	x
1	1	x

CANDIDATE ANSWER

Options: (Expected answer indicated with a tick)

- ⦿ 1
    0
    0
    1

- ○ 0
    1
    1
    0

- ○ 0
    0
    1
    1

- ✓ ○ 0
    0
    0
    1

No Comments

Logic Gates > Multiple Choice   CS Fundamentals

QUESTION DESCRIPTION

Given the following inputs, what will the output be for an **OR** gate?

Input 1	Input 2	Output
0	0	x
0	1	x
1	0	x
1	1	x

CANDIDATE ANSWER

Options: (Expected answer indicated with a tick)

✓ ⦿ 0
1
1
1

○ 0
1
1
0

○ 1
1
1
0

○ 0
0
0
1

No Comments