# **NES Programming**

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## **NES Virtualization**

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#### **Overview**

- Graphical Capabilities/Limitations
- Tools
- Assembly Basics
- Programming
  - Drawing Sprites
  - Gamepad Input
- Using Programs on a Real Nintendo

### **Graphical Capabilities/Limitations**

#### Screen

- 256x240 Resolution
- Horizontal Scanlines (240)

#### Memory

- PRG-ROM 32 KB (2 Banks)
- CHR-RAM 8 KB
- MMC's (later) could be used to extend these values

### **Graphical Capabilities/Limitations**

#### Sprites

- 8x8 and 8x16
  - 8x16 are two memory locations.
  - All sprites must be aligned to the same memory size, so 8x8 sprites take up a 8x16 position.
- Max of 64 sprites on the screen at one time.
  - Max of 8 sprites per scanline.
  - More causes flickering.

### **Graphical Capabilities/Limitations**

#### Colors

- 4 colors per sprite (technically 3, the fourth is transparent).
- Store up to 48 4-color palettes.
- Max of 56 colors total (...yea...).
- 25 colors max per scanline.
  - Note, each sprite can have up to 3 colors.
  - 8\*3=24, which is the max-1.

#### **Tools**

- Emulator
  - fceu Nintendo Emulator
- Assembler
  - xa Don't use this...
  - tasm Don't use this...
  - nesasm Nintendo assembler.
- Graphic Tools
  - Palette Generator Not really needed...
  - yy-chr You will need this...

### **Assembly Basics**

Lets program!

### **Programming**

Lets program!

Inside of a cartridge.



#### Several Types:

- o MMC1
- o MMC2
- o MMC3
- o MMC4
- o MMC5
- o MMC6



- PRGROM 2x16 KB banks.
- CHRROM 2x4 KB banks.



- PRGROM 1x8 KB bank (24KB locked).
- CHRROM 2x4 KB banks.



- PRGROM 2x8 KB banks.
- CHRROM 2x2 KB and 4x1 KB banks.



- PRGROM 1x16 KB bank (16KB locked).
- CHRROM 2x4 KB banks.
- Battery powered
  SRAM.



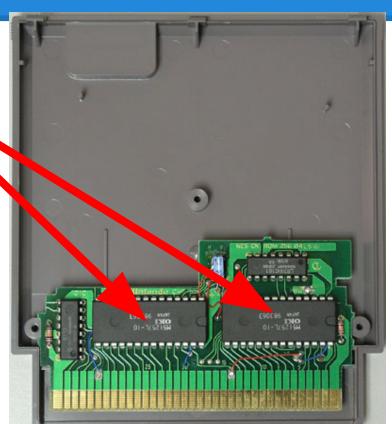
- Just speculation, increased memory.
- Extra 1 KB of RAM.



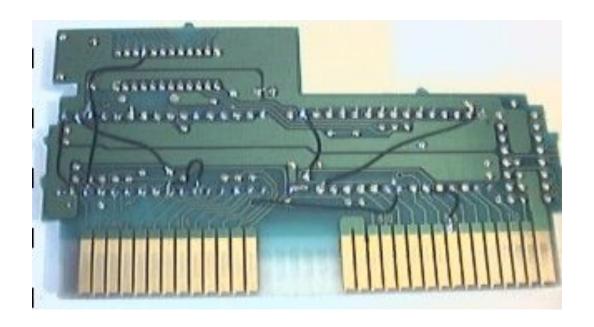
- MMC6
  - Same as MMC3.
  - Extra 1 KB of RAM.



- Unsolder these.
- Solder in sockets.



Change some of the traces.



Buy two AT49F002 chips.





Buy a universal EEPROM programmer.



- Process \*.nes file through <u>ReadNES</u>.
- Upload program data to one chip.
- Upload resources to other chip.

### Questions?

#### Sources

- https://helloacm.com/tutorial-1-c-programming-for-6502-8-bit-cpu/
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- http://skilldrick.github.io/easy6502/
- <a href="http://patater.com/gbaguy/nesasm.htm">http://patater.com/gbaguy/nesasm.htm</a>
- http://wiki.nesdev.com/w/index.php/PPU\_registers
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