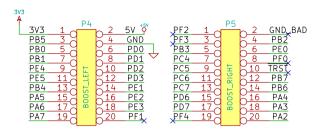
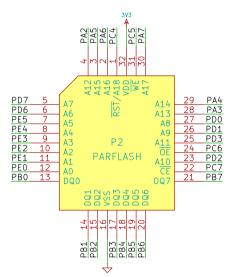


SPI_RST is a tentative pin for keeping the target board under reset while it is powered.

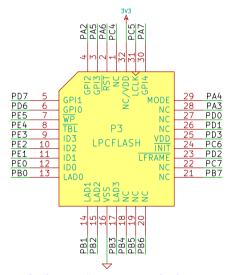
SPI header connection



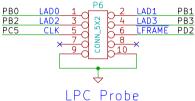
Booster pack connection



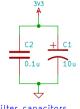
Unused address pins are usually left NC
On 512 KiB Devices pin 1 is used as A18
On Others it may be used as RESET, or NC



LAD[3:0], LCLK, #LFRAME, and ID[3:0] should be driven All other pins should be pulled high, but not driven - ID[3:0] should be driven low - TODO: Pin 32 is usually connected to VCC, however on some chips, it is left unconnected. They will not work unless powered through pin 25. The Stellaris is not capable of supplying enough current through GPIOs.



For development purposes Not needed in final design



Filter capacitors place next to socket



This is a PR	ELIMINARY design. Subject to change with	nout notice.
Alexandru Gagniuc		
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