### **BASIC PYTHON**

a logic gate in Python program

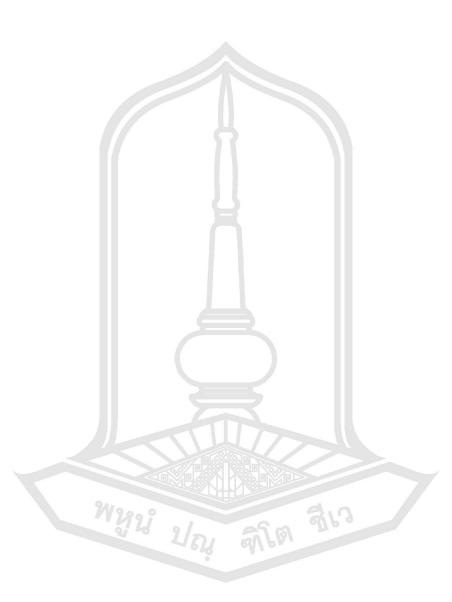
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### **BASIC PYTHON**

a logic gate in Python program

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Lecturer ASARAKHAM



## Logic gates

#### **Digital Logic Gate Symbols**

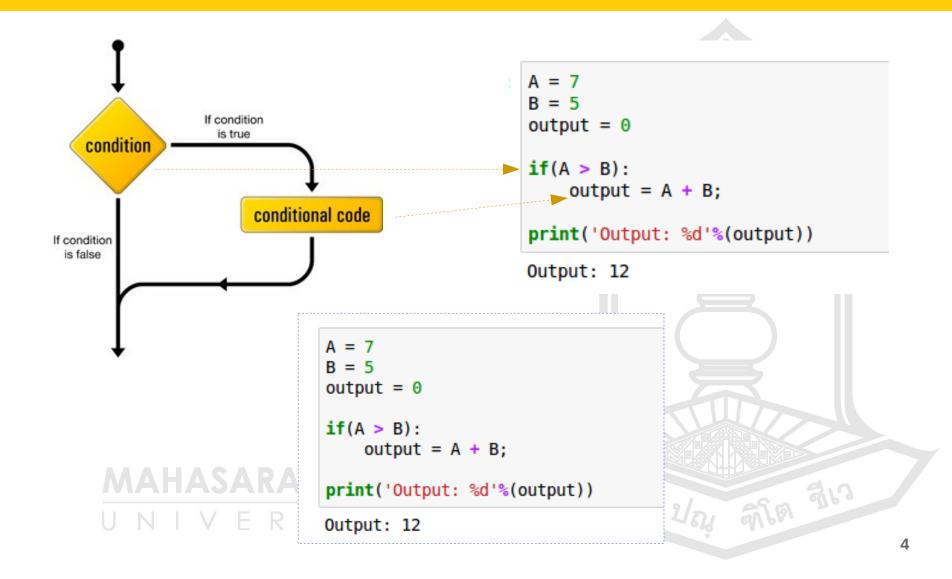
GATE	Symbol	Notation	TRUTH TABLE	
AND		$A \cdot B$	INPUT OUTPUT	
OR		A + B	INPUT OUTPUT  A B A OR B  0 0 0  0 1 1  1 0 1  1 1 1	
NOT	<b>→</b>	$\overline{A}$	INPUT OUTPUT	

			INPUT		OUTPUT
			A	В	A NAND B
NAND	1 10—	$\overline{A \cdot B}$	0	0	1
			0	1	1
			1	0	1
			1	1	0
	7		INPUT OUTPUT		
			A	В	A NOR B
torono.			0	0	1
NOR	1 20—	$\overline{A+B}$	0	1	0
			1	0	0
			1	1	0
		$A \oplus B$	INPUT OUTPUT		
			A	В	A XOR B
WOD			0	0	0
XOR			0	1	1
	<b>_</b> # /		1	0	1
			1	1	0

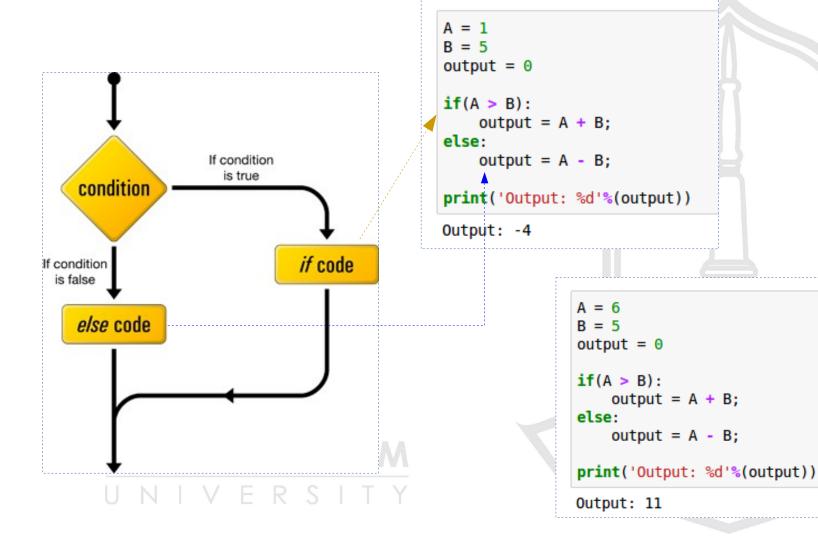
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Cr. https://forums.autodesk.com/t5/autocad-electrical-forum/autocad-electrical-logic-3 circuit-help/td-p/6706653

# if statement



### if..else statement

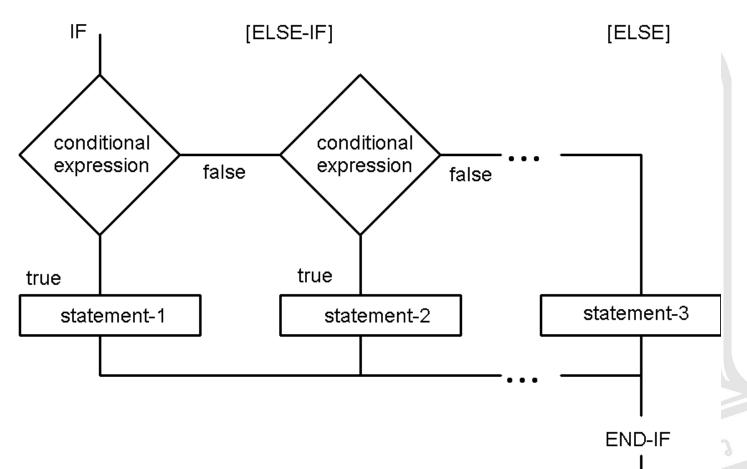


### **Practice**

- 1) เขียนโปรแกรมเพื่อคำนวณเกรด ในกรณีที่
  - นิสิตได้คะแนนตั้งแต่ 0 60 จะได้เกรด U
  - นิสิตได้คะแนนตั้งแต่ 61 100 จะได้เกรด S



### if..elif..else statement



**Cr.** https://docops.ca.com/ca-easytrieve/11-6/en/language-reference/statements-g-m/if-else-if-else-and-end-if-statements

### if..elif..else statement

```
A = 5
B = 5
output = 0
if(A > B):
    output = A + B;
elif(A < B):
    output = A - B;
else:
    output = A * B;
print('Output: %d'%(output))
Output: 25
```



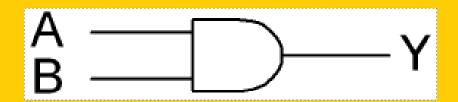


### **Practice**

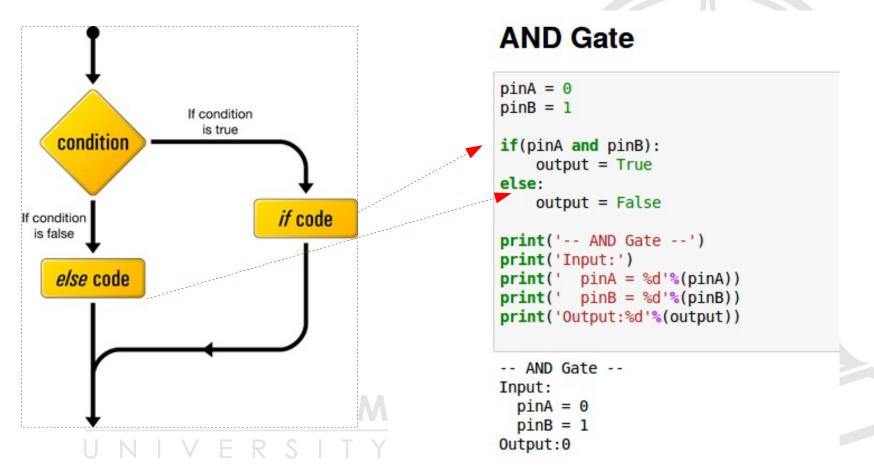
- 1) เขียนโปรแกรมเพื่อคำนวณเกรด ในกรณีที่
  - นิสิตได้คะแนนตั้งแต่ 0 49 จะได้เกรด F
  - นิสิตได้คะแนนตั้งแต่ 50 55 จะได้เกรด D+
  - นิสิตได้คะแนนตั้งแต่ 56 59 จะได้เกรด D
  - นิสิตได้คะแนนตั้งแต่ 60 64 จะได้เกรด C+
  - นิสิตได้คะแนนตั้งแต่ 65 69 จะได้เกรด C
  - นิสิตได้คะแนนตั้งแต่ 70 74 จะได้เกรด B
  - นิสิตได้คะแนนตั้งแต่ 75 79 จะได้เกรด B+
  - นิสิตได้คะแนนตั้งแต่ 80 100 จะได้เกรด A

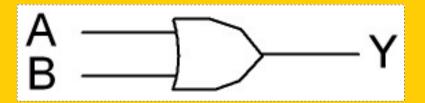
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### AND gate



### • if ... else statement





#### **OR Gate**

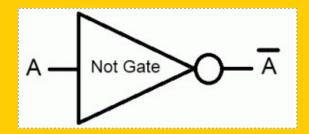
pinA = 0
pinB = 1
Output: 1

```
pinA = 0
pinB = 1

if(pinA or pinB):
    output = True
else:
    output = False

print('-- OR Gate --')
print('Input:')
print(' pinA = %d'%(pinA))
print(' pinB = %d'%(pinB))
print('Output: %d'%(output))
-- OR Gate --
Input:
```

### NOT gate



### **NOT Gate**

```
pinA = 1

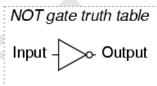
print('pin A = %d'%(pinA))

if(pinA == True):
    output = False
else:
    output = True

print('NOT Gate output: %d'%(output))

pin A = 1
NOT Gate output: 0
```

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Input	Output		
0	1		
1	0		

### **NOT Gate**

```
pinA = 0

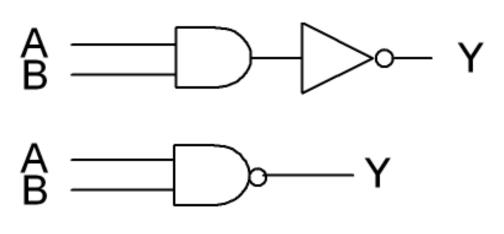
print('pin A = %d'%(pinA))

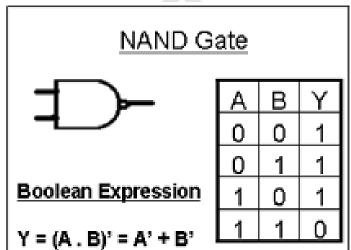
if(pinA == True):
    output = False
else:
    output = True

print('NOT Gate output: %d'%(output))

pin A = 0
NOT Gate output: 1
```

### NAND gate





### **MAHASARAKHAM**

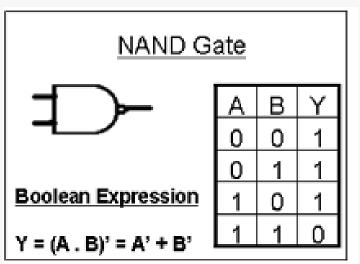
Cr. http://www.hardwaresecrets.com/introduction-to-logic-gates/4/http://verticalhorizons.in/universal-gates-in-digital-electonics-nand-and-nor/

### NAND gate

#### NAND Gate

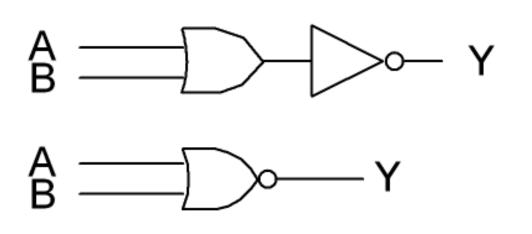
Output: 0

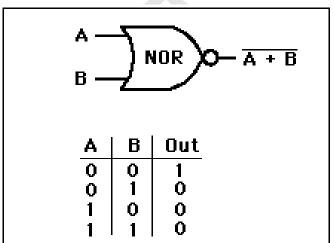
```
pinA = 1
pinB = 1
# AND gate
if(pinA and pinB):
                                 AND gate
   output = True
else:
    output = False
# NOT gate
if(output == True):
   output = False
                                  NOT gate
else:
                                                       Boolean Expression
   output = True
                                                       Y = (A . B)' = A' + B'
print('-- NAND Gate --')
print('Input:')
print(' pinA = %d'%(pinA))
print(' pinB = %d'%(pinB))
print('Output: %d'%(output))
-- NAND Gate --
Input:
 pinA = 1
 pinB = 1
```



## NOR gate

# Practice



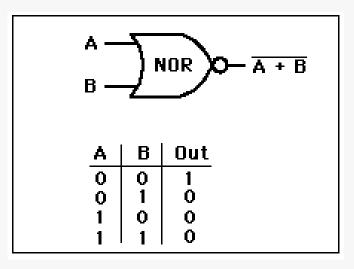


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### NOR gate

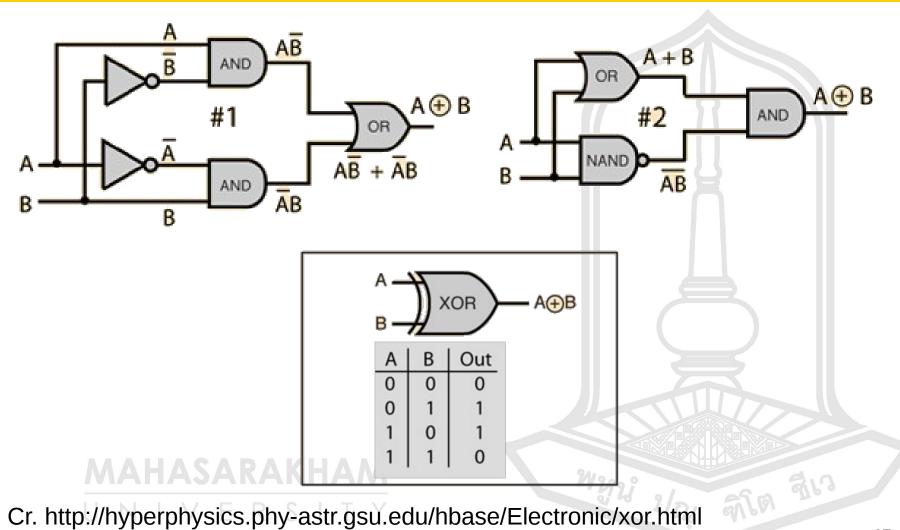
#### NOR Gate

```
pinA = 0
pinB = 0
# OR Gate
if pinA or pinB:
    output = True
else:
    output = False
# NOT gate
if(output == True):
    output = False
else:
    output = True
print('-- NOR Gate --')
print('Input:')
print(' pinA = %d'%(pinA))
print(' pinB = %d'%(pinB))
print('Output: %d'%(output))
-- NOR Gate --
Input:
 pinA = 0
  pinB = 0
Output: 1
```



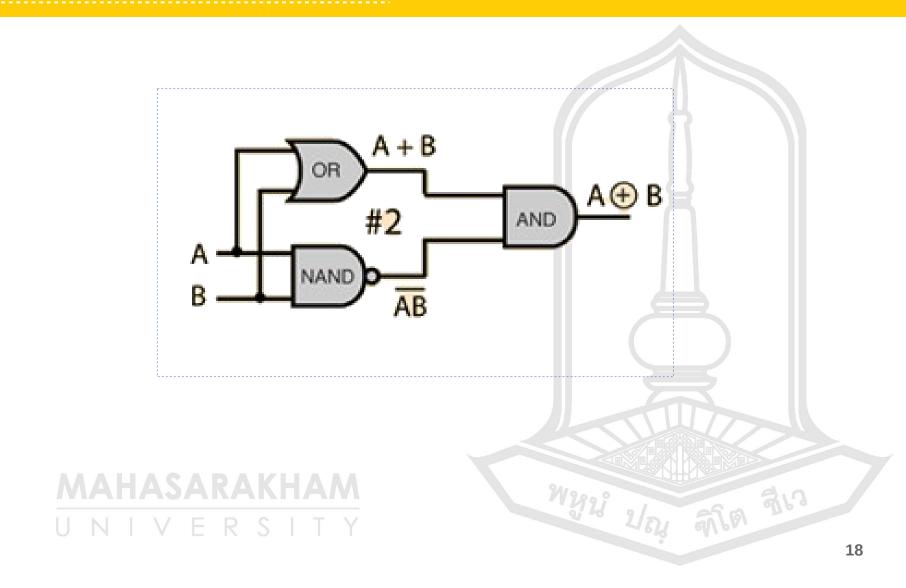






Cr. http://hyperphysics.phy-astr.gsu.edu/hbase/Electronic/xor.html

# Practice

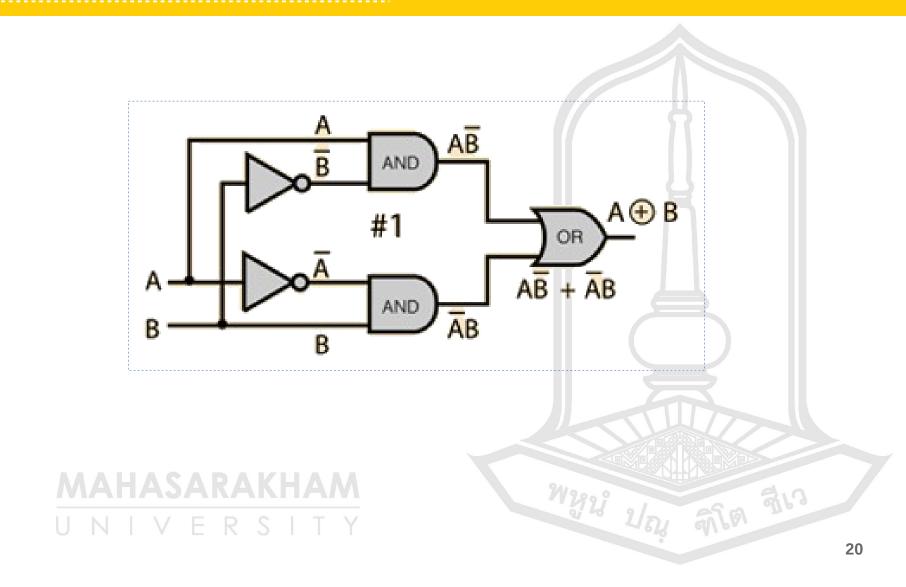


#### **XOR Gate**

```
pinA = 1
pinB = 0
# OR Gate
if(pinA or pinB):
    outputOR = True
else:
    outputOR = False
#NAND gate
# AND gate
if(pinA and pinB):
    outputNAND = True
else:
    outputNAND = False
# NOT gate
if(outputNAND == True):
    outputNAND = False
else:
    outputNAND = True
if(outputOR and outputNAND):
   output = True
else:
    output = False
```

```
print('-- XOR Gate --')
   print('Input:')
   print(' pinA = %d'%(pinA))
   print(' pinB = %d'%(pinB))
   print('Output: %d'%(output))
   -- XOR Gate --
   Input:
     pinA = 1
     pinB = 0
   Output: 1
#2
                 AND
   AB
                   क्रील ब्राप्त
```

# Practice



#### **XOR** gate

```
pinA = 0
pinB = 1
# NOT gate
if(pinB == True):
    outputNOTB = False
else:
    outputNOTB = True
# AND gate
if(pinA and outputNOTB):
    outputAND1 = True
else:
    outputAND1 = False
# NOT gate
if(pinA == True):
    outputNOTA = False
else:
    outputNOTA = True
# AND gate
if(outputNOTA and pinB):
    outputAND2 = True
else:
    outputAND2 = False
```

