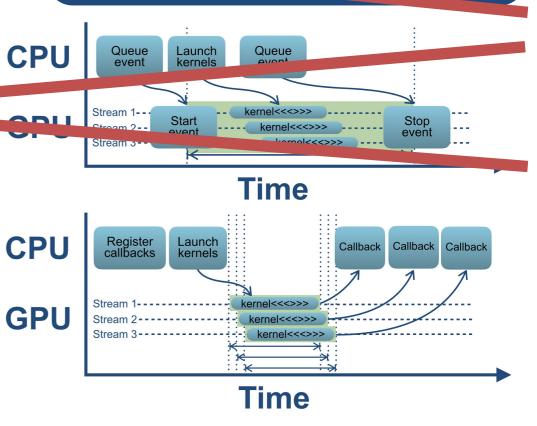
## Three possible on-the-fly timing strategies

- 1. Count CPU clock cycles
  - Requires additional device to host transiers
  - Implementation may be invasive
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  - Buffer requests and delivery of timing information handled by CUPTI
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