CSE 4095 – Special Topics in Computer Engineering III: Introduction to Embedded Systems Project Report

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Q1.

Source Code

```
main.s startup_stm32I476xx.s
                   INCLUDE core_cm4_constants.s
INCLUDE stm321476xx_constants.s
                 AREA data,
DCB "8AB", 0
DCB "478", 0
                            data, DATA, READWRITE
          str2
         result DCD 0
                  AREA main, CODE, READONLY
EXPORT __main
     10
     12
                  ENTRY
     13
    15
                   ;hex to dec first one
     16
                  LDR R2, = strl
BL hexToDec
     18
            ;hex to dec second one LDR R2, = str2
BL hexToDec
                 MOV R4, R0
     19
    20
21
                BL hexToDec
MOV R5, R0
    23
     24
                 ;subtraction
SUB R2, R4, R5
LDR R0, =result
    26
     28
     29
                   ; written to the memory that result label corresponds t
                  STR R2, [R0]
    31
     32 stop
                  B stop
    33
    34 hexToDec
                   PUSH{LR}
    36
37 parse
                  MOV RO, #0
                   LDRB R1, [R2], #1
    39
                  CMP Rl, #'0' ;if reached end of string BLT done
     40
                   CMP R1, #'9' ;if it is numeric
     42
                  BLE numeric
SUB Rl, Rl, #'A' - 10 ; then it is hexa
     43
     44
                  B convert
     45 numeric
                   SUB R1, R1, #'0'
    46
47 convert
48 LSL RO, RO, #4
49 ADD RO, RO, R1
    50
                  B parse
     51 done
    52
                 POP {PC}
    53
                   ENDP
    55
                   END
```

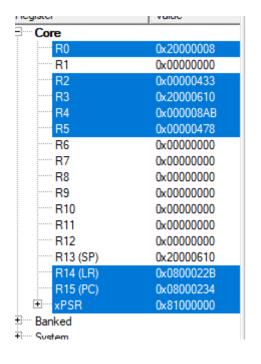
Code Explanation

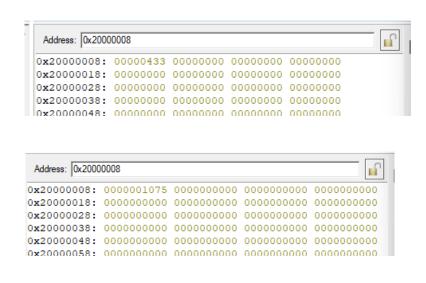
The code starts with two INCLUDE directives to import constants defined in two separate files 'core_cm4_constants.s' and 'stm32l476xx_constants.s'. These files define constants specific to the STM32L476 microcontroller. After the INCLUDE directives, the code defines three variables in the 'data' section: 'str1', 'str2', and 'result'. 'str1' and 'str2' are hexadecimal strings, and 'result' is a 32-bit integer initialized to zero.

The main section starts with the __main label, which is the entry point of the program. The program first calls the 'hexToDec' function twice to convert the two hexadecimal strings to decimal numbers and stores the results in registers R4 and R5. After that, the program subtracts the second value from the first one and stores the result in register R2. Then, it stores the result in the 'result' variable by writing it to the memory address that corresponds to the 'result' label.

The 'hexToDec' function is defined after the main section. It takes one argument in register R2, which is a pointer to a null-terminated hexadecimal string. It uses a loop to iterate over each character of the string and convert it to a decimal number. It then returns the decimal value in register R0. The function checks if the character is a valid hexadecimal digit. If it is a digit between 0 and 9, it calculates the decimal value of that digit by subtracting the ASCII value of '0' from the ASCII value of the digit, and then multiplying that value by 16 raised to the power of the current digit position. This value is then added to R0. If the character is a letter between A and F, the function calculates the decimal value of that letter in the same way, but adds 10 to the result to account for the fact that A represents 10, B represents 11, and so on. If the character is not a valid hexadecimal digit, the function returns -1 to indicate an error. Once the loop has processed all of the characters in the string, the function returns with R0, which is the decimal value represented by the hexadecimal string.

Output





The result of 8AB-478 in hexadecimal is 433. Which is 2219 - 1144 = 1075.

Source Code

```
main.s startup_stm32l476xx.s
                     INCLUDE core_cm4_constants.s
INCLUDE stm321476xx_constants.s
         AREA data, DATA, READMRITE
strl DCB "This is a test", 0
str2 DCB "This is a test", 0
strl_new SPACE 64
str2_new SPACE 64
     6 strl
7 str2
8 strl_new
    10
11
12
                    AREA main, CODE, READONLY
                     EXPORT __main
    13
14
15
16
                     ENTRY
         __main PROC
    17
18
19
20
21
                     LDR R0, = strl
LDR R1, = strl_new
BL upper_to_lower
                     LDR R0, = str2
LDR R1, = str2_new
BL lower_to_upper
    25
26 stop B stop
27
28 upper_to_lower
        upper_to_lower
PUSH{LR}
MOV R2, #0
MOV R3, #0
    29
         loopl
    33
                    LDRB R2, [R0], #1
CMP R2, #0
BEQ finish
    34
35
36
37
38
39
40
41
                     CMP R2, #'Z'
BGT other1
    42
43
44
45
46
47
48
49
50
51
52
53
                     ADD R2, R2, #'a' - 'A'
STRB R2, [R1], #1
B loop1
                     STRB R2, [R1], #1
                     B loop1
         lower_to_upper
PUSH{LR}
    54
55
56
57
58
         loop2
                    LDRB R2, [R0], #1
CMP R2, #0
                                    CMP R2, #0
             59
                                   BEQ finish
             60
             61
                                   CMP R2, #'a'
             62
                                  BLT other2
             63
             64
             65
                                   CMP R2, #'z'
             66
                                   BGT other2
             67
                                    ADD R2, R2, #'A' - 'a'
             68
             69
                                    STRB R2, [R1], #1
             70
                                    B loop2
             71
             72
                    other2
             73
                                   STRB R2, [R1], #1
             74
                                   B loop2
             75
             76
                    finish
             77
                                   STRB R3, [R1], #1
             78
                                    POP{PC}
             79
                                    ENDP
             80
             81
                                    END
             82
```

Code Explanation

The code defines three data items in the .data section:

str1 and str2: Two null-terminated strings that are initialized to "This is a test". These strings are used as inputs for the string manipulation functions.

str1 new and str2 new: Two empty buffers that are used to store the manipulated strings.

The __main function first calls the upper_to_lower function to convert the str1 string to lower case and store the result in str1_new. The __main function then calls the lower_to_upper function to convert the str2 string to upper case and store the result in str2_new.

The upper_to_lower function uses a loop to iterate over each character in the input string. For each character, the upper_to_lower function checks if it is an uppercase letter. If it is, the function converts it to lowercase by adding the difference between the ASCII codes of uppercase and lowercase letters ('a' - 'A') to the character. Then function writes the converted character to the output buffer. If the character is not an uppercase letter, the function writes the character as-is to the output buffer. The function ends by writing a null terminator to the output buffer and returning.

The lower_to_upper function is similar to upper_to_lower, but it converts lowercase letters to uppercase letters instead. The lower_to_upper function uses a loop to iterate over each character in the input string.

For each character, the lower_to_upper function checks if it is a lowercase letter. If it is, the function converts it to uppercase by adding the difference between the ASCII codes of lowercase and uppercase letters ('A' - 'a') to the character. The function writes the converted character to the output buffer. If the character is not a lowercase letter, the function writes the character as-is to the output buffer. The function ends by writing a null terminator to the output buffer and returning.

Output

```
      Memory 1
      4

      Address: 0x20000000
      0x200000000

      0x200000000: This is a test. This is a test. this is a test.
      0x200000000: THIS IS A TEST

      0x200000000: 0x20000000: 0x200000100: 0x20000140: 0x20000180: 0x20000180:
      0x20000180: 0x20000180: 0x20000180: 0x20000180: 0x20000180: 0x20000180: 0x20000180: 0x20000180: 0x20000180: 0x20000180: 0x20000180: 0x20000180: 0x20000180: 0x20000180: 0x200000180: 0x20000180: 0x
```

The result of the first "This is a test" is "this is a test".

The result of the second "This is a test" is "THIS IS A TEST".

Source Code

```
main.s core_cm4_constants.s startup_stm32l476xx.s

1 INCLUDE core_cm4_constants.s
2 INCLUDE stm32l476xx_constants.s
        4
5 NUM_ROWS EQU 4
6 NUM_COLS EQU 3
7
                     AREA matrices, DATA, READWRITE
             AREA matrices,
input_matrix
DCD 1, 2, 3
DCD 4, 5, 6
DCD 7, 8, 9
DCD 10, 11, 12
       10
       11
12
       13
      13
14
15 output_matrix
16 DCD 0, 0, 0, 0
17 DCD 0, 0, 0, 0
200 0, 0, 0, 0
       19
20
       21
                     AREA main, CODE, READONLY
       23
       24
25
                     EXPORT __main
ENTRY
       26
             __main PROC
       28
       29
30
                     ; Initialize input matrix index MOV R4, \sharp 0
       31
       32 ; Loop through columns of input matrix
33 outer_loop
       34
35
                      ; Initialize column index
                    MOV R2, #0
       36
                   MOV R6, #12
        38
       39
       41
       42 ; Loop through rows of input matrix
43 inner_loop
main.s core_cm4_constants.s startup_stm32l476xx.s
          ; Loop through rows of input matrix inner_loop
                 ; Calculate input matrix element address LDR R0, = input_matrix MOV R5, R4 MOV R4, R4, R6, R6 ADD R0, R0, R4, R5 MD R0, R0, R2, LSL $2 MOV R4, R5
    49
50
51
52
53
54
55
56
57
58
59
60
61
                ; Calculate output matrix element address LDR R1, = output_matrix MOV R5, R2 MUL R2, R2, R7 ADD R1, R1, R2 ADD R1, R1, R4, LSL $2 MOV R2, R5
                ; Load input matrix element LDR R3, [R0]
    62
63
64
65
66
67
71
72
73
74
75
76
77
78
80
81
82
                ; Store input matrix element in output matrix STR R3, [R1]
                ;Increment column index ADD R2, R2, #1
                ;Check if row index has reached end of input matrix CMP R2, NUM_COLS BNE inner_loop
                ;end of inner loop body
                ; Increment row index ADD R4, R4, #1
                 ;Check if column index has reached end of input matrix CMP R4, NUM_ROWS ENE outer_loop
    83
    84
85
86
87
88
89
          ;end of outer loop
         stop B stop
                        END
```

Code Explanation

This code defines two matrices, input_matrix and output_matrix, and then copies the elements of input_matrix to output_matrix in a column-wise fashion. The dimensions of the matrices are defined by the constants NUM_ROWS and NUM_COLS.

The code first initializes the index R4 to 0 to keep track of the row index of the input matrix. It then enters an outer loop that iterates over the columns of the input matrix. Within this loop, it initializes the index R2 to 0 to keep track of the column index of the input matrix, and enters an inner loop that iterates over the rows of the input matrix.

For each element in the input matrix, the code calculates the memory address of the corresponding element in the output matrix, loads the value of the input matrix element into register R3 and stores the value into the output matrix. This is done by calculating the index of input matrix at that location by multiplying with 12 (which is the size of a row), then calculating the index of output matrix at that location by multiplying with 16 (which is the size of a row again) and then switching the column and row registers to transpose it.

After processing all rows of a column, the code increments the column index and checks if it has reached the end of the input matrix. If not, the code continues processing the next column. If the column index has reached the end of the input matrix, the code increments the row index and checks if it has reached the end of the input matrix. If not, the code continues processing the next column.

Output

```
input matrix
                      DCD 1, 2, 3
                      DCD 4, 5, 6
The input matrix is
                      DCD 7, 8, 9
                      DCD 10, 11, 12
The output is
                 Memory 1
                  Address: 0x20000000
147A
                 0x20000000: 00000001 00000002 00000003 00000004
2 5 8 B
                 0x20000010: 00000005 00000006 00000007 00000008
                 0x20000020: 00000009 0000000A 0000000B 0000000C
369C
                 0x20000030: 00000001 00000004 00000007 0000000A
                 0x20000040: 00000002 00000005 00000008 0000000B
As it can be seen in
                 0x20000050: 00000003 00000006 00000009 0000000C
                 memory.
```