

# MEMS tunable polarization rotator for optical communication

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Master's Degree Project in  
Micro and Nanosystems  
Second level, 30 HEC  
Stockholm, Sweden June 2016



**KTH Electrical Engineering**

# MEMS tunable polarization rotator for optical communication

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Stockholm, February 6, 2016

# Acknowledgement

# Abstract

Describe briefly about the system

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# 1

## Chapter 1

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# Introduction

Communication and collected thinking is the key to the development and continuous evolution of human civilization which is driven by data - "The new oil of this digital era". With the advent of Internet of Things (IoT), it has been estimated that by 2020 there will be 50 billion connected devices and all that can be connected will be connected. Today only 40% of the world's population use the Internet and the amount of data we produce per minute through different social media platform like Facebook, Youtube, Twitter, Snapchat, Instagram, BuzzFeed, Tinder, Vine, Whatsapp etc. is growing exponentially (Info-graphic example). Also, with the advent of mobile communications, there has been a huge surge in the voice as well as data traffic all over the world. It has been estimated in Ericsson's mobility report (June, 2015) that 90% of the world's population over 6 years old will have a mobile phone by 2020. Currently, the telecommunications industry is moving towards IMS core networks which will mostly be IP based. So how is this humongous traffic managed? The answer is the optical fiber, which serves as the backbone of all the communication systems. The performance of optical fiber network is unprecedented and it is this backbone which gives us an unfathomable user experience. The current internet architecture has already pushed the optical fiber to the network edges and the trend is to push it as closer to the processor as possible. This has already opened up a new area of 'Siliconizing' photonics based on the decades of research obtained from microelectronics industry. The electronics industry have pushed the boundaries of the processing speed of integrated circuits after the invention of semiconductors and are currently at the edge where Moore's Law is at its upper limit. Although, with the current technology we have a decent processing power but what about the interconnects between the ICs. Transmission of electrons through copper would definitely slow things down. Think of a data center processing petabytes of data per minute, where interconnects between processors add a significant delay. These losses can be overcome by adding optical interconnects using the current technology which can also operate at low power catering to the needs of the environment.

## 1.1 Motivation

The movement of data in a computer is almost the converse of movement of traffic in a city. (<http://spectrum.ieee.org/semiconductors/optoelectronics/linking-with-light>). Intel

processor speed and bus speed comparison shows that although we have achieved good processing speed, the interconnects are still a thing of bother. Obviously, the closer engineers can bring the optical superhighway to the microprocessor, the fewer copper bottlenecks can occur. Until recently, exponential increases in the speed, efficiency, and processing power of conventional electronic devices were achieved largely through the downscaling and clustering of components on a chip. However, this trend toward miniaturization has yielded unwanted effects in the form of significant increases in noise, power consumption, signal propagation delay and aggravates already to serious thermal management problems. Alternatively, the wires can be made fatter, but then you'll run out of space. As a result, traditional microelectronics will soon fall short of meeting market needs, inhibited by the thermal and bandwidth bottlenecks inherent in copper wiring. Photons don't suffer from these limitations; their biggest problems are absorption and attenuation, neither of which is an issue over the distances inside a computer, or even across a room. Today Silicon Photonics Technology is a new approach to make optical devices out of silicon and use light (photons) to move huge amounts of data at very high speeds with extremely low power over a thin optical fiber rather than using electrical signals over a copper cable. Since, already enough capital investments has been done on current fabrication technology and infrastructure, engineers are working on creating monolithic design of integrated circuits which use light in place of electric signals. Organizations are trying to bridge this gap by creating highly integrated photonic and electronic components that combine the functionality of conventional CMOS circuits with the significantly enhanced system performance of photonic solutions. By allowing for the seamless integration of optical and electronic components on silicon-based substrates, this technology holds the key to fulfilling market needs for higher bandwidth and processing speed at lower power and cost.

## 1.2 Objectives

The current situation landscape in Silicon Photonics is quite exciting and a number of problems are there in the industry to realize a final optimal and robust prototype. The main objective of this thesis work is to design and fabricate low power tunable polarization rotator which can primarily mitigate the effects of dispersion in fiber optic communications. To tune at sufficient low power, MEMS (Microelectromechanical Systems) technology will be used along with photonics. The waveguides will be build upon current Silicon fabrication technology which will be characterized using an automated measurement setup to minimize human errors. In this thesis following objectives will be addressed:

- ☐ How to design a MEMS tunable polarization rotator?
- ☐ How to simulate the design using FEM (Finite Element Modeling) techniques like Comsol, CST etc.?
- ☐ How to effectively characterize the system with the measurements available?



- ☐ By how much can the power consumption be reduced in comparison to current state of the art?
- ☐ What are the different scenarios/flows to reduce the power consumption of polarization rotator?
- ☐ How robust and efficient the solution is in terms of real deployment?

## **1.3 MEMS and silicon photonics**

Microelectromechanical Systems (MEMS) are micrometre-sized sensors and actuators that are used in many devices in our everyday life. Using the same fabrication technology, one can fabricate on-chip optical circuits, which drastically improve the performance of telecommunication systems. However, both MEMS and silicon waveguides have been independently developed. This project aims to bring together both fields to try out new ideas to enable new applications and to improve existing ones.

## **1.4 Importance of these systems**

Silicon photonics is on the verge of creating a newer technology trend. It is on the brink of creating a technological revolution where semiconductor industry stood back 60 years from now just before the discovery of transistors. If this technology is developed successfully it can keep up with the tremendous data processing power needed by the current networking servers to deliver services. Since use of optics can also increase the bandwidth in a many-fold manner it can cater the needs of data rate needed by the futuristic networks. Also, as the vision is to move everything closer to the chip this technology can deliver ICs which can be easily fit into futuristic devices. Using MEMS for the tuning will ensure high resolution at very lower requirements. This is important because light transmitted to an optical circuit will typically come from an optical fibre, and optical fibres emit light with random polarization. These systems can expand the boundary of optical networks and bring it closer to the end devices by transgressing the network edges.

## **1.5 Outline of this thesis**

The outline of the thesis is as follows: Background, motivation and the research questions being addressed, is discussed in Chapter 1. In Chapter 2, the current state of art for the available solution is discussed. Chapter 3 discusses about the design of the final system and the results obtained about the simulation setup. In Chapter 4, documentation about the fabricated design is provided along with currently available standard fabrication technologies. Results and characterization are an important part of the work, which is discussed in Chapter 5. Finally, Chapter 6 and 7 discusses about the conclusion and

future work possibilities respectively along with the known limitations of the system if any.

# 2

## Chapter 2

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# State of the art

### 2.1 Theoretical background

Although silicon is the optimal material for electronics, only recently silicon is being considered as a practical option for electro-optics solutions. Silicon has many properties conducive to fiber optics. The band gap of silicon ( $\sim 1.1$  eV) is such that the material is transparent to wavelengths commonly used for optical transport (around 1.3–1.6  $\mu\text{m}$ ). One can use standard CMOS processing techniques to sculpt optical waveguides onto the silicon surface. Similar to an optical fiber, these optical waveguides can be used to confine and direct light as it passes through the silicon. (cite: book, graham reed). Due to the wavelengths typically used for optical transport and silicon's high index of refraction, the feature sizes needed for processing these silicon waveguides are on the order of 0.5–1  $\mu\text{m}$ . The lithography requirements needed to process waveguides with these sizes exist today. If we push forward to leading-edge research currently under way in the area of photonic band-gap devices (PBGs), today's state-of-the-art 90-nm fabrication facilities should meet the technical requirements needed for processing PBGs. What this says is that we may already have all or most of the processing technologies needed to produce silicon-based photonic devices for the next decade. In addition, the same carriers used for the basic functionality of the transistor (i.e. electrons and holes) can be used to modulate the phase of light passing through silicon waveguides and thus produce 'active' rather than passive photonic devices. Finally, if all this remains CMOS-compatible, it could be possible to process transistors alongside photonic devices, the combination of which could bring new levels of performance, functionality, power and size reduction, all at a lower cost.

**2.1.1 Maxwell's equations**

**2.1.2 Optical waveguides**

**2.1.3 Polarization in optical waveguides**

**2.2 Polarization rotator**

**2.2.1 Passive polarization rotator**

**2.2.2 Active polarization rotator**

# **3**

Chapter 3

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## **Design and simulation**

### **3.1 Approach**

### **3.2 Choice of simulation**

### **3.3 Designing the experiment**

#### **3.3.1 Use case: Passive polarization rotator**

#### **3.3.2 Use case: Active polarization rotator**

# 4

Chapter 4

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## Fabrication

# **5**

Chapter 5

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## **Results**

### **5.1 Experimental setup for measurement**

### **5.2 Results**

### **5.3 Analysis**

# 6

Chapter 6

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## Conclusions



# 7

Chapter 7

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## Limitations and future work

Here I have cited the [1] and [2] just for fun.

### 7.1 Limitations

### 7.2 Future work

# Abbreviations

**IoT** Internet of Things. 1

# Bibliography

- [1] Analog Systems, “ADXL335 Datasheet. <http://www.analog.com/media/en/technical-documentation/data-sheets/ADXL335.pdf>,” 2010.
- [2] D. Kiburz, R. Jacobs, F. Reckling, and J. Mason, “Bicycle accidents and injuries among adult cyclists,” *The American journal of sports medicine*, vol. 14, no. 5, pp. 416–419, 1986.