

1.

a)

T1	T2	T3	P	f(T1, T2, T3, P)
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	1
0	1	0	0	0
0	1	0	1	1
0	1	1	0	0
0	1	1	1	1
1	0	0	0	0
1	0	0	1	1
1	0	1	0	0
1	0	1	1	1
1	1	0	0	0
1	1	0	1	1
1	1	1	0	1
1	1	1	1	1

POS: $f(T1, T2, T3, P) = (T1 + T2 + T3 + P)(T1 + T2 + T3 + P')(T1 + T2 + T3' + P)(T1 + T2' + T3 + P)(T1 + T2' + T3' + P)(T1' + T2 + T3 + P')(T1' + T2 + T3 + P)(T1' + T2 + T3' + P)(T1' + T2' + T3 + P)$

b) Minimized:

Combining: $(T1 + T2 + T3)(T1' + T2 + T3)(T1 + T2' + P)(T1' + T2 + T3)(T1 + T2 + P)(T1' + T2 + P)(T2' + T3 + P)(T2' + T3 + P)$

Combining: $(T_2 + T_3)(T_1 + P)(T_2 + P)(T_2' + T_3 + P)$

Distributive: $(T_2T_1 + T_2P + T_3T_1 + T_3P)(T_2T_2' + T_2T_3 + T_2P + T_2'P + T_3P + PP)$

$xx' = 0$ and $xx = x$: $(T_2T_1 + T_2P + T_3T_1 + T_3P)(T_2T_3 + T_2P + T_2'P + T_3P)$

combining : $(T_2T_1 + T_2P + T_3T_1 + T_3P)(T_2T_3 + P + T_3P)$

distributive : $(T_1T_2T_3 + T_1T_2P + T_1T_2T_3P + T_2PT_3 + T_2P + T_2PT_3 + T_2T_3TP + T_3PT_2T_3 + T_3P + T_3P)$

Absorption: $(T_1T_2T_3 + T_2P + T_3P)$

→ **$f(T_1, T_2, T_3, P) = T_2P + T_3P + T_1T_2T_3$**

2.

a) SOP:

$f = \sum M(1,3,5,6,7) = x_1'x_2'x_3 + x_1'x_2x_3 + x_1x_2'x_3 + x_1x_2x_3' + x_1x_2x_3$

Combining: $x_1'x_3 + x_2'x_3 + x_1x_2 + x_1x_3 + x_2x_3$

combining: $x_1x_2 + x_3$

$f = x_1x_2 + x_3$

b) POS:

$f = \prod M(0,2,4) = (x_1 + x_2 + x_3)(x_1 + x_2' + x_3)(x_1' + x_2 + x_3)$

Combining: $(x_1+x_3)(x_2+x_3)$

$f = (x_1+x_3)(x_2+x_3)$

c) SOP cost: 6

POS cost: 9

SOP has lowest hardware complexity

3. XNOR out of a 4-input mux:

x1	x0	f(x1,x0)
0	0	1
0	1	0
1	0	0
1	1	1

Select bits are x1x0

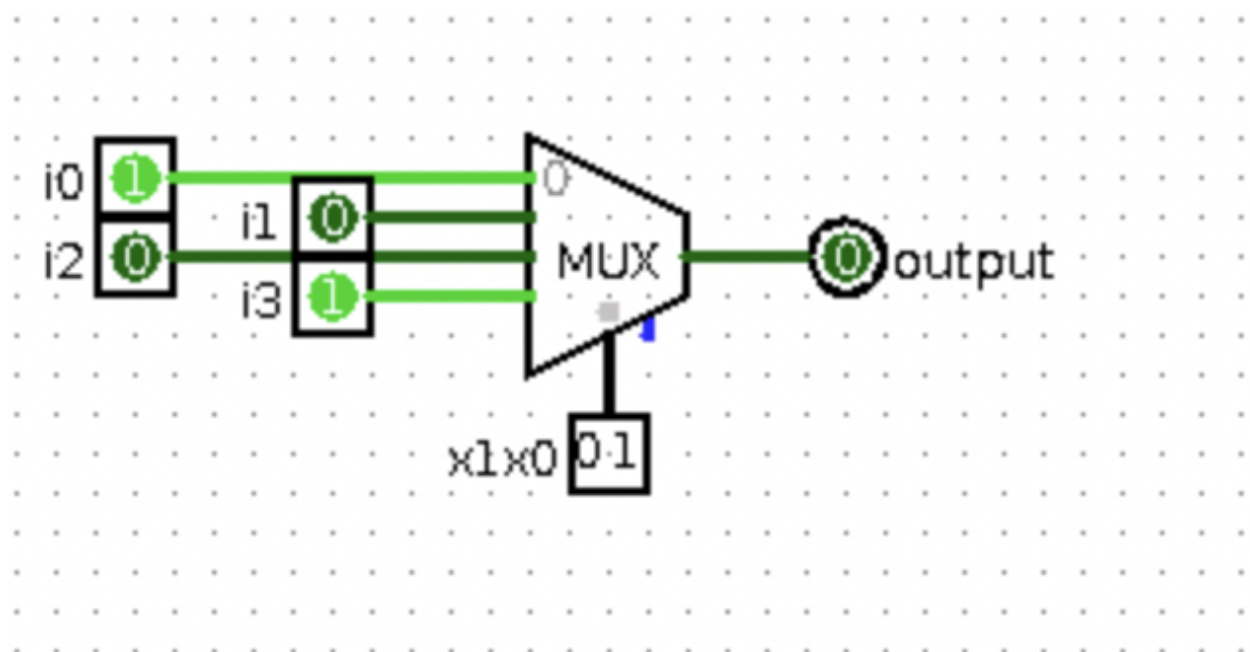
Input 0 to the mux is 1

Input 1 to the mux is 0

Input 2 to the mux is 0

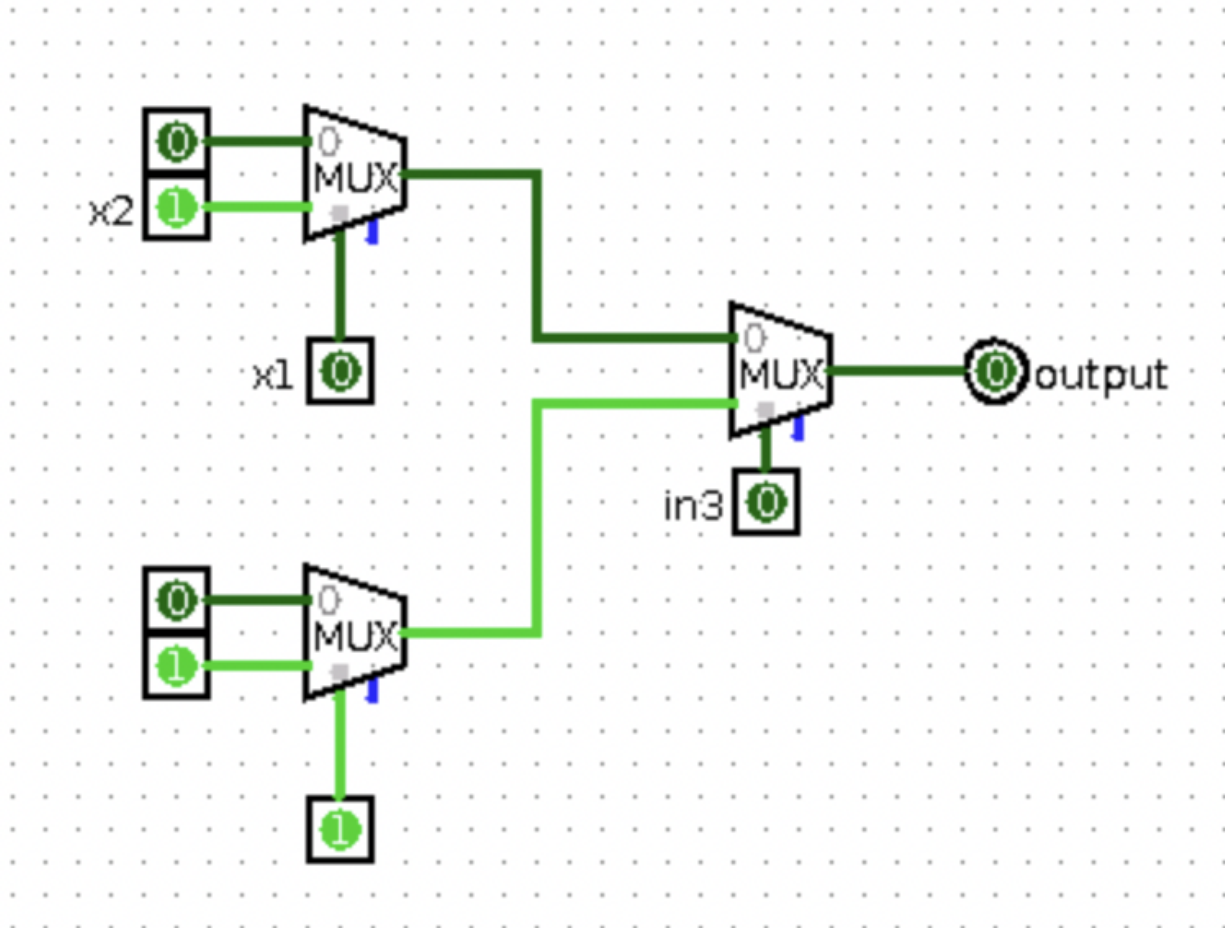
Input 3 to the mux is 1

Shown in logism:



- a) In3 is x1. In2 is 0. In3 is x2. So, when x_1 and x_2 are both 1, the mux outputs 1. Otherwise, the output is 0. In4, In5, and In6 are all 1, so that the high bit for the third mux is always 1. In7 is x3, so when it is low, the output depends on x_1x_2 . If it is high, the output is 1.

Shown in logisim:



- b) In1 is x3. In2 is 0. In3 is x1. So, when x1 and x3 are both 1, the mux outputs 1. In4 is 0. In5 is 1. In6 is x3. So mux outputs 0 if x3 is 0 and 1 if x3 is 1. In7 is x2. So, when x2 is 0, the output depends on x1x3. If x2 is 1, then the output depends on x3.

Shown in logisim:

