

Figure 1: Verso 1.0 do circuito e placa

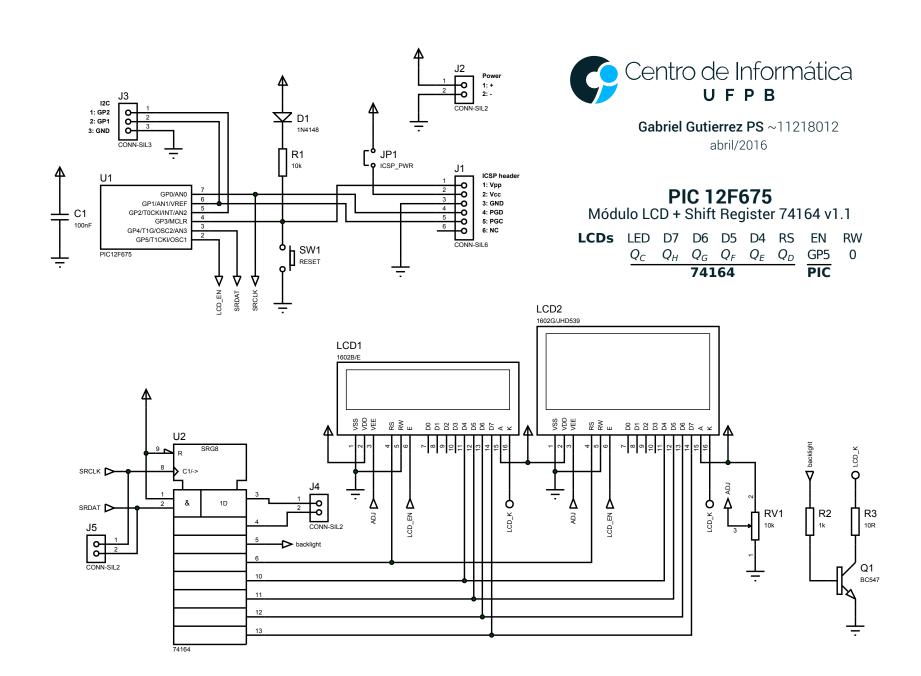


Figure 2: Verso 1.1 do circuito

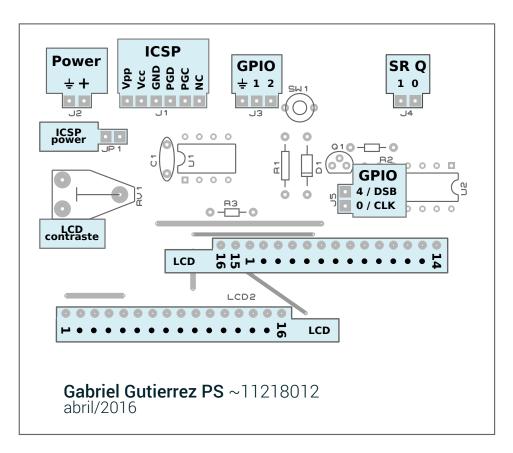


Figure 3: Verso 1.1 da placa