

Absolutely 📖 — here's a **comprehensive yet well-organized detailed summary** of **Chapter 2 (Computer-System Structures)** and **Chapter 3 (Operating-System Structures)** from *Silberschatz, Galvin & Gagne (2002)*.

This version is designed as **study notes** — detailed enough for exam prep, assignments, or class discussions, but written in clear, understandable language.

Chapter 2 – Computer-System Structures

1. Computer-System Architecture Overview

- A **computer system** consists of **CPU**, **main memory**, and **I/O devices**.
 - **Goal:** Achieve concurrent operation between CPU and I/O for efficiency.
 - **Device Controllers:**
 - Each I/O device (disk, printer, etc.) has its own controller.
 - Controller has a **local buffer** used to temporarily hold data.
 - CPU and device controllers work in parallel.
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2. Computer-System Operation

Basic Workflow

1. **CPU** moves data from/to main memory.
2. **Device controller** moves data between device and its local buffer.
3. After completion, controller **interrupts** CPU to indicate I/O completion.

Advantages

- Concurrent execution improves system efficiency.
 - CPU does not waste time waiting for slow I/O.
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3. Interrupts and Their Handling

Definition

- An **interrupt** is a signal that temporarily halts the CPU's current task to attend to another event (usually I/O completion or an error).

Types

1. **Hardware Interrupts** – generated by devices (e.g., keyboard, disk).
2. **Software Interrupts (Traps)** – generated intentionally by programs (e.g., system calls or errors like divide-by-zero).

How It Works

1. The CPU saves the address of the current instruction.
2. Jumps to an **Interrupt Service Routine (ISR)** via **Interrupt Vector** (a table storing ISR addresses).
3. Executes ISR → restores state → resumes interrupted program.

Protection

- Interrupts are disabled while one is being handled to avoid **lost interrupts**.
- The OS is **interrupt-driven** — always responds to events.

4. Input/Output (I/O) Structure

Two Main Approaches

Type	Description
Synchronous I/O	CPU waits until I/O completes before continuing.
Asynchronous I/O	CPU continues execution; I/O completion reported later via interrupt.

System Calls

- Applications cannot directly perform I/O.
- They use **system calls** (e.g., `read()`, `write()`) to request the OS to handle I/O safely.

Device-Status Table

- Maintains details of each device:
 - Device type and ID
 - Memory address
 - Current state (busy, idle, error)
- Updated dynamically as I/O operations occur.

5. Direct Memory Access (DMA)

- Designed for **high-speed I/O**.
 - **DMA Controller** handles data transfers directly between **main memory** and **device buffers**.
 - CPU only involved once per data block (interrupts only once per block).
 - Greatly reduces CPU load and improves performance.
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6. Storage Structure

Main Memory

- Volatile storage (loses data when powered off).
- Directly accessed by the CPU.

Secondary Storage

- Nonvolatile storage with large capacity (e.g., hard disks).
- Data is permanent until explicitly deleted.

Disk Organization

- Disk consists of:
 - **Platters**
 - **Tracks**
 - **Sectors** (smallest addressable storage unit)
 - **Disk Controller:** manages disk operation and interfaces with the OS.
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7. Storage Hierarchy

Level	Example	Speed	Cost	Volatility
1	CPU Registers	Fastest	Highest	Volatile
2	Cache	Very fast	High	Volatile
3	Main Memory (RAM)	Medium	Moderate	Volatile
4	Secondary (Disk/SSD)	Slower	Low	Nonvolatile
5	Tertiary (Tape/Optical)	Slowest	Lowest	Nonvolatile

Caching Concept

- Frequently used data stored in faster memory (cache).
 - Improves performance via locality of reference.
 - Requires **cache consistency** and **management policy** (e.g., LRU, FIFO).
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8. Hardware Protection

To ensure that **user programs do not interfere** with the OS or other programs, multiple levels of protection are required.

A. Dual-Mode Operation

- **User Mode:** Executes user applications.
- **Kernel Mode (Monitor Mode):** Executes privileged OS operations.
- **Mode Bit:** Indicates the current mode.
 - 0 = kernel mode
 - 1 = user mode
- When an interrupt occurs, the system automatically switches to kernel mode.

Example:

- **Intel 8088:** No dual mode (MS-DOS lacked protection).
 - **Intel 80486:** Supports dual mode (used in Windows NT, OS/2).
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B. I/O Protection

- I/O instructions are **privileged**.
 - Prevents user programs from:
 - Directly controlling hardware.
 - Modifying interrupt vectors.
 - All I/O requests go through **system calls**.
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C. Memory Protection

- Prevents one process from accessing another's memory or OS memory.
- Implemented using:
 - **Base Register:** Smallest legal physical address.
 - **Limit Register:** Range of allowed addresses.

- Memory access outside this range → **trap** or **protection fault**.
 - Base/limit registers are **privileged instructions**.
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D. CPU Protection

- **Timer** prevents CPU monopolization by a single process.
 - Timer decrements every clock tick; when it reaches zero → interrupt → OS regains control.
 - Used for:
 - Time-sharing.
 - Preventing infinite loops.
 - Tracking system time.
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9. Network Structure

Types of Networks

Type	Description
LAN (Local Area Network)	High-speed network in small areas (e.g., campus, office).
WAN (Wide Area Network)	Connects computers over long distances (e.g., Internet).

- Networks allow **resource sharing** and **distributed computing** among multiple systems.
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Chapter 3 – Operating-System Structures

1. Common System Components

1. **Process Management**
2. **Memory Management**
3. **File Management**
4. **I/O System Management**
5. **Secondary Storage Management**
6. **Networking**
7. **Protection System**
8. **Command Interpreter**

2. Process Management

- **Process:** Program in execution.
- **Resources Required:** CPU time, memory, files, I/O devices.

OS Responsibilities

- Create, schedule, and terminate processes.
 - Synchronize processes to prevent conflicts.
 - Enable **Interprocess Communication (IPC)**.
 - Handle **deadlocks** and **resource sharing**.
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3. Memory Management

- **Main memory:** Fast, volatile storage.
 - OS keeps track of:
 - Which memory locations are used.
 - Which process owns each part.
 - Handles **allocation** and **deallocation** as processes start and end.
 - Decides which processes to load when memory is full.
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4. File Management

- **File:** Collection of related information (data, program, etc.).
 - **OS Responsibilities:**
 - File & directory creation/deletion.
 - Provide access methods (open, read, write, close).
 - Map files to physical locations.
 - Handle file backups on stable media.
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5. I/O System Management

- Contains:
 - **Buffer caching** to improve efficiency.
 - **Device-driver interface** to abstract hardware.
 - **Specific drivers** for hardware (printers, disks, etc.).

6. Secondary-Storage Management

- Provides permanent storage for programs and data.
 - **OS Responsibilities:**
 1. **Free-space management**
 2. **Storage allocation**
 3. **Disk scheduling** (optimizing access time)
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7. Networking / Distributed Systems

- System where processors are connected via a **communication network**.
 - Each processor has its **own local memory** and **no shared clock**.
 - Uses communication **protocols** for coordination.
 - **Advantages:**
 - Speed-up computation.
 - Increased data reliability.
 - Shared resource access.
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8. Protection System

- **Goal:** Prevent unauthorized access to system resources.
 - **Must:**
 - Identify users/programs.
 - Define access rights.
 - Enforce rules for safety and privacy.
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9. Command-Interpreter System (Shell)

- Interface that reads user commands and executes them.
- Commands deal with:
 - Process management
 - I/O operations
 - File and memory access
 - Security and networking

Examples

- **Command-Line Interface (CLI)** – Windows Command Prompt
 - **Shell (UNIX/Linux)** – `bash`, `sh`, `zsh`
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10. Operating System Services

Service	Description
Program Execution	Load, run, and terminate user programs.
I/O Operations	Provide secure interfaces for I/O.
File Manipulation	Create, read, write, and delete files.
Communication	Between processes (local or remote).
Error Detection	Detect and handle hardware/software errors.

Additional System Functions

- **Resource Allocation** – share CPU, memory, and I/O fairly.
 - **Accounting** – track usage for billing/statistics.
 - **Protection** – ensure controlled access to system resources.
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11. System Calls

- Interface between **user programs** and **OS kernel**.
- Often written in **assembly** or called from **high-level languages** like C.

Parameter Passing Methods

1. Pass in **registers**
2. Pass **table address** in a register
3. Pass parameters on the **stack**

Categories of System Calls

Category	Examples
Process Control	create, terminate, wait, execute
File Management	open, read, write, close
Device Management	request, release, attach
Information Maintenance	get/set system info
Communication	send, receive, attach remote devices

12. System Programs

Provide user-level tools for managing the system:

1. File manipulation (copy, delete, list)
 2. Status information (`top`, `ps`)
 3. File modification (editors)
 4. Programming support (compilers, debuggers)
 5. Program execution (loaders)
 6. Communication (email, FTP)
 7. Application programs (utilities, games)
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13. System Structure Models

MS-DOS

- Minimal structure.
- Limited modularity (all code runs together).
- Efficient but insecure.

UNIX

- Two parts:
 - **System programs**
 - **Kernel**
- Kernel provides:
 - File system
 - CPU scheduling
 - Memory and device management

Layered Approach

- OS divided into layers:
 - Layer 0 = Hardware
 - Layer N = User Interface
- Each layer uses only lower-level functions → modular, secure, and maintainable.

Microkernel

- Moves most system functions (drivers, servers) to **user space**.
- **Advantages:**
 - Easy to extend and port.
 - More reliable (less kernel code).

- Better security.
 - Communication via **message passing**.
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14. Virtual Machines (VMs)

Definition

A **virtual machine** is a software emulation of a hardware computer, providing an environment for running programs.

Features

- Each VM behaves as an independent system.
- OS creates the illusion of multiple CPUs and memory spaces.

Examples

- **Java Virtual Machine (JVM):**
 - Runs platform-independent Java bytecode.
 - Consists of:
 - Class loader
 - Class verifier
 - Runtime interpreter
 - JIT compiler (improves performance)
- **Intel 8086 VM:** Emulates older hardware on newer systems.

Advantages

- Complete isolation and protection.
- Excellent for OS development/testing.
- Portability — code can run anywhere.

Disadvantages

- Complex implementation.
 - Resource duplication — no direct sharing between VMs.
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15. System Design & Implementation

Goals

- **User Goals:** Easy, reliable, secure, fast.
- **System Goals:** Efficient, flexible, maintainable.

Mechanisms vs. Policies

- **Mechanism:** How something is done.
- **Policy:** What is done.
- Separation increases flexibility.

Implementation

- Modern OS written in **high-level languages** (C, C++).
 - Easier to read, debug, and port to other hardware.
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16. System Generation (SYSGEN) & Booting

SYSGEN

- Configures the OS for specific hardware at installation time.

Booting

- Process of loading OS into memory and starting execution.
 - **Bootstrap Program:**
 - Stored in ROM.
 - Locates and loads the OS kernel into memory.
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Would you like me to **combine Chapters 1–3 into a single formatted PDF or Word study guide** (with organized headers, color-coded sections, and page-ready formatting)? It would make for a clean, printable reference for exams.