# Imperial College London

# Parallelization of iSALE Using OpenMP

# By

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**Project Plan Prepared for the ACSE-9 Final Project** 

# **Rationale**

iSALE (impact-SALE) is a well-established shock physics code (written in Fortran 95) that has been developed and used for over two decades to simulate impact cratering (e.g. the collision of an asteroid with a planetary surface and related processes). The code has been benchmarked against other 3D impact simulation codes and validated by comparison of simulation results with laboratory-scale impact experiments. Two and three-dimensional versions of the software exist, and the 3D version has included parallel optimization, achieved using simplistic domain decomposition and MPI.

Since the iSale has been used for more than twenty years and used in different fields, improve the performance and the efficiency of the software has become the priority. Currently, for the most challenging or higher spatial resolution simulations, the running time of the serial code can be up to a month or more, which is a significant time cost. With the rapid development of computer architectures in recent years, the processing power of the computer is getting stronger and stronger and can provide more creative ways to improve the performance of the code. Parallelization is one of the most efficient ways that comes with the development of computer architectures. As mentioned above, currently, this software has two and three-dimensional versions, but only the 3D version includes parallel optimization.

Thus, this project has two main objectives. First, the project aims to test, extend and complete iSALE2D parallelization using OpenMP, and provide an initial template which will be used in further development. Since the iSALE solution algorithm involves a series of big and complex loops, adding parallel programming to the code has the potential to reduce the run time substantially. Second, the project will explore the three different parallel scheduling methods which are dynamic, static and guided, and combine with other parameters such as chunk size to find the best combination for a specific test case, then get the optimal performance of the paralleled iSALE.

# **Background**

In this project, I will use the iSALE shock physics code (Wunnemann et al., 2006), which is an extension of the SALE hydrocode (Amsden et al., 1980). To simulate hypervelocity impact processes in solid material, SALE was modified to include an elasto-plastic constitutive model, fragmentation models, various equations of state (EoS), and multiple materials (Melosh et al., 1992; Ivanov et al., 1997). More recent improvements include a modified strength model (Collins et al., 2004), a porosity compaction model (Wunnemann et al., 2006; Collins et al., 2011) and a dilatancy model (Collins et al., 2014).

The partial differential equations solved by the basic SALE program are the compressible Navier-Stokes equations and the mass and internal energy equations, the finite difference method is applied (Amsden et al., 1980). Figure 1 shows the flow diagram for iSALE2D (Collins et al., 2016).

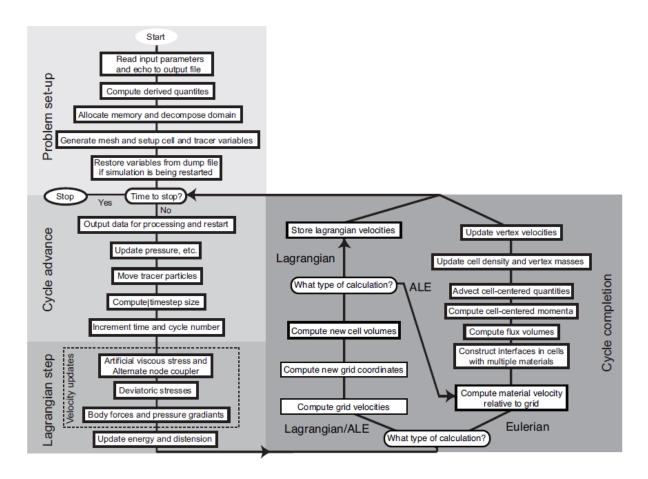


Figure 1. Flow Diagram of iSALE2d

With developments in recent years, the iSALE program includes models which are rheologic models, porosity models, strength models, thermal softening model, damage model and dilatancy model (Collins et al., 2016), used to solve more specific situations, such as porous compaction of impact crater formation (Wunnemamm, Collins, G., and Melosh, H., 2006). The explicit time step and limitations make the program more straightforward. Furthermore, there is no external library used in the iSALE program, which reduces the software dependence on external resources.

This project will be updated on a new brunch of GitHub iSALE repository, which will be easy to check any progress and helpful for further development,

# Work Plan

The work plan of this project can be separated into six main parts:

- VTune Profiling
- Code Refactoring
- First Parallel Evaluation
- Parallelization for Other Subroutines

- Evaluation of Parallel Speed Up
- Final Report

The timeline and expected deliverable for each part are listed in the Gantt chart below.

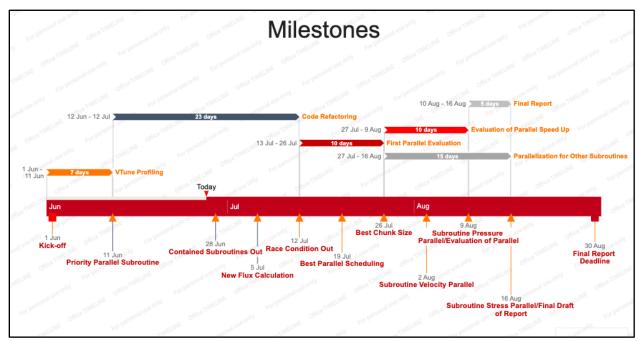


Figure 2. Gantt Chart for The Project with Expected Finish Time and Deliverable

### **VTune Profiling**

Code profiling is the first and significant part for the project and can be used to point out a clear direction of the project. Although the main objective is to add parallelization to the code, parallel all loops or all subroutines is not an ideal way to improve the efficiency of the code. Because running processors also cost time, if a loop is not that complicated, adding parallel will have a negative effect. The VTune amplifier is a commercial application for software performance analysis. It both supports the Mac OS and Linux system and will be used to run the whole code, timing each subroutine and generate a configuration analysis report. It will provide me with a list of time cost on each subroutine, which helps me figure out which subroutine I should parallel first, and which one is the end target of parallelization.

#### **Code Refactoring**

Since I get the subroutines which cost most run time from the last section, I can consider adding parallelization to these subroutines. However, the code refactoring is required because the original structure of the code has some problems with parallel programming. The structure problems can be divided in:

#### 1. Contained Subroutines

In the current code, there are some contained subroutines involved in a main subroutine, which will affect the efficiency of the code and is hard to add parallel. What I am going to do is to

separate the subroutines outside the large one, then I can add parallel to both small and large subroutine.

#### 2. Race Condition

Race condition will exist on the node after adding parallelization. In the current code structure, data from different cells will pass to the neighboring node, and the race condition will cause data overwriting on the node. I am going to use orders in OpenMP or rewrite the code to avoid the race condition.

## 3. Data Transportation in Specific Direction

The data in some loops need to be transported in a specific direction, for example, from left to right. Adding parallelization will mess up the specific orders of the data transportation. What I am going to do is to refactor the loops contain this situation to avoid any conflicts.

#### **First Parallel Evaluation**

- 1. Parallel Scheduling Static, Dynamic, Guided
- 2. Chunk size

In this part, I will test different parameters in parallelization with different sample tests to determine the best combination for each sample test. Since there are three main parallel scheduling methods I can use in the !\$comp parallel schedule() order, I will test each of them with different chunk size. The best scheduling method and chunk size will be varied with different sample tests. All combinations will be recorded and used in the final test.

### **Parallelization for Other Subroutines**

After dealing with the prior subroutine, I will follow the same strategy and use the same parameters to parallel other subroutines. The main subroutines include the subroutine for advection, velocity, pressure and shear stress. One of them will become the priority of parallelization after profiling, and others will be dealt with in this part.

### **Evaluation of Parallel Speed Up**

I will evaluate the speed up of parallelization in this part. To achieve the purpose, I will test the code by using an increasing number of threads and see if the performance is getting better. Meanwhile, since there are two different nodes available, which are CX nodes and AMD nodes, I will use both nodes to run the tests as well and record the performance of the code. Furthermore, I will record the run time of a sample test with different resolutions. I believe the parallelization will increase the efficiency of the code.

### **Final Report**

After finishing implementing all parts above, I will work on the final report. The report will contain efforts I will make in this project, results analysis and conclusion.

# **Progress So Far**

# Profiling

As mentioned above, the purpose of profiling is to run the test on the Intel VTune Amplifier, get the list of run time, find which subroutine costs most processing time and parallel this subroutine first. After running the most challenging sample test, I got the report below:

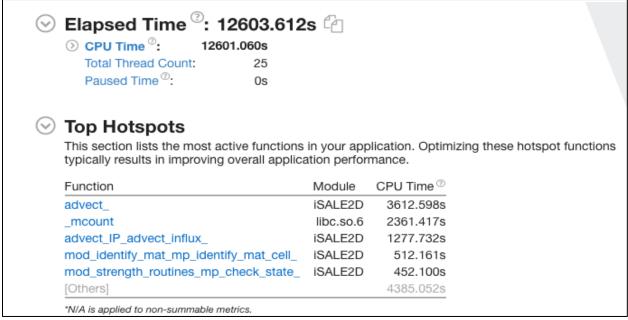


Figure 3. Bottlenecks and Hotspots Check

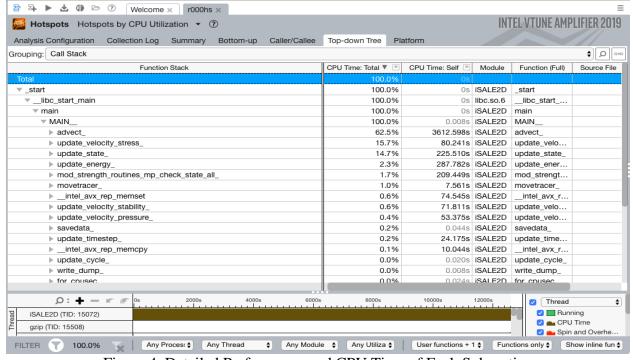


Figure 4. Detailed Performance and CPU Time of Each Subroutine

Figure 2 and 3 show the top hotspots in the software. The subroutine *advect* costs 62.5% of the total processing time. Thus, the subroutine *advect* will be the priority for parallel programming. Meanwhile, the subroutine *update\_velocity\_stress* and *update\_state* costs 15.7% and 14.7%, total 30.4% of the run time. These three subroutines cost more than 90% of the run time. The next one is the subroutine *update\_energy*, and it only cost 2.3%. Thus, I will start from subroutine *advect*, and end at the *update\_state*.

#### • Code Refactoring – Remove Contained Subroutines

As mentioned above, there are some contained subroutines in the main subroutine, which will be hard to add parallel programming on it. I moved them out, defined all used parameters at the beginning of the subroutine and put all input parameters into the brackets in the subroutine line.

The original iSALE already has some existing framework tests for testing the correctness of the code. Thus, I used these tests to check my work. With any small changes in the code, I run the test to check if the software with the updated code could compile successfully and gave me all passed check results for each parameter. The difficulty of this procedure was the failure test did not show any error report to help me correct the code. Thus, I had to explore and run the test with even a really small change every time, which cost some time to finish this part. The part has been finished; the deliverable is the code without contained subroutines.

### • Code Refactoring – Race Condition

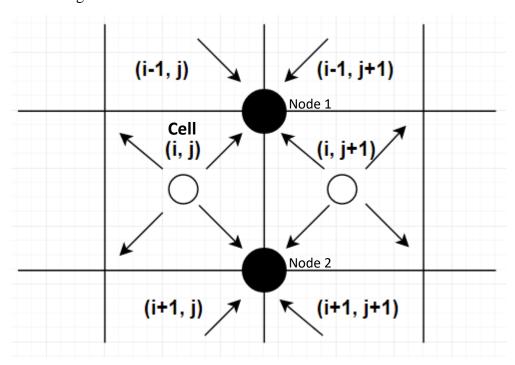


Figure 5. Sample Example of Mesh Grid and Race Condition

The plot above shows that in the current code, each node will receive data from its four neighbor cells. If the code is serial, the data will be passed to the point by following the orders of increasing i and j. However, after adding parallelization, data in the cells will be processed on multiple

processors simultaneously, and send to the node at the same time, which means the information from the cell (i, j+1) will overwrite the information from the field(i, j).

I will add the !\$omp parallel reduction order in the loop to solve this problem. This order allows multiple processors send and receive data sequentially, each processor saves a copy of all updated parameters, and at the end of the order, use a specific symbol to connect the original parameters and the updated parameters, and finally update the shared parameters to remove this potential conflict. Another way is to rewrite the loop. Each field will be updated by receiving information from its four neighbor points. I will try the first method to evaluate the performance of the parallelization. If the results are not correct or the efficiency does not increase apparently, I will try to use the second method. Since the method has been found, this part will be finished shortly.

## • Code Refactoring – Data Transportation in Specific Direction

The original data is in a 2D frame, to simply explain the problem, I will use a 1D frame here. The problem can be explained by using the following pseudo-code:

```
do i=1, nx
   if(1.eq.1)then
      fr = 0
      volflux_l(i) = fr
      volflux_r(i) = calculate_right_flux()
      fr = volflux_r(i)
end do
```

The pseudo-code shows that in each iteration, the volume flux on the left side equals to the number of fr, then the volume flux on the right will be updated with the number from the function  $calculate\_right\_flux$ , and finally, update the fr equaling to the volume flux on the right side. The structure shows the data was passed from the left to the right. However, if I add the parallelization to this, each processor will process one iteration, and I cannot promise the data can be passed in the specific order because the processing time of each processor will be varied.

To solve this problem, I will update the *fr* value before update the volume flux on both the left and right side. The method I will use is explained by using the following pseudo-code:

```
do i=1, nx
    fr(i) = calculate_right_flux(i)
end do
do i=1, nx
    volflux_l(i) = fr(i-1)
    volflux_r(i) = fr(i)
end do
```

The new code can be paralleled easily. I can add a parallel section in the first loop without changing anything in the second loop. Since the function *calculate\_right\_flux* is the most expensive part, this method will reduce the run time significantly.

# **Reference**

iSALE includes extensions, corrections, and enhancements to the original SALE code made by several workers since the early 1990s. Melosh began improvements to the code by implementing an elasto-plastic constitutive model in tandem with the viscous model and incorporated the Grady-Kipp fragmentation algorithm and several equations of state for impacts, including the Tillotson equation of state.

Then, Melosh's primarily-Lagrangian version of the original SALE was improved to include a wider range of possible rheologic models (released as SALES-2) and used to simulate impact crater collapse. Wünnemann substantially rewrote large parts of SALE, incorporating the improvements of Ivanov and other advances such as a third target material. Further important refinements to the constitutive model were made by Collins, Melosh and Ivanov. Wünnemann, Collins and Melosh incorporated the  $\varepsilon$ - $\alpha$  porous-compaction model into iSALE.

More recently, based on previous work of Schmalzl, Dirk Elbeshausen implemented in iSALE custom made in-memory compression routines for efficient data output and access. Thomas Davison and Dirk Elbeshausen improved iSALE's material and tracer particle assignment routines to enable easy set-up of multiple simple-shaped "objects" that can be combined to form complex layered impactor and target bodies.

Gareth Collins added a routine to construct a self-consistent central gravity field for simulation of giant impacts onto spherical planetary bodies. Collins also made modifications to interface reconstruction method to reduce numerical diffusion of low-volume fraction materials in problems with more than two materials. Collins has also made refinements and improvements to the porous compaction model for simulating impacts in high-porosity target materials.

Since 2004, iSALE has been co-developed by Collins and Wünnemann and since 2006 also by Dirk Elbeshausen and Thomas Davison. The software was restructured and rewritten in Fortran 95 by Dirk Elbeshausen. He provided the code with more clearly arranged input-files and enabled an easier usage and extension of the code. Dirk Elbeshausen developed several tools for iSALE, such as a three-dimensional visualization and animation software (VIMoD).

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