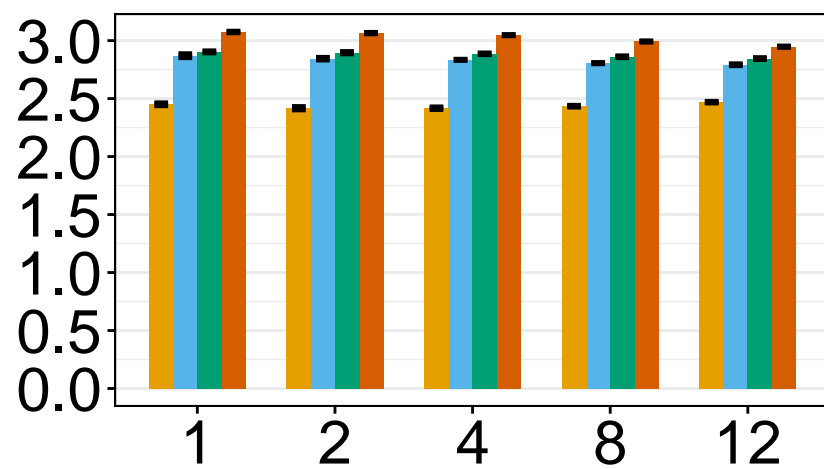


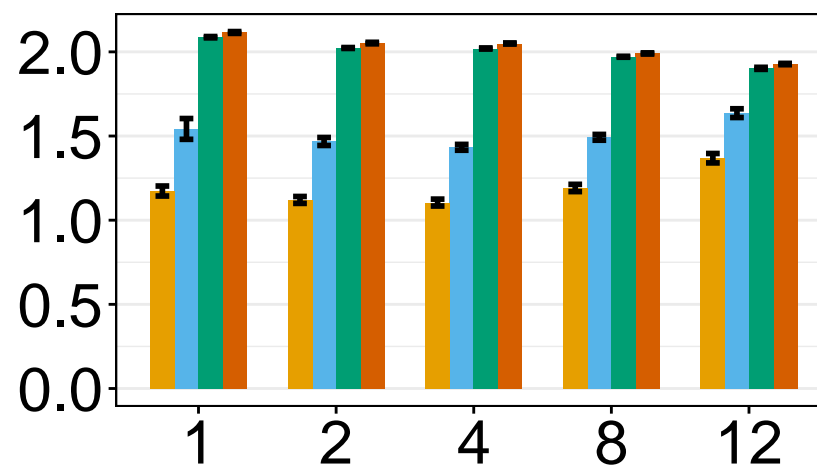
■ No Prefetcher ■ L1 Prefetcher ■ L2 Prefetcher ■ L1 + L2 Prefetcher

Instructions per Cycle

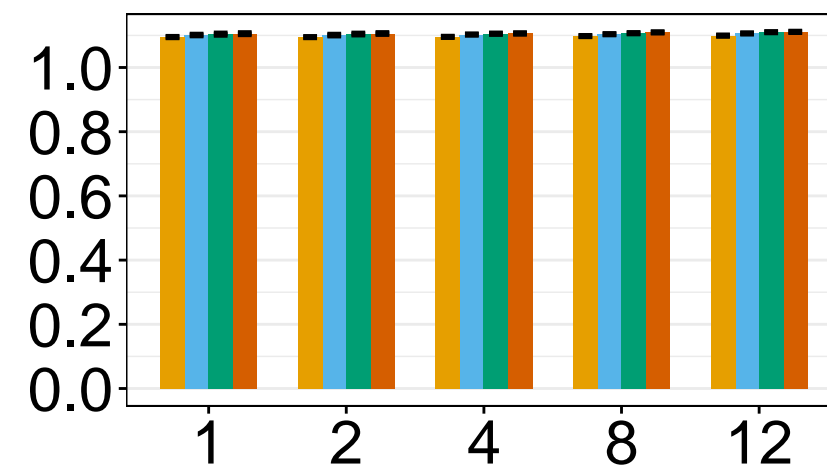
BT



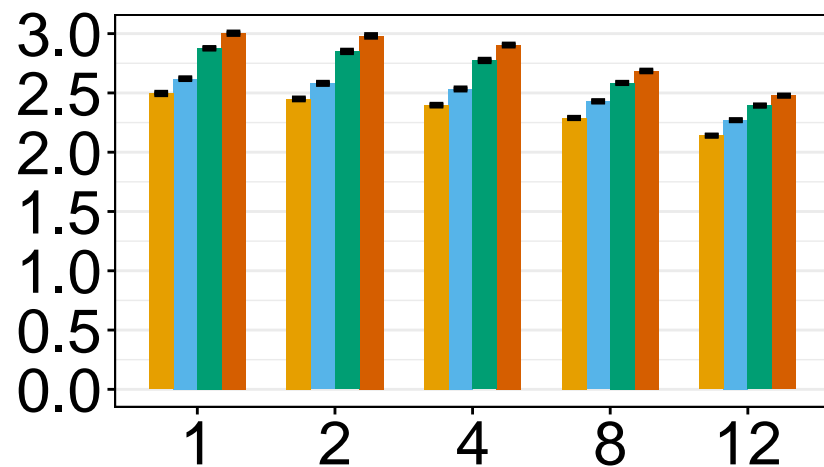
CG



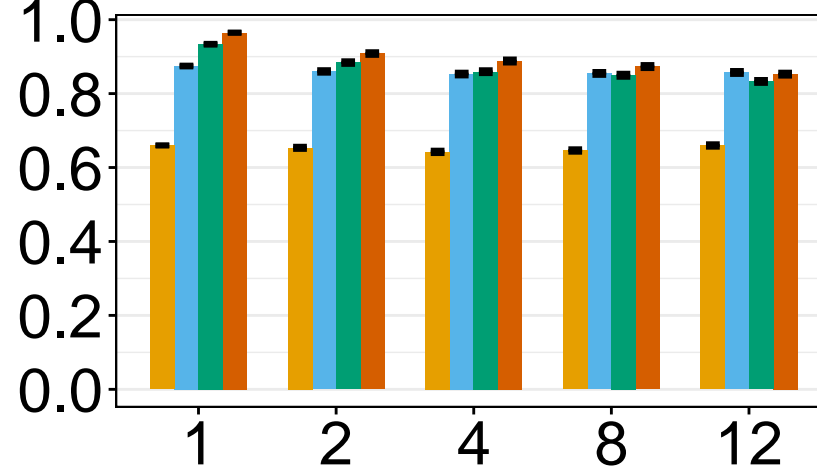
EP



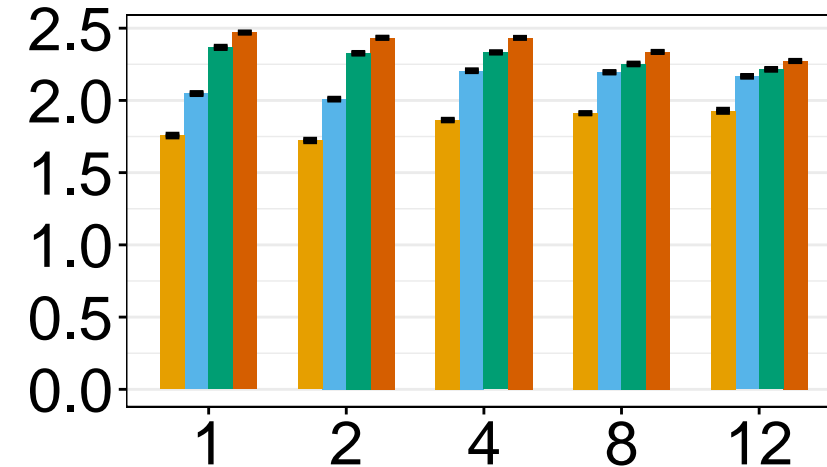
FT



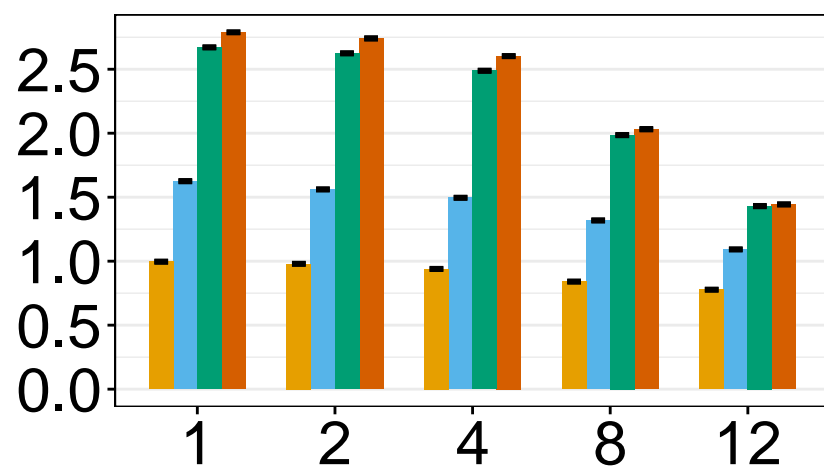
IS



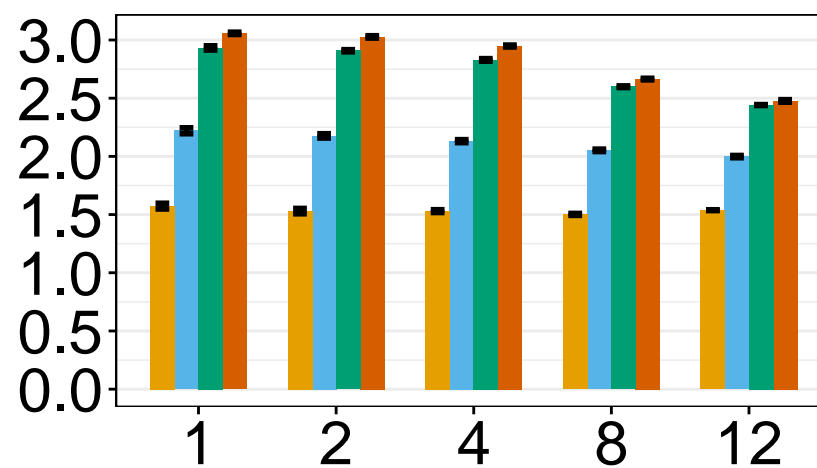
LU



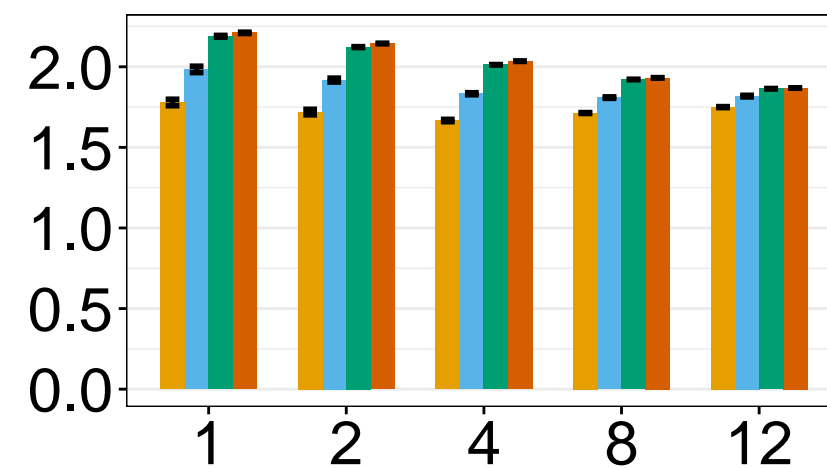
MG



SP



UA



Number of Threads