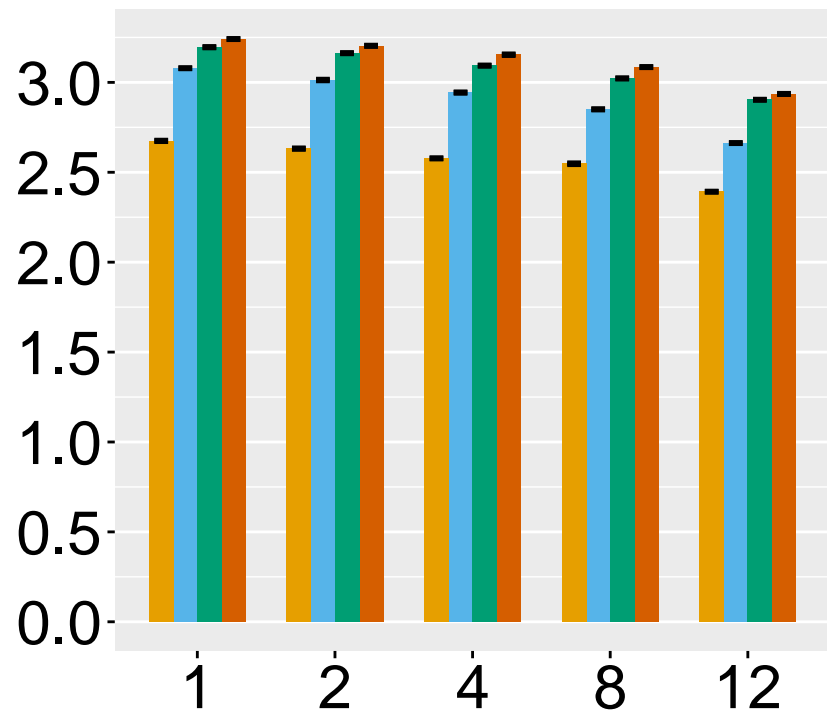


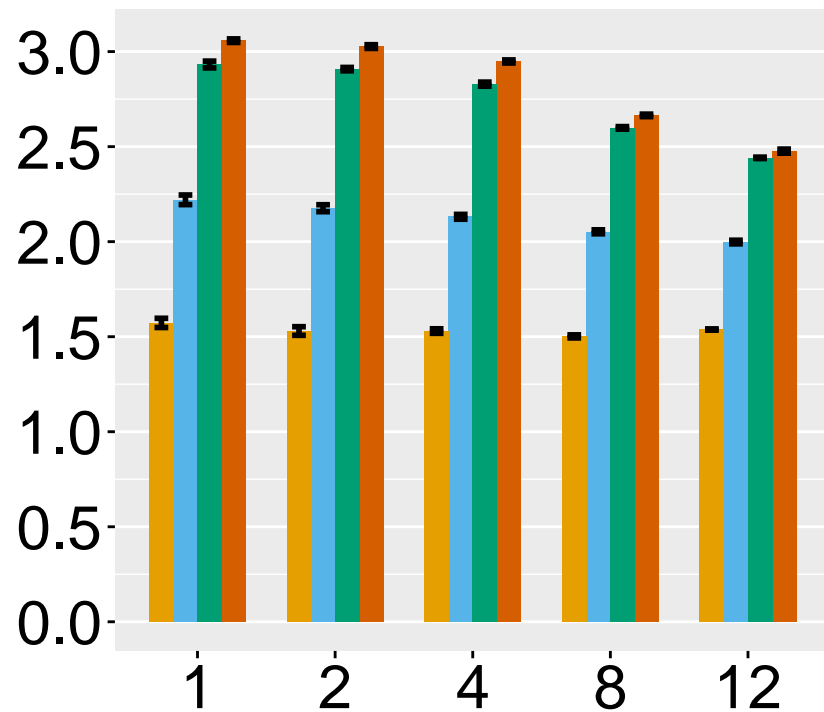
■ No Prefetcher ■ L1 Prefetcher ■ L2 Prefetcher ■ L1 + L2 Prefetcher

Instructions per Cycle

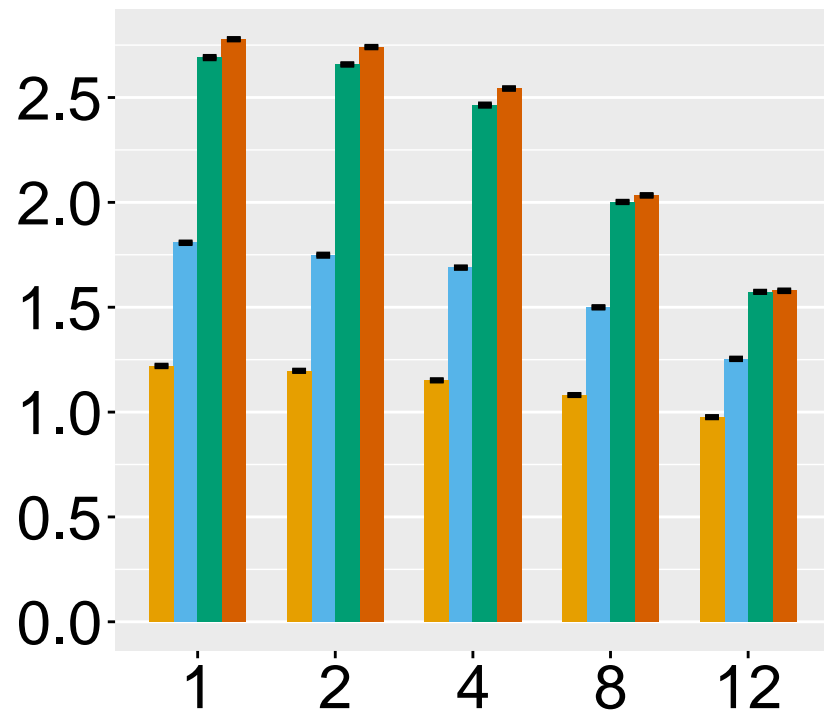
Class W



Class A



Class B



Number of Threads