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TO: _____

LED DOT MATRIX MODULE

MODEL : SLM1608MD2

SPEC No.	RESQ-22003	Issue Date	MAY 30,1997	DIV.	Date	Signature
REV. No.	DATE	REV. No.	DATE	Written		
3.0	NOV.01,1993			Check		
				Approval		

1. MODEL : **SLM1608MD2** (V.3.0)

2. SPECIFICATION

DISPLAY COLOR	RED, GREEN, AMBER
DOT SIZE (㎜)	7
DOT PITCH (㎜)	8
NO. OF DOTS	256 (16 x 16)
DIMENSION (㎜)	128 x 128 x 20
WEIGHT (g)	160

3. ELECTRICAL CHARACTERISTICS

- ABSOLUTE MAXIMUM RATING(Ta=25℃)

ITEM	SYMBOL	CONDITION	UNIT
SUPPLY VOLTAGE	Vcc	5.25	V
CLOCK FREQUENCY	f	40	㎒
INPUT VOLTAGE	Vin	-0.3 - Vcc+0.3	V
OPERATION TEMPERATURE	Topr	-10 - +45	℃
STORAGE TEMPERATURE	Tstg	-20 - +70	℃

* LED surface temperature must be maintained below 60℃. So loading the ventilation fan is recommendable.

- ELECTRICAL CHARACTERISTICS

ITEM	SYMBOL	RATING / CONDITION	UNIT
SUPPLY VOLTAGE	Vcc	5.0 ± 0.25	V
CLOCK FREQUENCY	f	MAX. 40	㎒
CURRENT CONSUMPTION	I	MAX. 2.8	A
FRAME FREQUENCY	Ffr	70 - 100	㎐

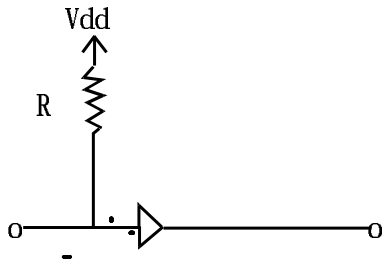
4. RECOMMENDABLE OPERATING CONDITIONS

ITEM	SYMBOL	CONDITION	UNIT
SUPPLY VOLTAGE	Vcc	5	V
OPERATING TEMPERATURE	Topr	0 - 40	℃

5. INPUT LEVEL

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT "L"	Vil	-	-	0.8	V
INPUT "H"	Vih	2.2	-	-	

* All input is pulled up 50 kΩ.

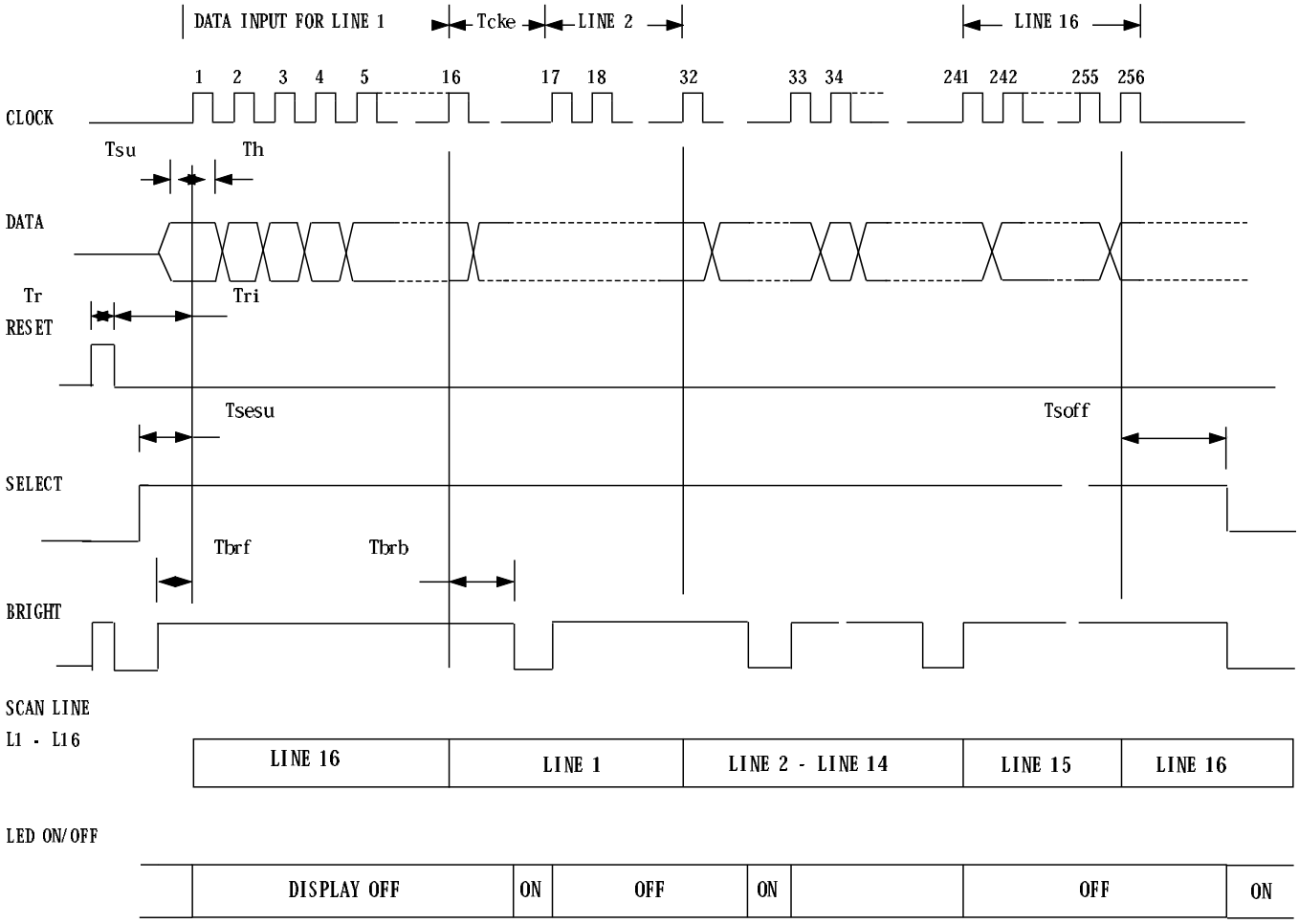


INPUT BUFFER (R= 50 kΩ)

6. FUNCTION

PIN NAME	FUNCTION DESCRIPTION
Vcc	Power supply of the module
GND	Ground of the module
RED DATA	Data input for red color
GREEN DATA	Data input for green color
SELECT	Data input control "H" : Data input and display "L" : Data input disable and display memorized data
BRIGHT-WRITE	Total brightness control data write (48bit pattern) "H" : Data write "L" : normal display
BRIGHT-CLOCK	Shift clock of total brightness control data pattern (5 - 20㎒ continuous clock)
BRIGHT	Display on / off control "H" : Display off "L" : Display on Brightness can be controlled using pulse width.
CLOCK	clock signal for data input and display
RESET	For initializing counter value "H" : counter initializing "L" : normal operation The memorized data in the module are not cleared.

7. TIMING CHART



(Ta=25℃ , Vcc=5V)

NOTE 1 : BRIGHTNESS IS DECIDED BY WIDTH OF Tcke.

CHARACTERISTICS	SYMBOL	MIN.	MAX.	UNIT
CLOCK CYCLE	T	-	25	ns
DATA SETUP TIME	Tsu	10	-	ns
DATA HOLD TIME	Th	10	-	ns
CLOCK ENABLE TIME	Tcke	NOTE 1	-	ns
RESET INPUT TIME	Tri	10	-	ns
RESET TIME	Tr	20	-	ns
SELECT SETUP TIME	Tsesu	10	-	ns
SELECT OFF TIME	Tsoff	10	-	ns

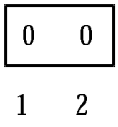
8. OPTICAL CHARACTERISTICS

PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNIT
LUMINANCE	RED GREEN	Lv	70 70	90 90	120 120	cd/㎡
PEAK EMISSION WAVELENGTH	RED GREEN	λp	- -	630 565	- -	nm
SPECTRUM RADIATION BANDWIDTH	RED GREEN	△λ	- -	35 30	- -	nm

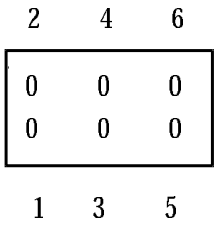
* DUTY RATIO : 1/16 , FRAME FREQUENCY : 100 Hz

9. CONNECTIONS

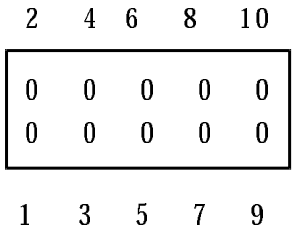
CN1



CN2



CN3



CN1

1	GND
2	Vcc

CN2

1	NO CON
2	GND
3	NO CON
4	GND
5	SELECT
6	GND

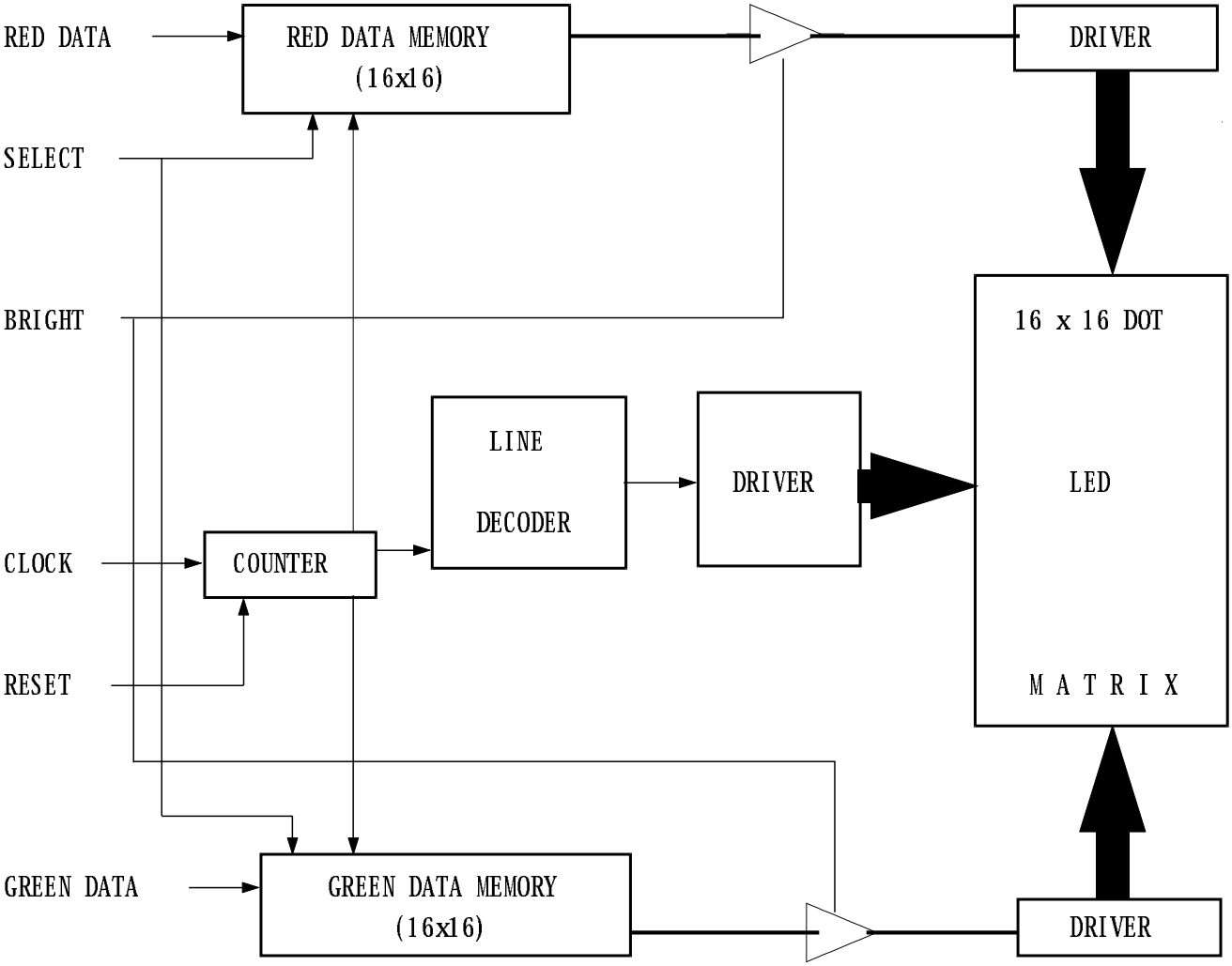
CN3

1	GND	2	RED DATA
3	GND	4	GREEN DATA
5	GND	6	CLOCK
7	GND	8	BRIGHT
9	GND	10	RESET

CONNECTOR TYPE NAME (TO IMPROVE, IT IS ABLE TO CHANGE)

CN1 : DIF3FB-2P-2.5C2 (HIROSE KOREA., LTD.)
CN2 : A71308-0106N (HIROSE KOREA, LTD.)
CN3 : A71308-0110N (HIROSE KOREA., LTD.)

10. BLOCK DIAGRAM



* PIN ASSIGN

CONNECTOR 2

BRTCLK	0 0	GND	BRTCLK : 5-20 ㎒ CONTINUOUS CLOCK SIGNAL
BRTWRT	0 0	GND	BRTWRT : WHEN "HIGH" LEVEL DATA ARE STORED
SELECT	0 0	GND	SELECT : REFER TO "6. FUNCTION"

* METHOD OF BRIGHT-LEVEL DATA SET-UP

LEVEL BIT PATTERN DATA INPUT PATH : SAME PATH(PIN) AS DISPLAY DATA INPUT

MODULE SELECT : MODULE OF A MATCHED POINT WHICH IS ON THE BRIGHT & SELECT SIGNAL MATRIX. THAT IS , THE MODULE IS SELECTED WHEN BOTH OF SIGNALS "BRIGHT" AND "SELECT" ARE HIGH SIMULTANEOUSLY.

BRIGHT-LEVEL DATA SET-UP : WHEN SIGNAL "BRT-WRT" IS "HIGH" , THE LEVEL DATA OF RED AND GREEN ARE MEMORIZED IN TWO 48-BIT SHIFT REGISTORS RESPECTIVELY ACCORDING TO THE PULSE OF "BRT-CLK". AFTER SETTING UP DATA, THE SIGNAL "BRTWRT" MUST BE LOW DURING NORMAL DISPLAY RUN.

* DISABLE BRIGHTNESS-LEVEL CONTROL FUNCTION
PLEASE FIX THE SIGNAL "BRT-WRT" TO HIGH.

* ADJUSTMENT OF BRIGHT-LEVEL IN EACH COLOR AND MODULE

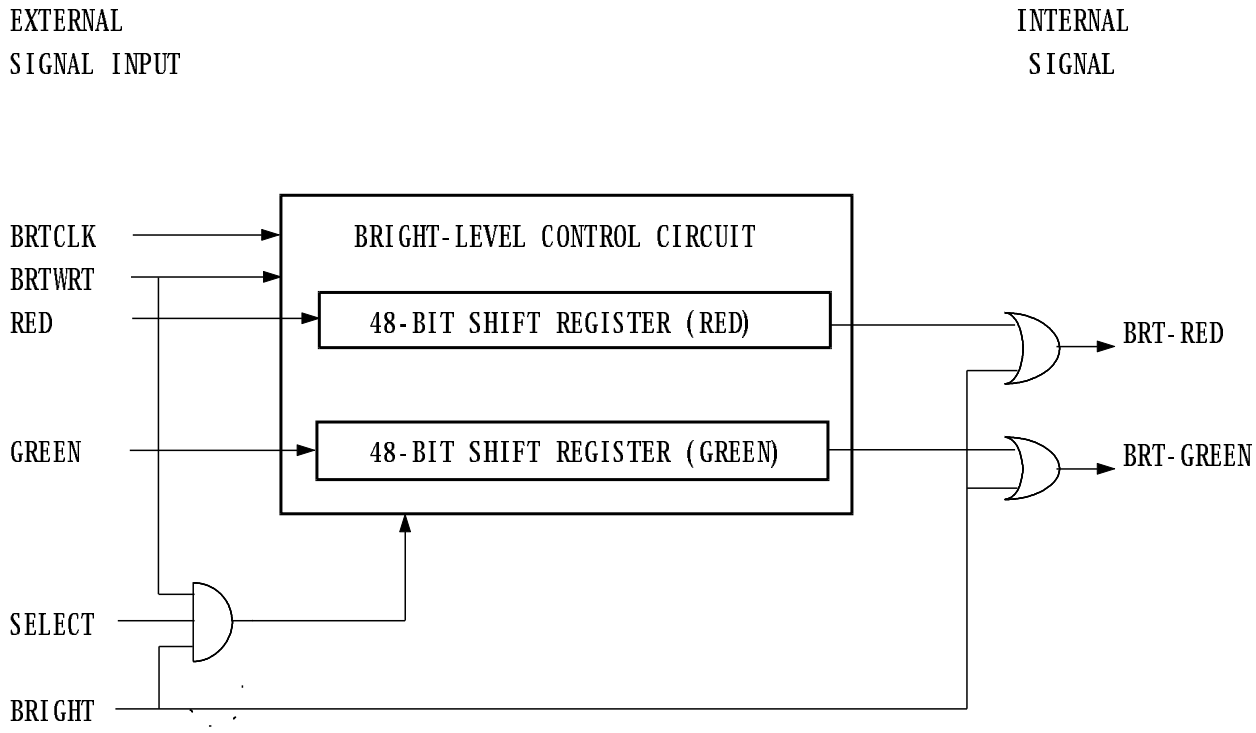
- DESIGN 48-BIT LENGTH TEMPLATE PATTERN FOR EACH BRIGHT-LEVEL CONTROL STEP.

- FOR CONTROLLING SENSITIVITY OF THIS FUCTION, ADJUST THE SPEED OF CONTINUOUS CLOCK SIGNAL "BRT-CLK"

- TO ADJUST BRIGHT-LEVEL OF INDIVIDUAL COLOR OR MODULE, A CERTAIN KIND OF H/W AND S/W TOOL IS REQUIRED.
  DURING THE ASSEMBLY AND TESTING A DISPLAY BOARD, A MAN CAN TRY VARIOUS LEVEL FOR INDIVIDUAL MODULE AND COLOR TO REDUCE THE BRIGHTNESS UNBALANCE BETWEEN MODULES.
  AFTER TUNING OF UNIFORMITY IN A WHOLE BOARD, THE BRIGHT-LEVEL DATA OF ALL MODULES AND THEIR COLOR SHOULD BE MEMORIZED IN ROM OR S/W DISKETTE. BECAUSE THE MEMORY IN MODULE IS VOLATILE, THESE DATA SHOULD BE SUPPLIED EVERYTIME WHEN THE DISPLAY BOARD SYSTEM IS INITIALED.

- WHEN SYSTEM IS POWER ON, INITIALIZATION PROCEDURE IS REQUIRED FOR STORING BRIGHT-LEVEL DATA WITH HARDWARE OR SOFTWARE METHOD.

11. TOTAL BRIGHTNESS LEVEL CONTROL BLOCK



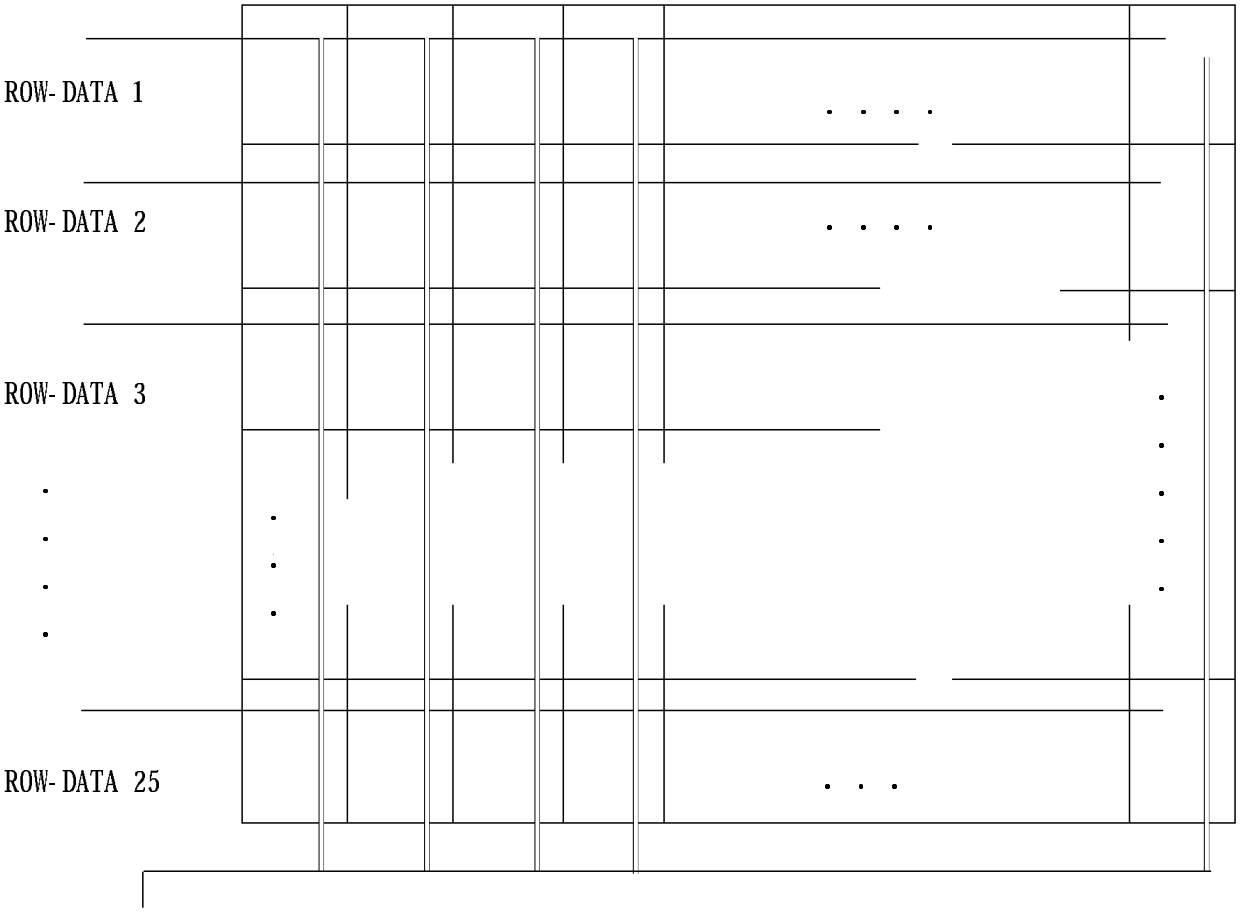
* FUNCTION

DURING NORMAL DISPLAY, TOTAL BRIGHTNESS OF A MODULE CAN BE CONTROLLED THROUGH PULSE WIDTH OF "BRIGHT" SIGNAL. BUT UNBALANCE OF BRIGHTNESS BETWEEN MOULES HAS CAUSED SIGNIFICANT PROBLEMS IN DISPLAY QUALITY.
THERFORE, OUR DESIGN TARGET OF THIS FUCTION IS TO GIVE SYSTEM-MAKER USEFUL METHOD FOR ADJUSTING UNFORMITY OF WHOLE DISPLAY BOARD BY SETTING THE BRIGHTNESS LEVEL OF EACH COLOR IN INDIVIDUAL MODULE.

TWO 48-BIT DATA PATTERN ARE STORED IN SHIFT-REGISTERS RESPECTIVELY FOR RED AND GREEN COLOR, AND THOSE PATTERNS ARE LOGICALLY OR-ED WITH "BRIGHT" SIGNAL TO GENERATE NEW INTERNAL SIGNALS WHICH CONTROLS BRIGHTNESS OF EACH COLOR.

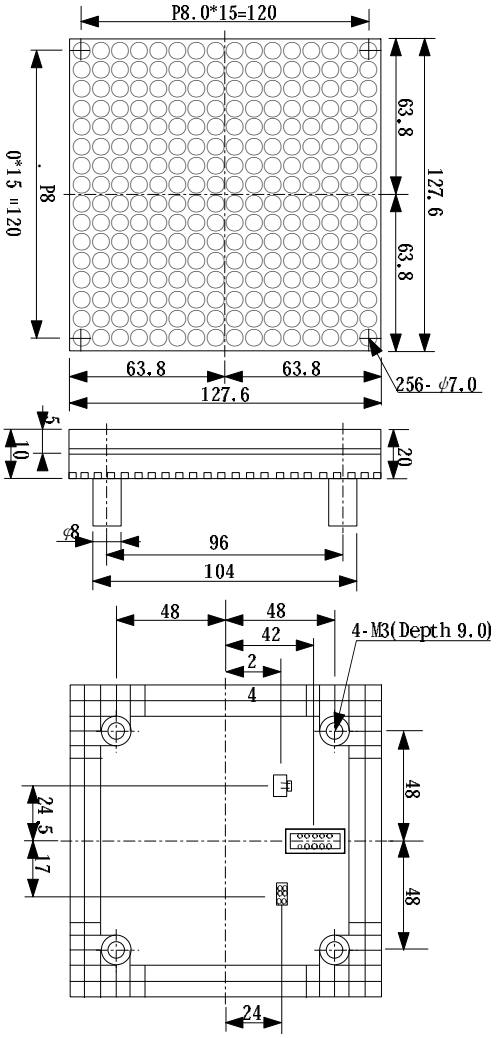
12. CONNECTION OF LARGE DISPLAY PANEL

EX) 40 x 25 MODULES



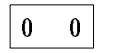
ROW-DATA 1 - 25	COMMON : BRT-WRITE BRT-CLOCK
	EACH ROW : SEL-1 . . . . . SEL-25
COLUMN-DATA 1 - 40	COMMON : RED DATA GREEN DATA RESET
	EACH COLUMN : CLK-1 . . . . CLK-40 BRT-1 . . . . BRT-40

10. DIMENSION



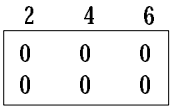
* torelance : ±0.3

CN1



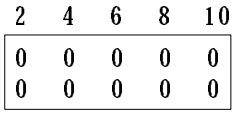
1	2
1	GND
2	Vcc

CN2



1	3	5
1	NO CONNECT	
2	SELECT	
3	GND	
4	BRT-WRITE	
5	GND	
6	BRT-CLOCK	

CN3



1	3	5	7	9
1	GND	2	RED DATA	
3	GND	4	GREEN DATA	
5	GND	6	CLOCK	
7	GND	8	BRIGHT	
9	GND	10	RESET	

14. PRECAUTIONS ON INSTALLING

- Please pay attention to radiate the heat from LED modules especially when the size of the display panel is large and ventilation condition is not good.

- Please do not give a mechanical shock to avoid reformation of the LED MATRIX MODULE.

- Please do not scratch the surface of the LED MODULE.

- Modules should be handled under anti-static control.

- Twisted cable or shielded wire is recommendable for safety operation from high frequency noise.

15. PACKING METHOD

- SLM1608MD2 : EACH PIECE IS ENVELOPED IN ANTI-STATIC ENVELOPE.
  SMALL INNER BOX CONTAINS 10 PIECES. (150x150x275 ㎜)
  OUTER BOX CONTAINS 10 SMALL BOXES. (= 60 PIECES)
  (290x320x475 ㎜)

MODEL	INNER BOX		OUTER BOX	
	DIMENSION (㎜)	NO. OF MODULE	DIMENSION (㎜)	NO. OF MODULE
SLM1608MD2 (128㎜)	150x150x275	10	290x320x475	60