

Figure 1: Main Loop

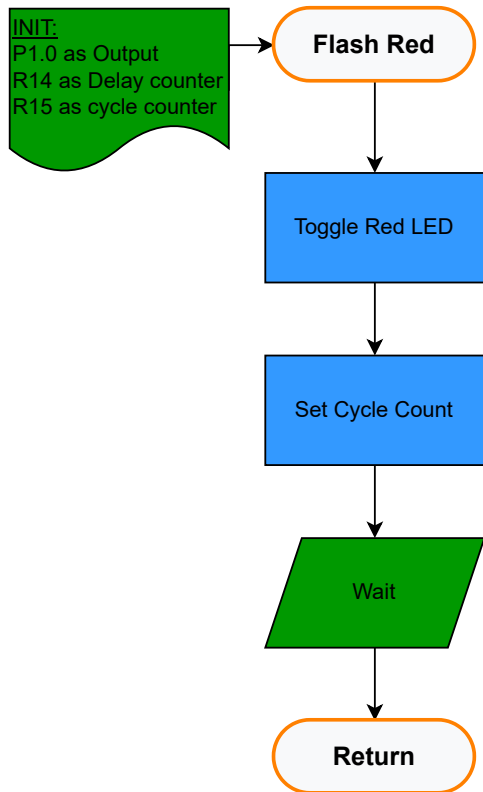


Figure 2: Flash Red Subroutine

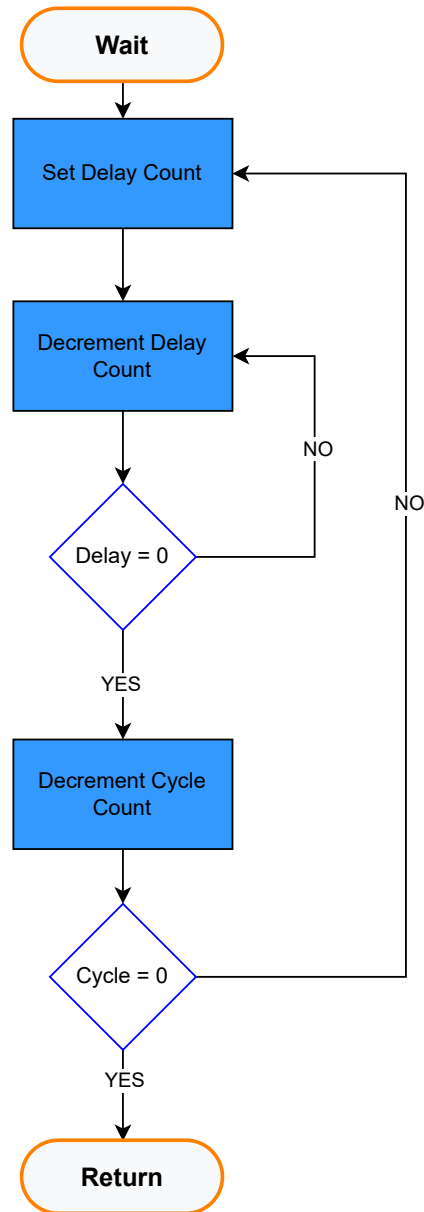


Figure 3: Delay Subroutine

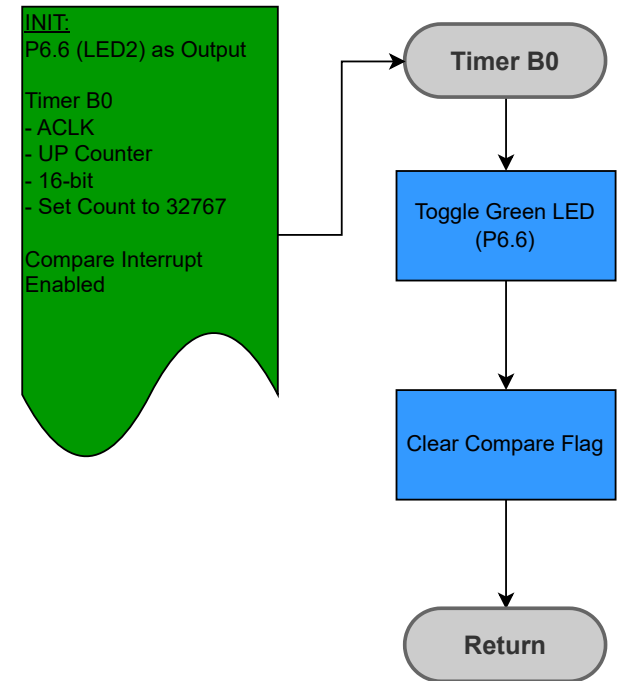


Figure 4: Timer B0 ISR