

Setup:
P1.0 as output
P3.2 as output (high, SCL)
P3.0 as output (high, SDA)
enable pull-up resistors on SDA

Timer Interrupts
Set TBOCTL:
Select ACLK
Select UP Counting
TBOCCR0 = 32778
TBOCCTL0 enabled
Set TB1CTL:
Select SMCLK
Select UP Counting
TB1CCR0 = 100
TB1CCTL0 enabled

Registers
R6 = 208 (RTC addr.)
R11 = 2 (CLK/Data op.)
R10 = 0 (clock output)
R4 = 0x2004
R12 = 0 (track cur. reg.)

Memory
Reserve 0x2000-0x2003
slave read addresses:
0x2004 = 0000000100000000b
0x2006 = 0001000100000010b
slave write addresses:
Reserve 0x2008-0x200B

Notes
R5 is wait counter
R7 is write register
R8 is read register
R9 is read/write bit counter
R13 is mem. tracker addr
R14 is mem. write addr.





