

INIT:
 Set P1.5 as SDA - Output
 Set P1.4 as SCL- Output
 Clear R4 - Clock Counter
 Clear R5 - State(0-free, 1-TX, 2-RX, 3-Stop, 4-ack)
 Clear R6 - Tx Buffer
 B0CCR0 - SCL Clock
 Count - 500
 B0CCR1 - SDA Clock
 Count - 250

