

EELE 465 Project 3 Demo

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Project Overview

System Includes:

- 16-input Membrane Keypad used to:
 - * Lock/unlock system
 - * enter password to validate operations
 - * Determine patterns and speed of phase changes on LCD display
- RGB LED informing user of system state
- LCD Display which displays one of a variety of patterns the user may choose
- Heartbeat LED to validate basic system operations

System Overview (Methods)

- main (orchestrates decision making and tracks current system state)
- read_input (reads an input from keypad)
- wait_for_unlock (while in locked state, controls changes of system state based upon user inputs)
- passkey (validates user's entered pin, when appropriate)
- update_color (updates current count-to variables to compare to timer ISR's pwms for RGB LED)
- patternX (an array of similar methods selected based upon user input which update LCD display phases of each available pattern)

System Overview (Interrupts)

- RGB_ISR (controls red, green, and blue uptime at ~50 Hz)
- Heartbeat_ISR (controls heartbeat LED)
- LCD_ISR (while LCD is operating, controls when next pattern phase is to be sent)

* Extra*

- Limit_ISR (determines whether time limit has been reached for passkey input)

(detailed flowchart next page)

Init:

RGB Status LED

- * P1.1-P1.3 as outputs
- * Timer Interrupt B0 (for RGB_ISR)
- * int pwms, red_counter, green_counter, blue_counter
- * vartype system_states
- * system_states State

LCD Display

- * P5.3-P5.0, P6.3-6.0 as outputs
- * Timer Interrupt B2 (for LCD_ISR default state: disabled)
- * int phase
- * int update_phase

Keypad

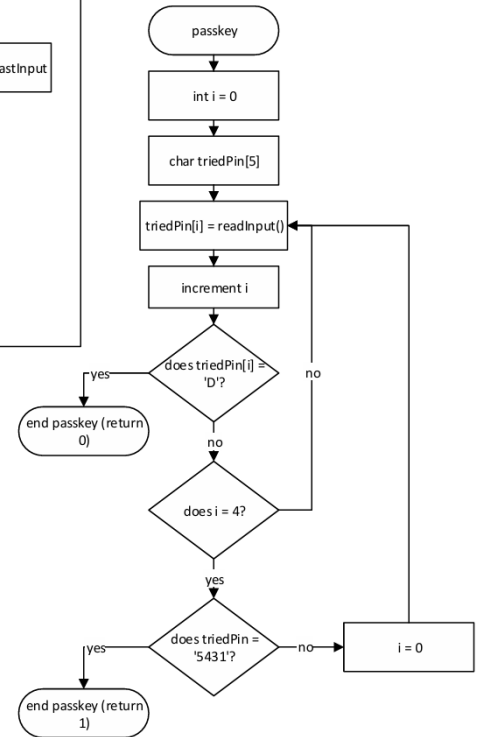
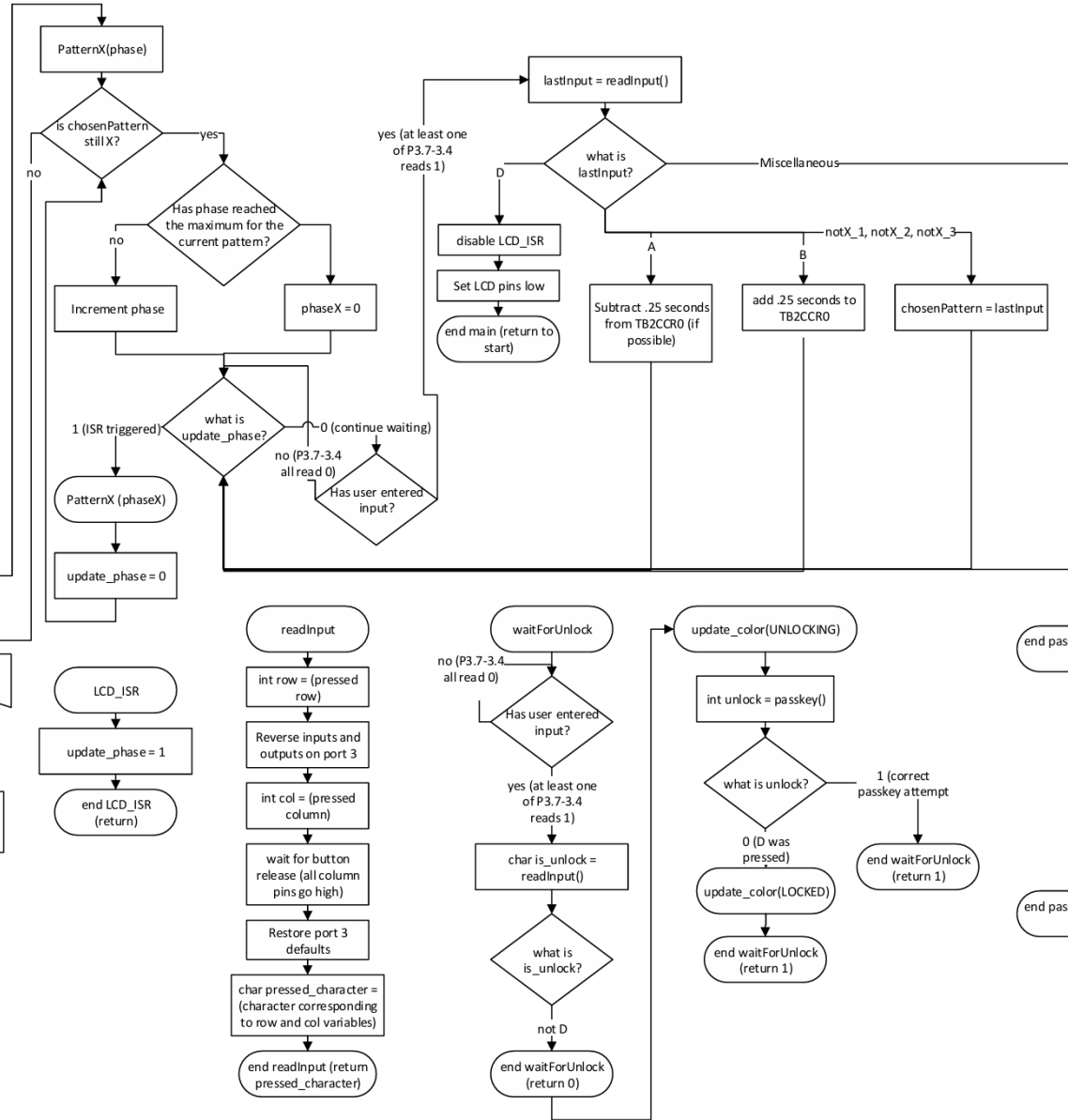
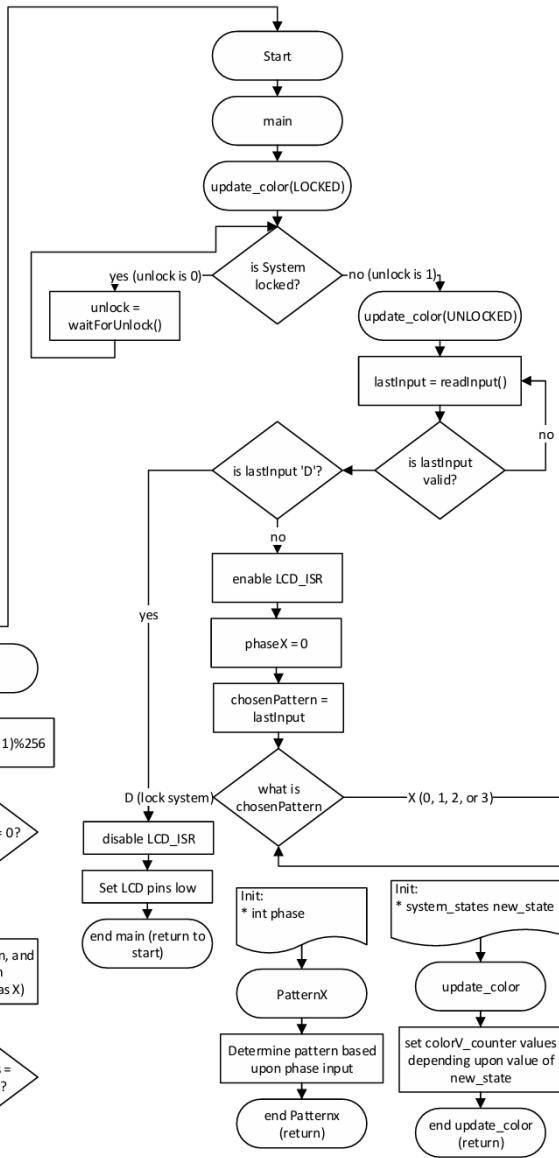
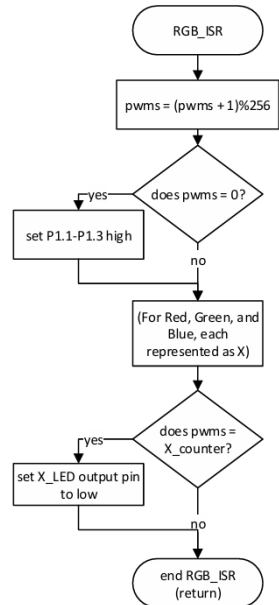
- * P3.7-P3.4 (rows) as inputs (pull down resistors on all of port 3)
- * P3.3-3.0 (columns) as outputs (high)

Heartbeat

- * P1.0 as output
- * Timer Interrupt B0 (for Heartbeat_ISR)

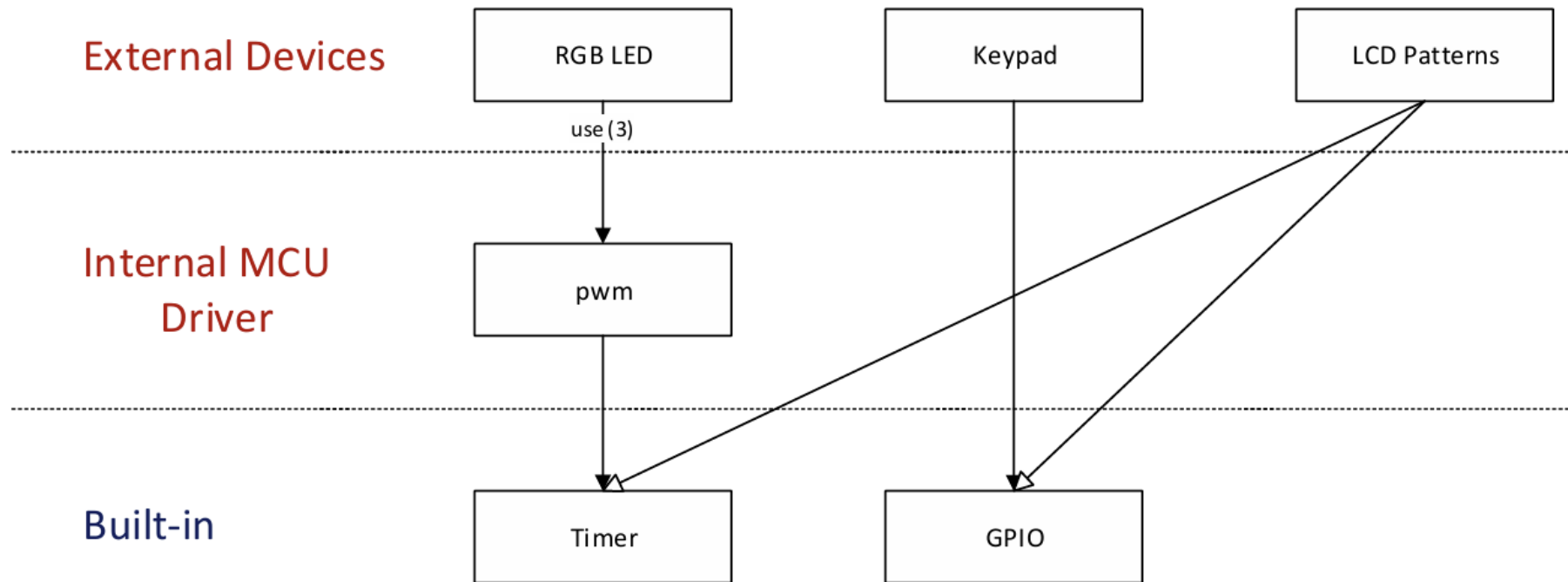
Main

- * int unlock = 0;
- * char lastInput = '0'
- * char chosenPattern
- * int rows



System Architecture Diagram

Software Architecture



Circuit Diagram

