

INIT:
P5.(4-0) output
P6.(2-0) output
P3.(7-4) input
P3.(3-0) output
P1.1 input ADC
P1.(2,3) SDA/SCL
P4.(6,7) SDA/SCL
P1.0 - output
P6.6 - output
TimerB0 - SMCLK
TimerB1 - ACLK
TimerB2 - ACLK
I2CB0 - Master LCD
I2CB1 - Master LM92

