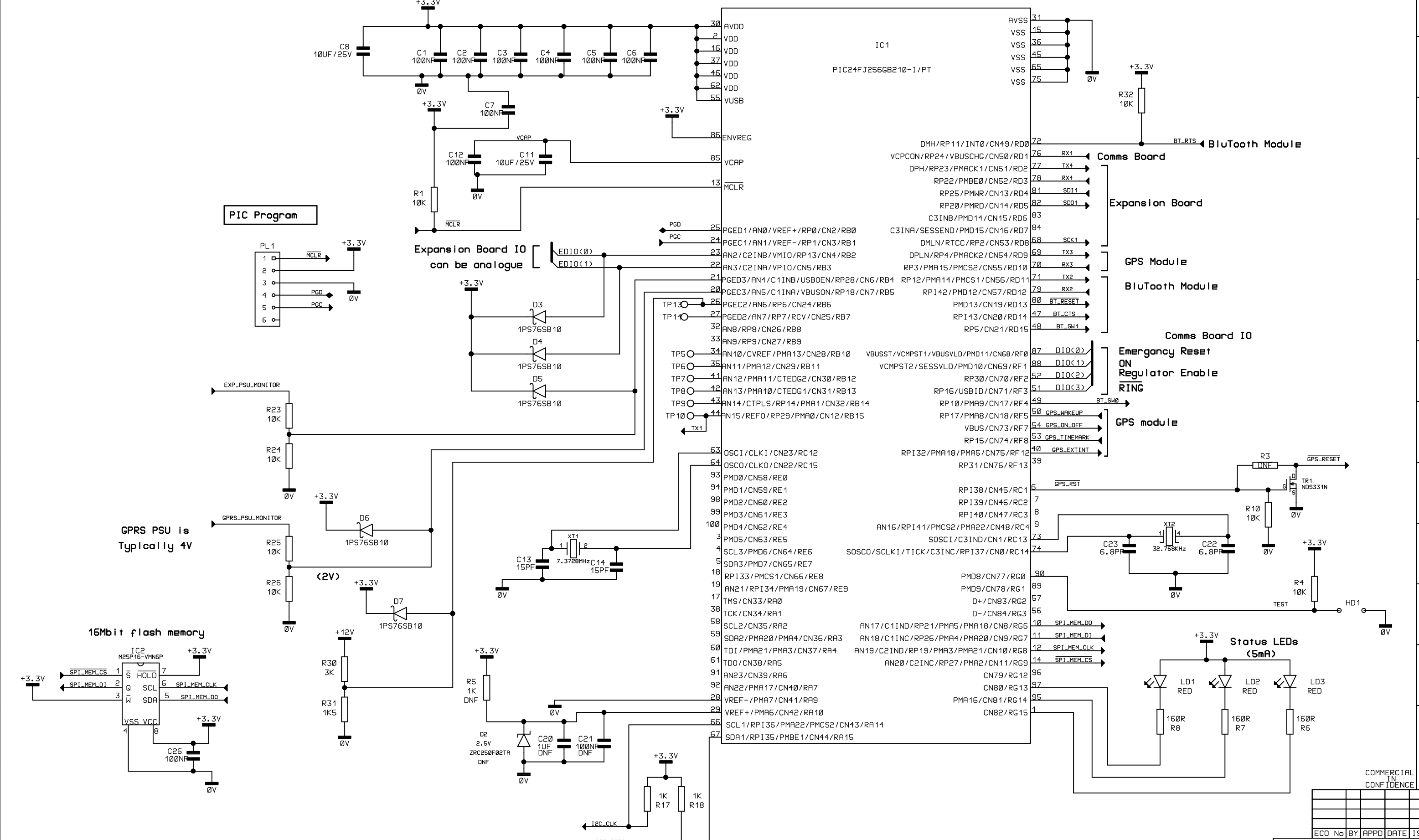
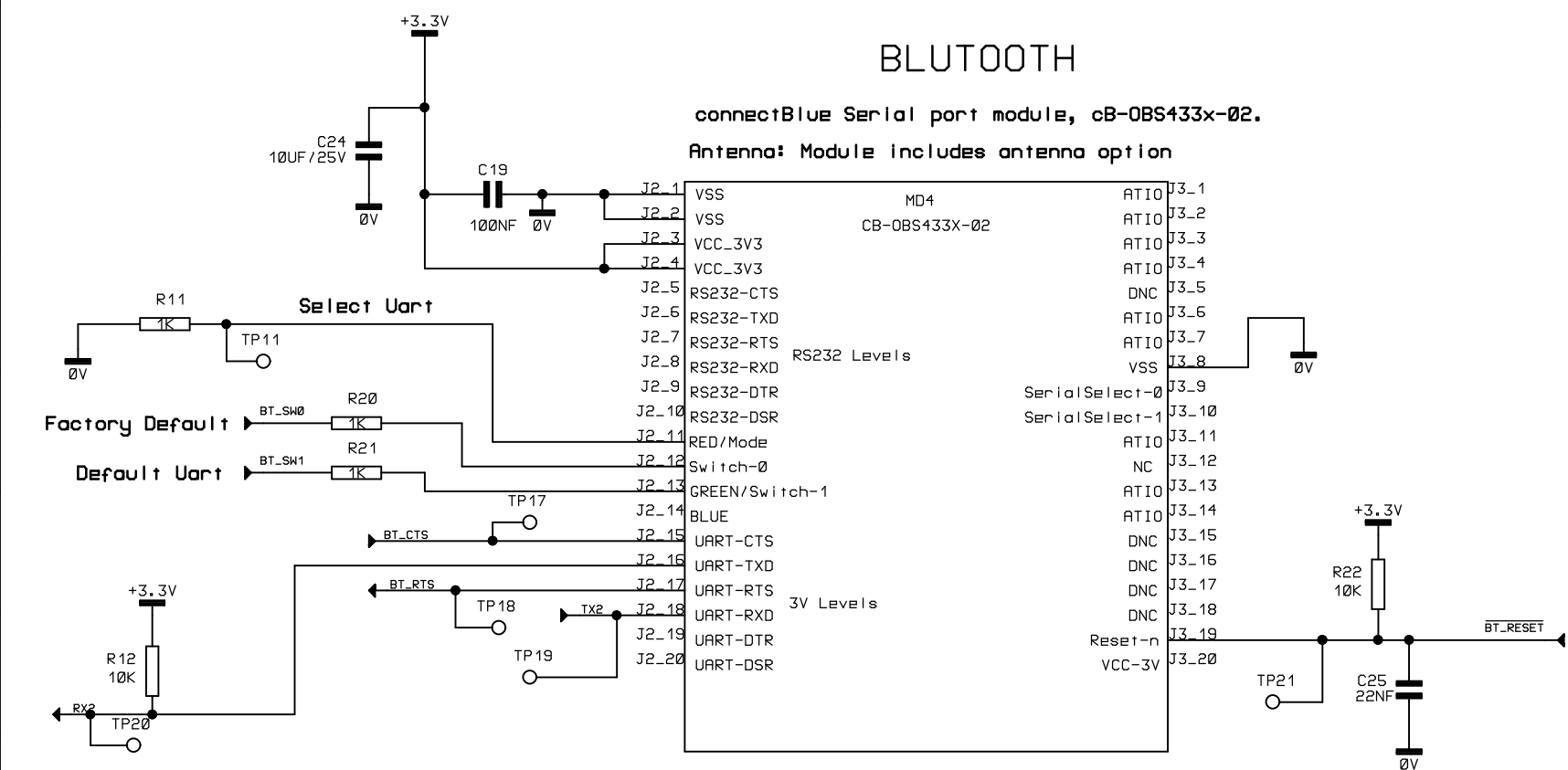
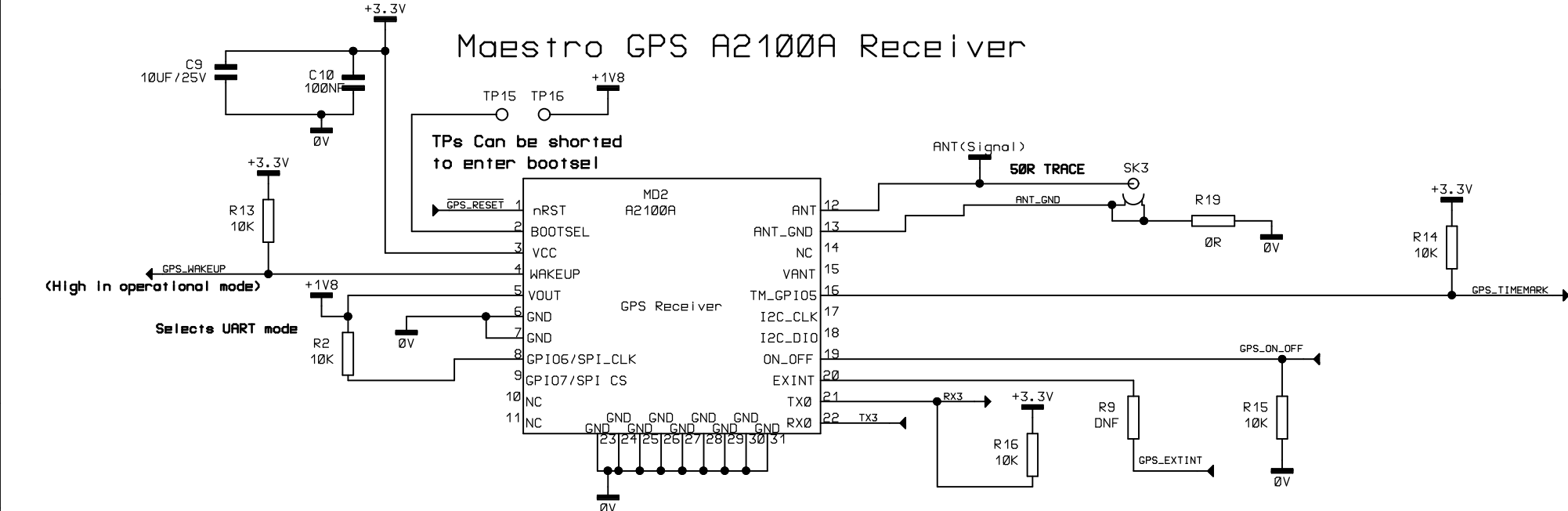


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GPS / BLUETOOTH

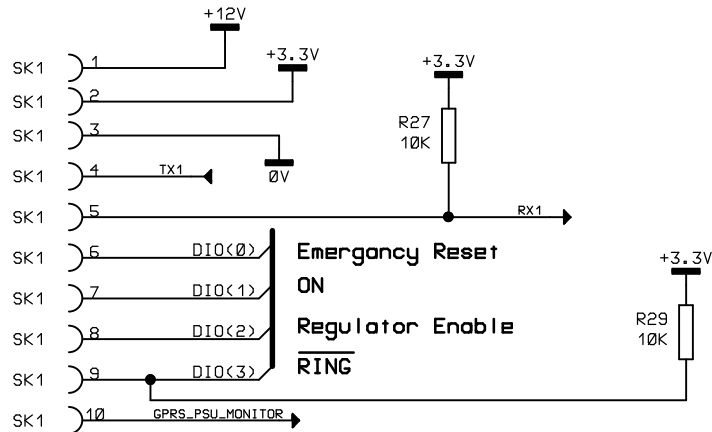
TITLE		SSL
LOW POWER BLUE TRUTH DETECTOR MAIN PCB CIRCUIT DIAGRAM		

© Copyright 2005 Simulation Systems Ltd Unit 12, Market Industrial Estate, Yaffton, Bristol, BS49 4RF, England www.simulations-systems.co.uk		File: Z:\SSL\DRAWING OFFICE\PCB\5577\5577-202
Designer: J.Herdman	Reviewer:	
Drawn: JH 09/12	Chkd:	Date:
Drq No:5577-202	SHT 2 of 3	Iss:Dev 21

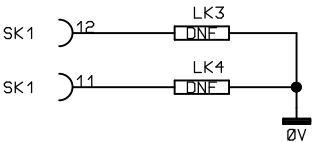
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Comms Board (Primarily GPRS,
LAN/4G as alternatives)

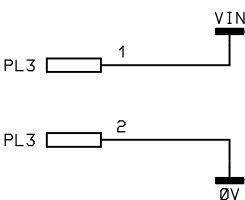
All signals @ 3.3V levels



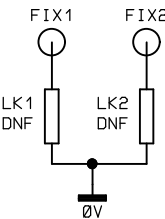
Fixing holes for comms board



12V Power Input

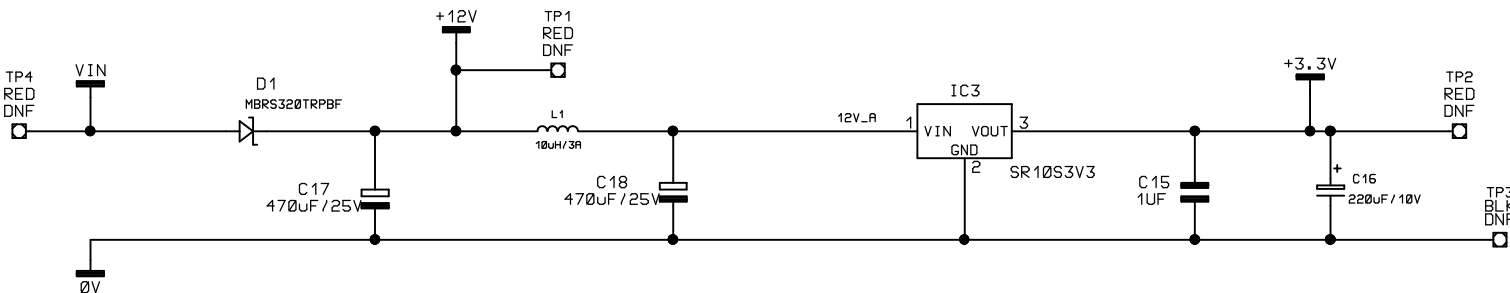
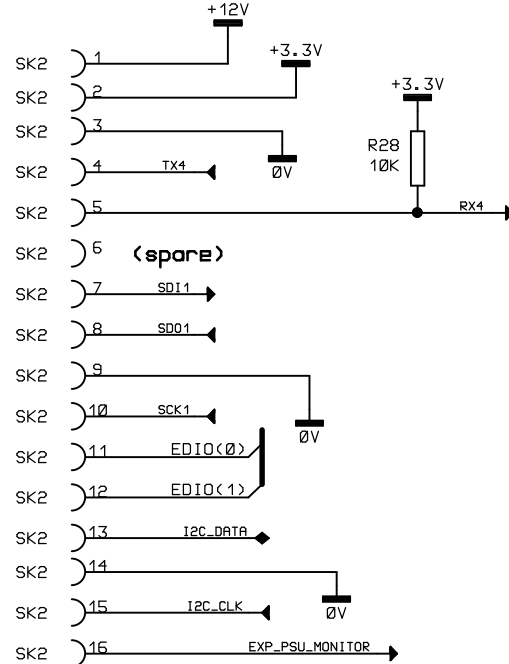


For expansion board

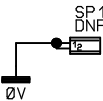


Expansion Board

All signals @ 3.3V levels



Spade connection option



CONNECTORS / PSU

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TITLE
LOW POWER BLUE TRUTH DETECTOR
MAIN PCB CIRCUIT DIAGRAM

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File: Z:\SSL\DRAWING OFFICE\PCB\5577\5577-202

Designer: J.Herdman

Drawn: JH 09/12

Drq No:5577-202

Reviewer:

Chkd:

Date:

SHT 3 of 3

Iss:Dev 21