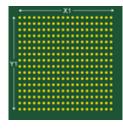
HDI Stackup Planner — Detailed Report for HSP-189471 Option B

Sierra Circuits, Inc.

www.protoexpress.com/hdi



Customer Input

Part Number/ Rev amINO/ 1.0

PCB Size in X : 0.51 inches X 0.59 inches

: Signal Outer Layer **BGA Pitch** : 0.65 mm

Total Number of Pins : 49

: X1 = 7 Pins Y1 = 7 Pins **BGA Pins Pattern**

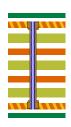
Material : 0.59

PCB Thickness : 0.031 inches

Number of Signal :36



MicroVias depicted by **SOLDER MASK** TOP SIGNAL L-1 DIELECTRIC PLANE L-2 DIELECTRIC L-3 PLANE DIELECTRIC **BOTTOM SIGNAL**



Total Thickness

1 Oz 1 Oz 1 Oz 1 Oz 0.0316 (inches)

Finished Copper Weight

Finished Thickness (inches) 0.0005 0.0014 0.0035 0.0014 0.0180 0.0014 0.0035 0.0014 0.0005

Customer Saved Impedance Results

SOLDER MASK

Layer	Impedance Model	Impedance (ohms)	Trace Width (mils)	Space (mils)
Layer 1	Soldermask Coated Microstrip Single-ended	51.7	5	
Layer 1	Soldermask Coated Microstrip Differential Pair	90.03	5	6

Stackup Details

Num	ber of Layers	Number of Signal Layers	Number of Sequential Laminations	Number of Plane Layers	Maximum Number of Laser Drills	Mechanical Drills
	4	2	0	2	0	1

BGA Fan out Recommendations

BGA Area Trace Geometry

	Trace/Space within BGA area	Maximum number of Trac between adjacent BGA pads/vias		
Top Layer	5 mils	0	0	0
Inner Layers	4.5 mils	1	0	0
Bottom Layer	4.5 mils	1	0	0

BGA Area Detail

Outside of BGA Area

MicroVia Pad Diameter : 12 mils
MicroVia Drill Diameter : 6 mils
Mechanical MicroVia Pad Diameter
Mechanical MicroVia Drill Diameter : 8 mils
Mechanical MicroVia Drill Diameter : 8 mils
Mechanical MicroVia Drill Diameter : 8 mils
Diameter : 8 mils

Via Set Information

This stack up supports the following via set L1-L4 $\,$

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