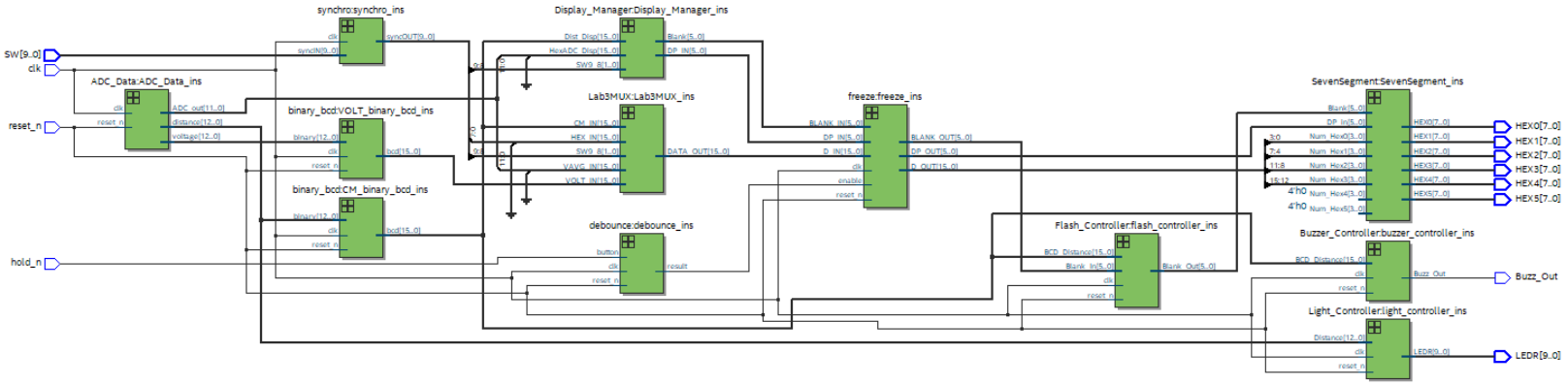
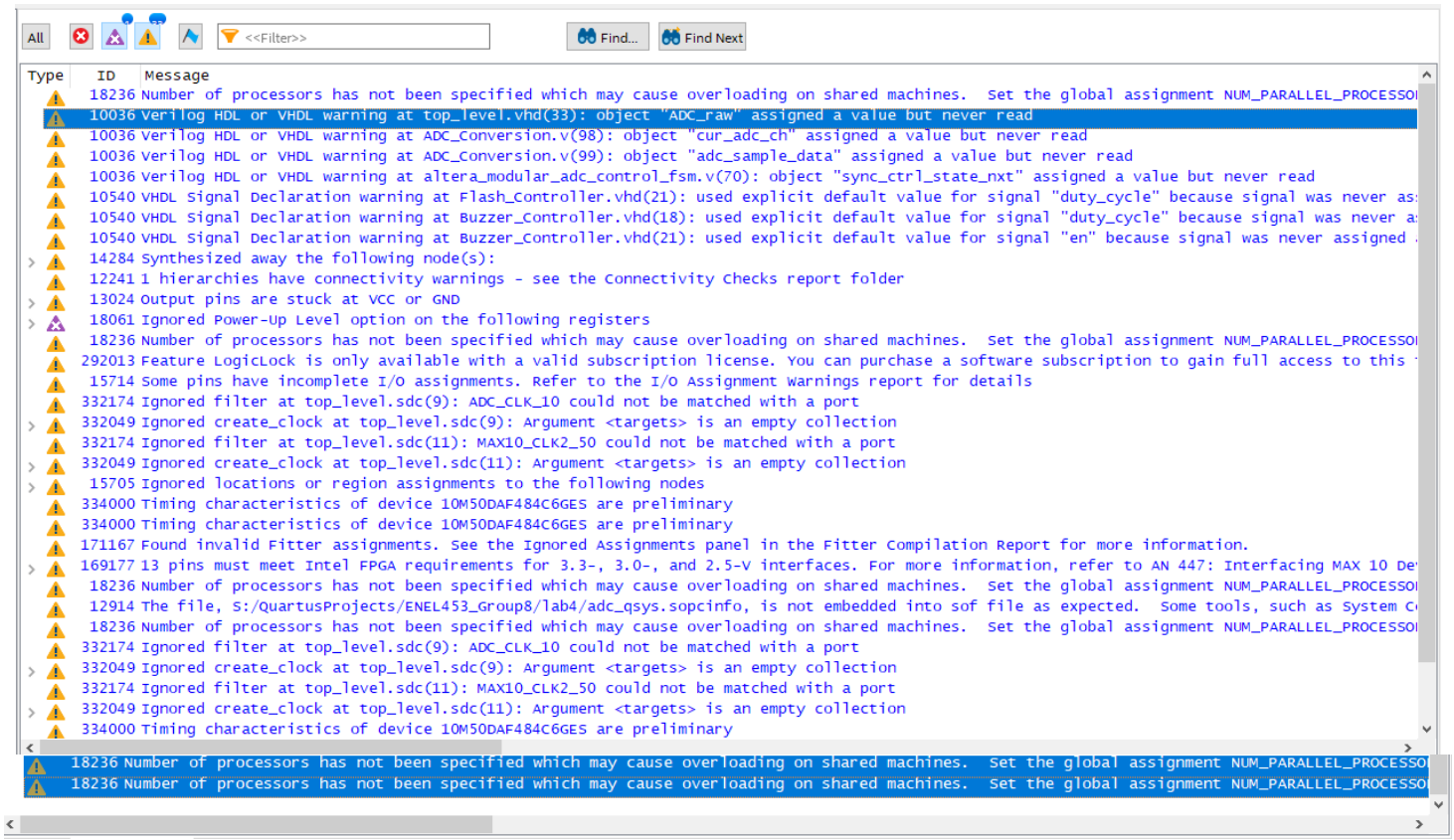
Lab 4 Design Record – Group 8

Students: Nabeel Amjad, Tahseen Intesar, Michael Tagg

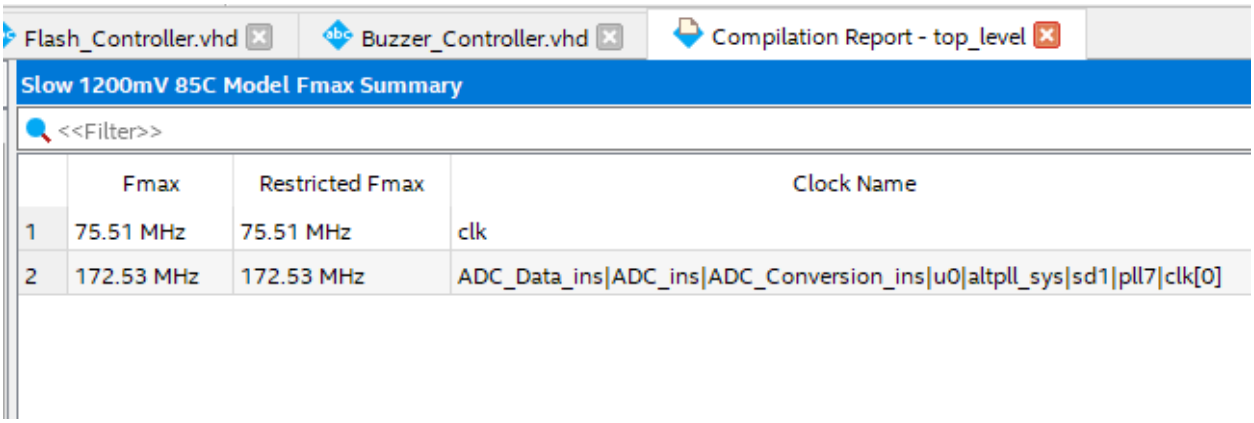
RTL Viewer



ERRORMESSAGES



TIMING@85C



SIMULATIONS

tb_top_level.vhd