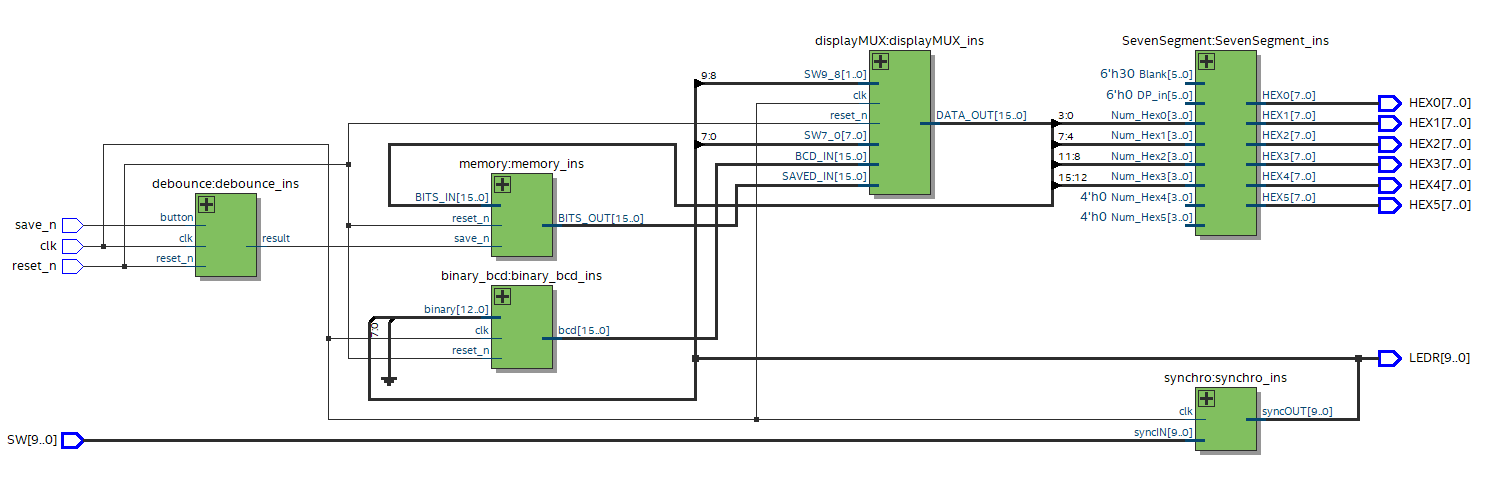
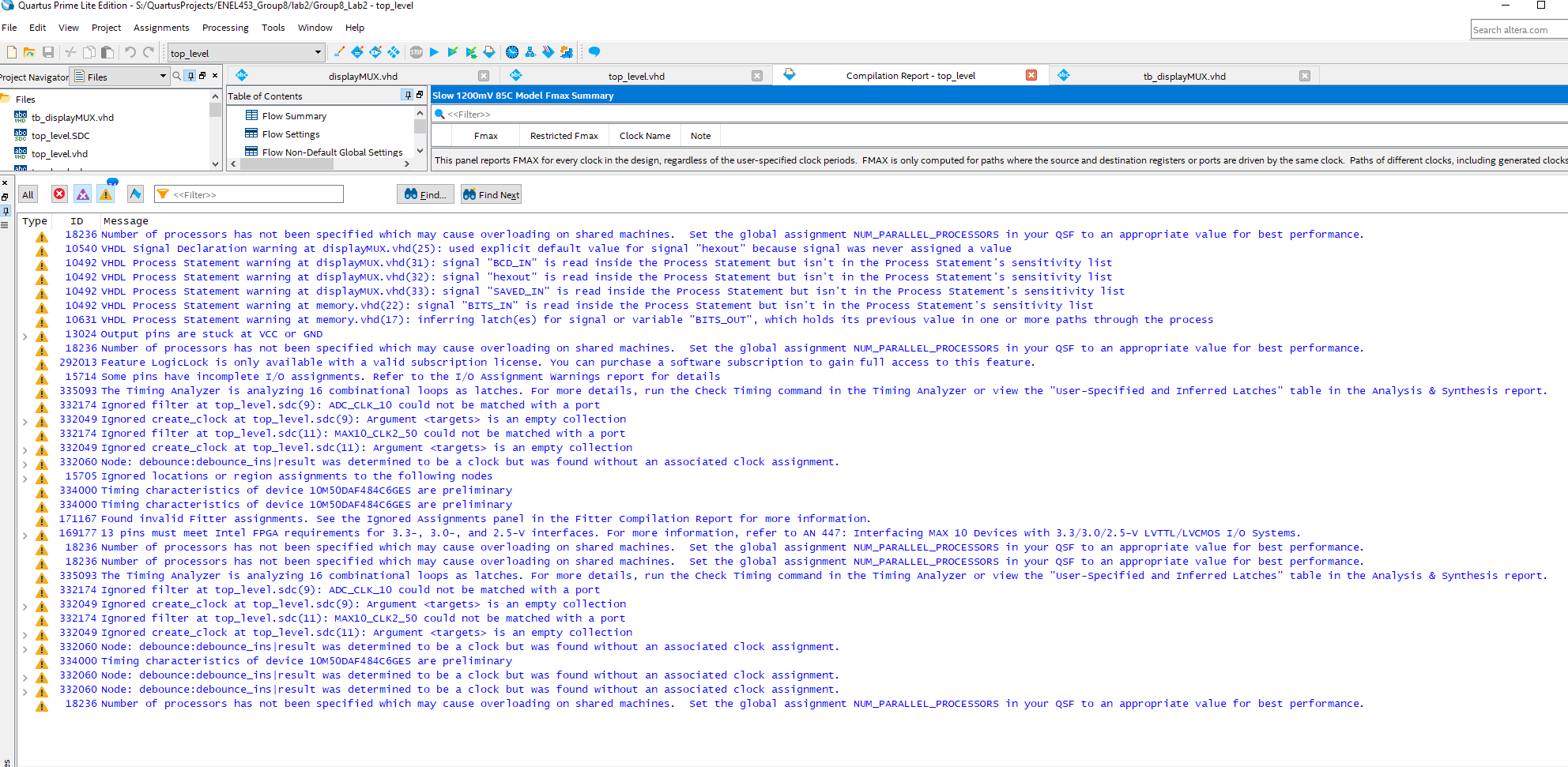
Lab 2 Design Record – Group 8

Students: Nabeel Amjad, Tahseen Intesar, Michael Tagg

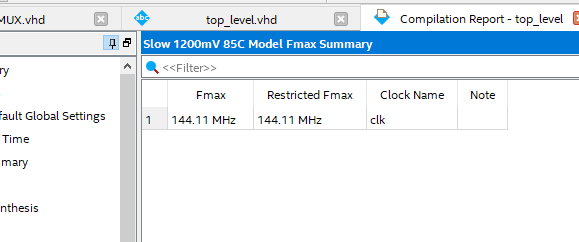
RTL Viewer



ERRORMESSAGES



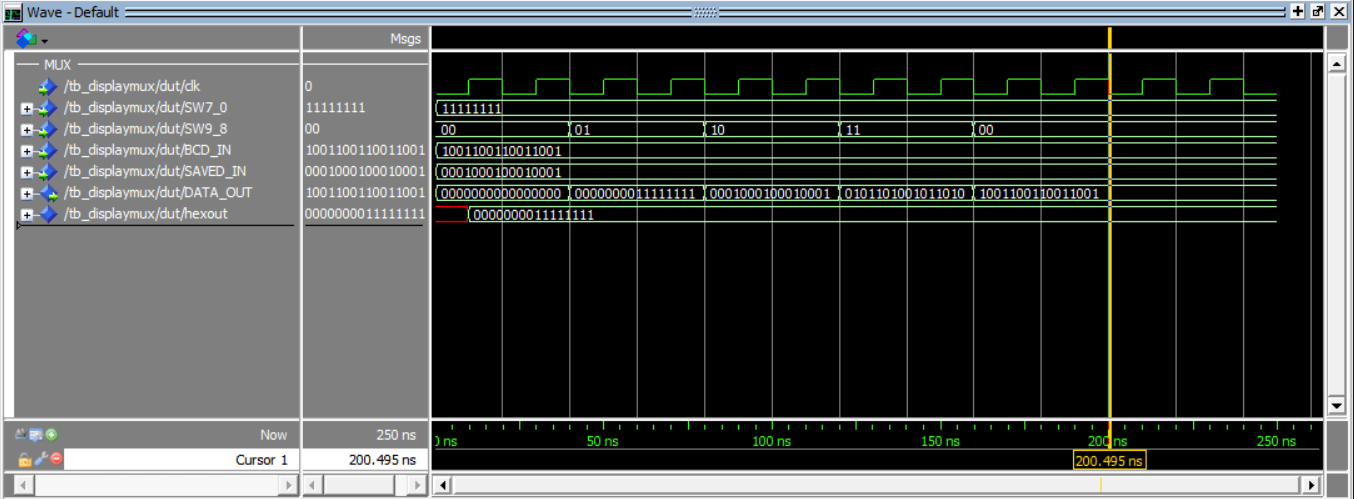
TIMING@85C



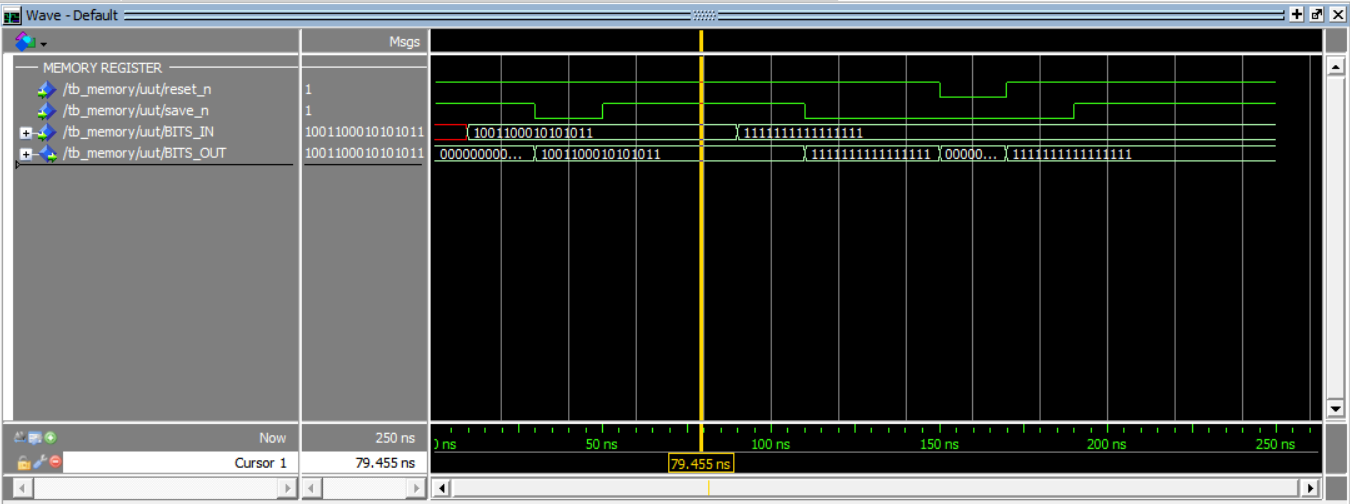
SIMULATIONS

**TOP LEVEL**

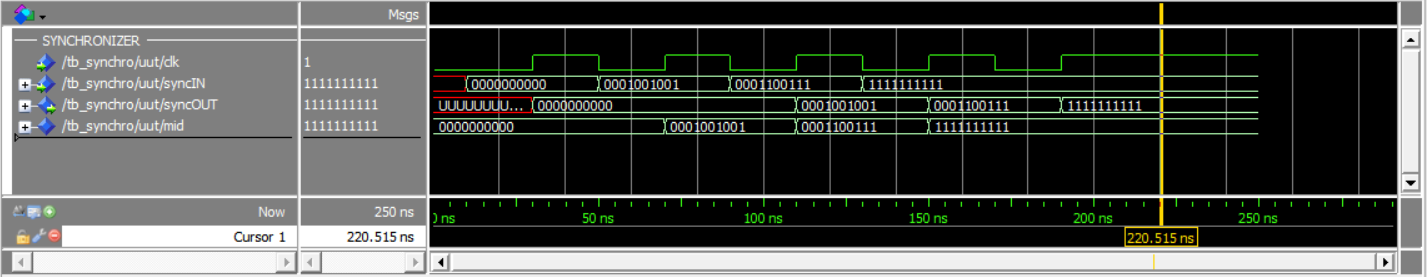
**4:1 MULTIPLEXER**

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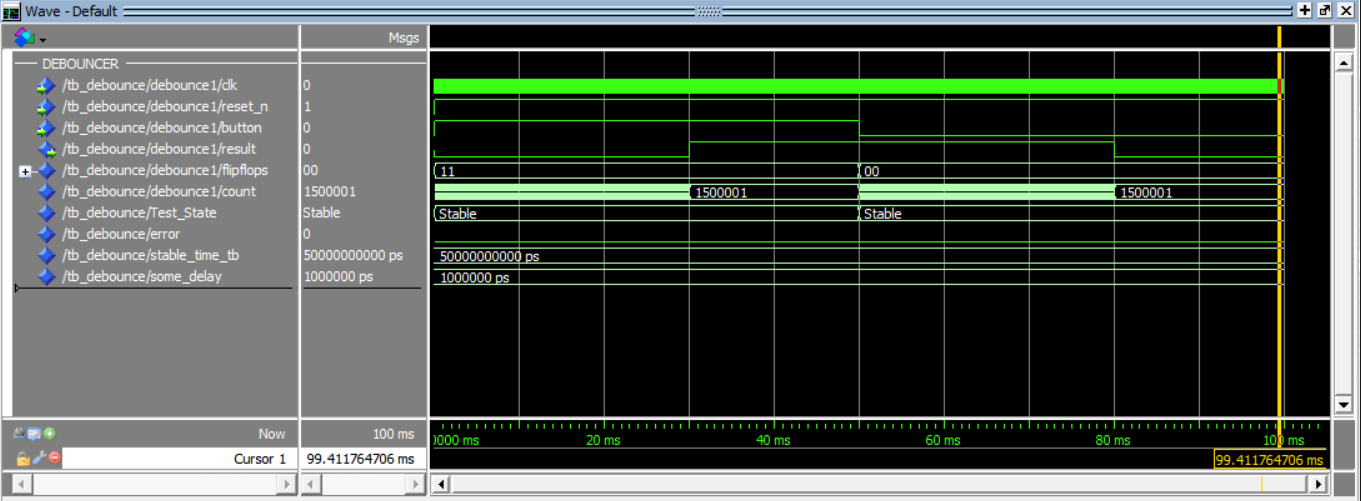
**MEMORY REGISTERS**

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**SYNCHRONIZER**

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**DEBOUNCER**

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