

Design Rules Verification Report

Filename : E:\Users\mtlang\Github\ECE315_Altium_Files\business_card.PcbDoc

Warnings 0
Rule Violations 0

| Warnings | |
|----------|---|
| Total | 0 |

| Rule Violations | |
|--|---|
| Clearance Constraint (Gap=8mil) (All),(All) | 0 |
| Short-Circuit Constraint (Allowed=No) (All),(All) | 0 |
| Un-Routed Net Constraint ((All)) | 0 |
| Modified Polygon (Allow modified: No), (Allow shelved: No) | 0 |
| Width Constraint (Min=99999mil) (Max=50mil) (Preferred=0mil) (HasFootprint('BUCKYBADGER') or | 0 |
| Width Constraint (Min=10mil) (Max=10mil) (Preferred=10mil) (All) | 0 |
| Power Plane Connect Rule(Direct Connect)(Expansion=20mil) (Conductor Width=10mil) (Air Gap=10mil) (Entries=4) | 0 |
| Minimum Annular Ring (Minimum=6mil) (All) | 0 |
| Hole Size Constraint (Min=15mil) (Max=100mil) (All) | 0 |
| Hole To Hole Clearance (Gap=10mil) (All),(All) | 0 |
| Minimum Solder Mask Sliver (Gap=0mil) (All),(All) | 0 |
| Silk To Solder Mask (Clearance=0mil) (IsPad),(All) | 0 |
| Silk to Silk (Clearance=5mil) (All),(All) | 0 |
| Net Antennae (Tolerance=0mil) (All) | 0 |
| Board Clearance Constraint (Gap=0mil) (All) | 0 |
| Room 06_Buzzer (Bounding Region = (1050mil, 2260mil, 1990mil, 2930mil) (InComponentClass('06_Buzzer')) | 0 |
| Room 03_MCU (Bounding Region = (1044.488mil, 1030mil, 1990mil, 2260mil) (InComponentClass('03_MCU')) | 0 |
| Room 05_LEDs (Bounding Region = (2000mil, 1050mil, 3580mil, 2620mil) (InComponentClass('05_LEDs')) | 0 |
| Room 02-Power (Bounding Region = (3610mil, 1030mil, 4470mil, 2970mil) (InComponentClass('02-Power')) | 0 |
| Height Constraint (Min=0mil) (Max=1000mil) (Preferred=500mil) (All) | 0 |
| Total | 0 |