



M908

LoRa Communication + MCU Module

With Intel Quark

Preliminary DATASHEET 27th February, 2018

Table of Contents

1	Р	rodu	uct Brief
	1.1	E	Brief1
	1.2	F	Part Number1
2	F	eatu	ures and Applications
3	В	Block	Diagram
4	T	echr	nical Specifications3
	4.1	A	Absolute Maximum Ratings3
	4.2	(Operation Condition 3
	4.3	١	Wireless Specifications
	4.4	L	LoRa RF Performance4
		4.4.	1 Band 915 MHz (US)
		4.4.	2 Band 868 MHz (EU)
		4.4.	3 Band 780 MHz (CN)
		4.4.	4 Band 923 MHz (AS)
		4.4.	5 Band 922 MHz (KR)
		4.4.	6 Band 433 MHz (EU)9
		4.4.	7 Band 490 MHz (CN)
	4.5	F	Power Consumption11
5	C	ime	nsions
6	Р	in A	ssignments
7	R	Recor	mmended Footprint14
8	R	Refer	rence Design Circuit
9	R	Recoi	mmended Reflow Profile
10) S	iP M	lodule Preparation
	10.	1 H	Handling
	10.	2 9	SMT Preparation
11	L P	acka	age Information
17) г)OCII	mont History

1 Product Brief

1.1 Brief

The SiP module includes Intel Quark SE C1000 and Semtech SX1276.

Low-power-consumption system-on-chip that provides edge analytics by combining an x86 MCU with a sensor subsystem and pattern-matching capability through a hardware-accelerated engine.

The Semtech SX1276 carries LoRa® long range modem that provides ultra-long range spread spectrum communication and high interference immunity whilst minimising current consumption.

For the software and driver development, we provide extensive technical document and reference software code for the system integration. Hardware evaluation kit and development utilities are ready to applied.

KEY FEATURES

- Bluetooth 4.2 Low Energy Compliant
- Embedded 32 bits high performance
 MCU with clock up to 48MHz
- 512 kB programmable flash
- 32 kB SRAM
- Built-in antenna
- Module size: 12 x 12 mm

1.2 Part Number

Part Number	RF Band	Description
908H	868, 915, 780, 922, 923MHz	EU868, US915 ISM Band
908L	433, 490MHz	EU433, AS430 ISM Band



2 Features and Applications

Feature List

- MCU
 - ➤ Intel 32MHz Quark MCU (C1000)
 - ➤ 32MHz DSP for sensor subsystem
 - 8 kB 2-way L1 instruction cache
 - Low Latency Data Tightly Coupled Memory (TCM)
 - ➤ Interface to on-die SRAM: 1.49 DMIPs/MHz
- LoRa transceiver
 - > Full Band 868/915/922/923/780/433/490 MHz
 - Supports end-device class A/B/C
 - > 168 dB maximum link budget
 - +20 dBm 100 mW constant RF output vs. V supply
 - > +14 dBm high efficiency PA
 - ➤ High sensitivity: Down to -148 dBm
 - > Programmable bit rate up to 300 kbps
- Memory
 - > 384 kB of on-die NVM + 8 kB OTP on-die NVM
 - > 80 kB of on-die SRAM
- Power management
 - SiP Status: Active, Sleep and off
 - Sensor subsystem: Sensing active, sensing wait and sensing standby
 - > Platform power DC-DC 1.8V, 3.3V
- Industry standard I/O hardware
 - ➤ USB 1.1 FS device
 - 2 I²C / 2 SPI / 2 UART, 4 Timers, 4 PWM
- Sensor subsystem interface
 - > 2 I²C / 2 SPI
 - ➤ 4 channel 12-bit ADC
 - 2 timers
- Idustry standard I/O hardware characteristics
 - ➤ Operation range: -40°C ~ +85°C
 - LGA-144 pins, 12 x 12 x 1.3 mm

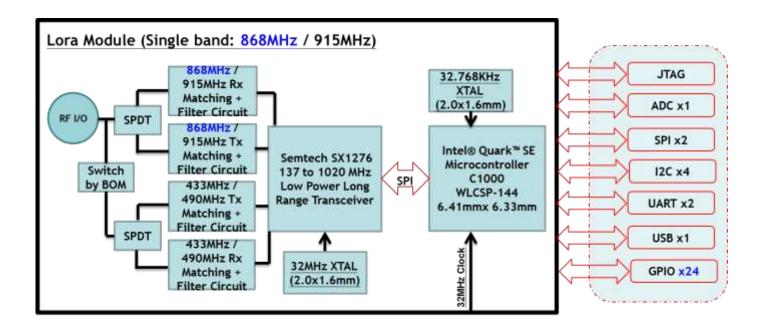
Applications

- Utility metering and lighting control
- Long range irrigation systems
- Wireless alarm and security systems
- Internet of Things (IoT)
 - > Industrial monitoring and control
 - Sensor networks
 - ➤ Home and building automation



3 Block Diagram

There is fully integration module with Semtech SX1276, 32MHz low power crystal. Intel C1000 Quark Processor.





4 Technical Specifications

Operation and storage condition

4.1 Absolute Maximum Ratings

Item		Description	Value	Unit						
Ratings	Ratings Over Operating Free-Air Temperature Range									
1	Supply voltage	All supply pins must have the same voltage	3.3	V						
2	Voltage on any pin		3.3/1.8	V						
3	Storage temperature	range	-40 ~ 110	°C						

4.2 Operation Condition

Operating Condition	Min	Typical	Max	Unit
VCC		3.3		V
Operation ambient temperature range	-40		85	°C

4.3 Wireless Specifications

The M908 module is compliant with the following features and standards:

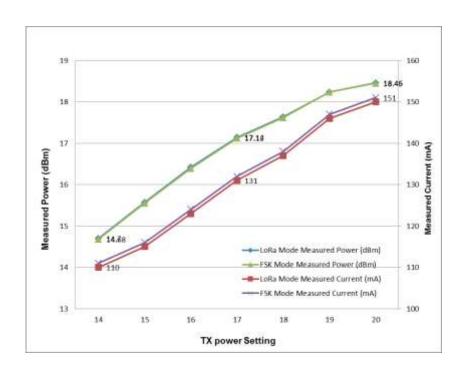
Features	Description
Frequency	868/915/922/923/780/433/490 MHz
Modulation Method	FSK, GFSK and LoRa Technology Modulation
Sensitivity	Down to -148 dBm

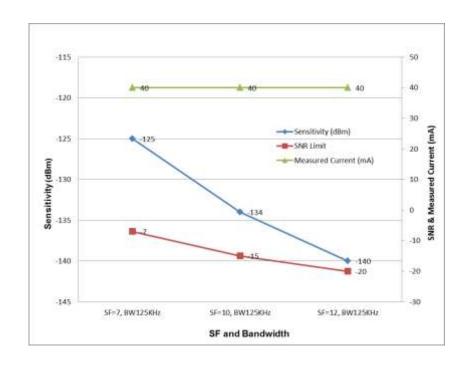


4.4 LoRa RF Performance

4.4.1 Band 915 MHz (US)

Transmitter output power: (Tx power setting from 14 ~ 20 dBm)

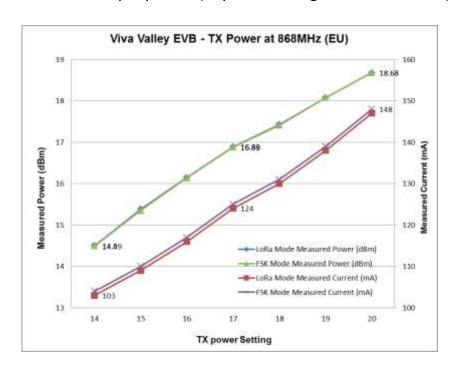


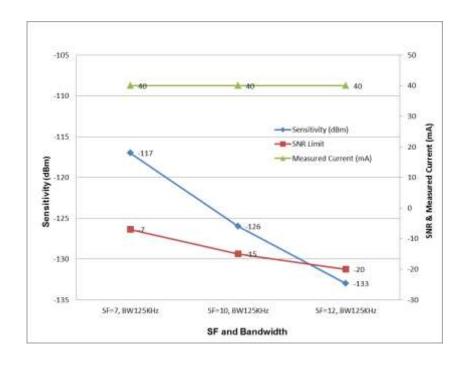




4.4.2 Band 868 MHz (EU)

Transmitter output power: (Tx power setting from 14 ~ 20 dBm)

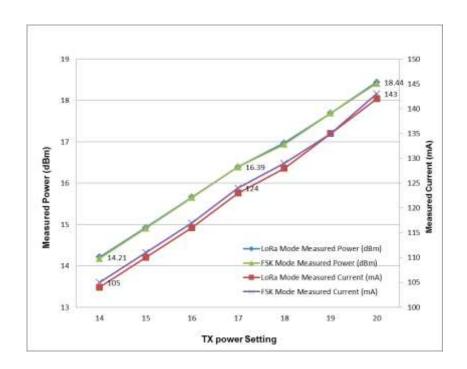


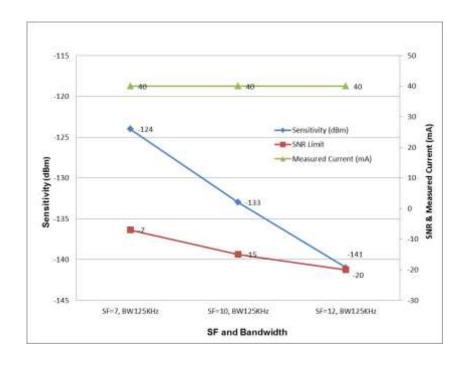




4.4.3 Band 780 MHz (CN)

Transmitter output power: (Tx power setting from 14 ~ 20 dBm)

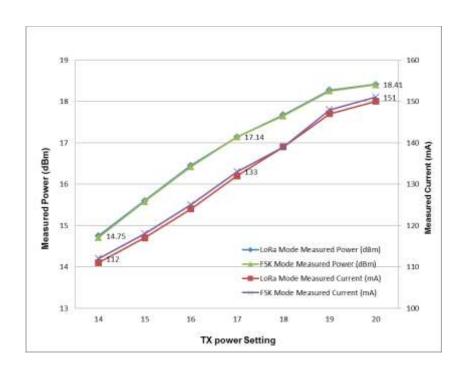


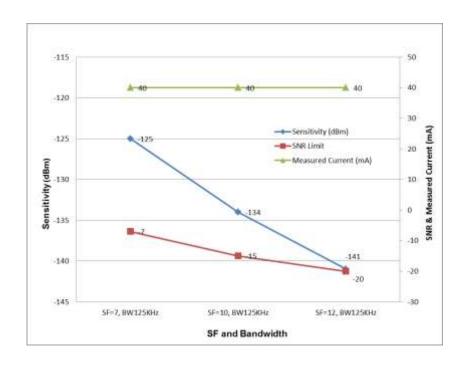




4.4.4 Band 923 MHz (AS)

Transmitter output power: (Tx power setting from 14 ~ 20 dBm)

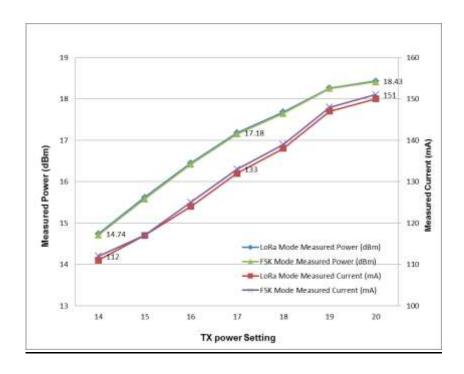


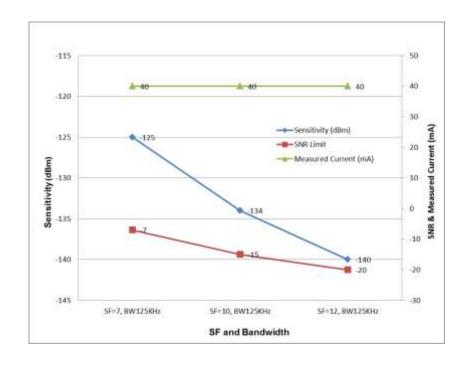




4.4.5 Band 922 MHz (KR)

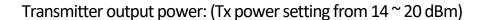
Transmitter output power: (Tx power setting from 14 ~ 20 dBm)

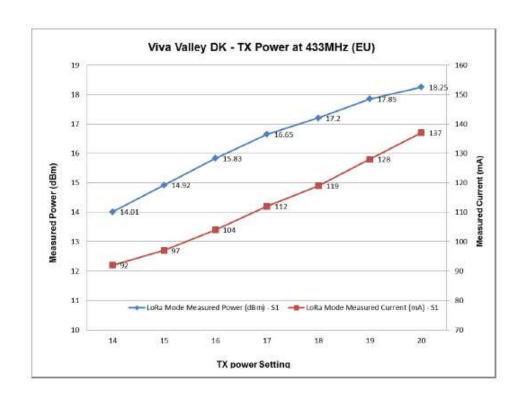


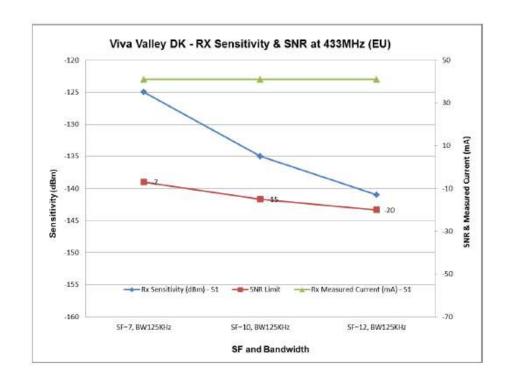




4.4.6 Band 433 MHz (EU)

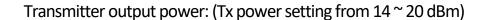


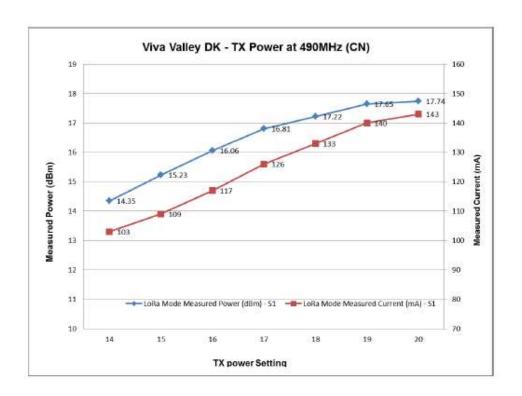


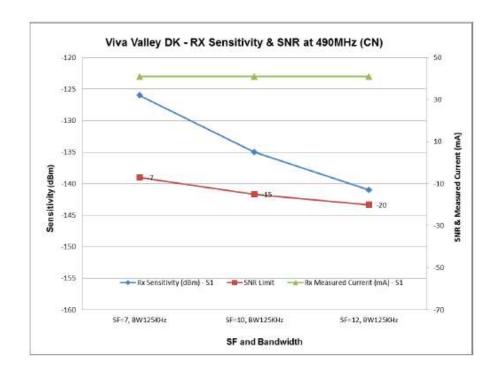




4.4.7 Band 490 MHz (CN)









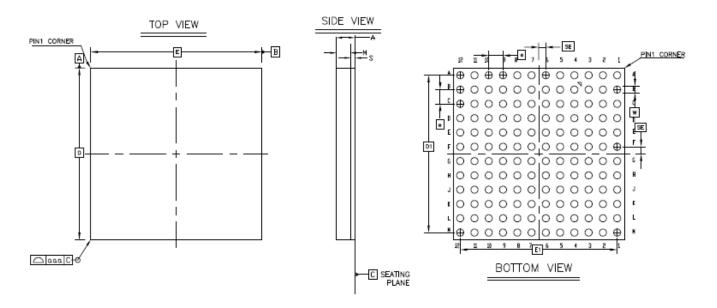
4.5 Power Consumption

Item	Тур.	Units
TX with 18.7dBm at 915MHz (Continue Tx with RF Test FW)	149	mA
TX with 18.3dBm at 433MHz (Continue Tx with RF Test FW)	136	mA
Standby Mode with RF Test FW	32 ~ 33	mA
Rx (Continue Rx with RF Test FW)	~40	mA
Deep Sleep	~2	mA



5 Dimensions

The size and thickness of the M908 module are 12mm (W) x 12mm (L) x 1.3mm (H):



Dimension Detail

		Symbol	MIN.	NOM.	MAX.			
Package :	Package :							
Body Size:	X	E D		12.000 12.000				
Ball Pitch :	'	e		1.000				
Total Thickness :		Α		1.332	1.402			
Mold Thickness :		М		1.000	1.030			
Substrate Thickness :		s		0.332	0.372			
Solder Thickness :		Т						
Lead Width		W	0.480	0.500	0.520			
BODY CENTER TO CONTACT BALL	BODY CENTER TO CONTACT BALL			0.500				
		SE	0.500					
Package Edge Tolerance :		aaa	0.100					
Lead Count :	п	144						
EDGE BALL CENTER TO CENTER:	D1	11.000						
		E1	11.000					



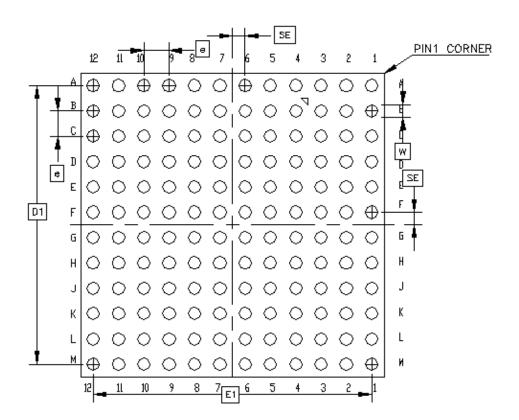
6 Pin Assignments

	1	2	3	4	5	6	7	8	9	10	11	12
Α	RF_IO	GND	GND	GND	GND	VCC_AON _1P8	TR_RXTX	GPIO[21]	GPIO[24]	GPIO[25]	AREF_PA D	GND
В	GND	GND	GND	GND	GND	GND	SPI1_SS_ MISO	GPIO[22]	GPIO[23]	GPIO[26]	GND	VCC_IO_A ON
С	I2C0_SDA	I2C0_SCL	I2C1_SCL	I2C1_SDA	SPI0_SS_ MISO	SPI0_SS_ CS_B[3]	SPI1_SS_ MOSI	SPI1_SS_ CS_B[2]	GND	GPIO[27]	PWM[3]	PWM[2]
D	I2C1_SS_S CL	I2C1_SS_S DA	GND	GND	SPI0_SS_ SCK	SPI0_SS_ CS_B[2]	SPI1_SS_ SCK	SPI1_SS_ CS_B[3]	GND	GND	PWM[0]	PWM[1]
E	I2C0_SS_S CL	I2C0_SS_S DA	GND	GND	SPI0_SS_ MOSI	SPI0_SS_ CS_B[1]	SPI1_SS_ CS_B[1]	SPI1_SS_ CS_B[0]	GND	RST_B	GND	VCC_HOS T_1P8_PG
F	ADC[0]	GPIO_SS_ [5]	GPIO_SS_ [3]	GND	GND	SPI0_SS_ CS_B[0]	TDI	TDO	GND	GND	VCC_AVD _OPM_2P6	VCC_AVD _OPM_2P6
G	USB_NP	USB_DP	GND	GND	GND	GND	TRST_B	TMS	GND	PLT_REG_ EN	GND	VCC_PLT_ 1P8_IND
Н	GND	GPIO_SS_ [4]	GPIO_SS_ [2]	GND	GND	GPIO[1]	TCK	AON_GPIO [0]	AON_GPIO [4]	AON_GPIO [2]	GND	VCC_PLT_ 1P8
J	UART0_TX D	UART0_RX D	GND	GND	GND	GPIO[2]	GPIO[3]	AON_GPIO [5]	AON_GPIO [3]	AON_GPIO [1]	GND	VCC_PLT_ 3P3
K	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	VCC_PLT_ 3P3_IND
L	OSC32M_ OUT	GND	UART1_TX D	UART1_CT S	VCC_CMP	GND	GND	GND	VBAT_SO C	GND	VCCOUT_ AON_1P8	VCC_HOS T_1P8_IND
М	OSC32M_I N	GND	UART1_RX D	UART1_RT S	VCC_USB _3P3	VCC_ADC	VDD_RFS	TR_NRES ET	VBAT_SO C	GND	VCC_HOS T_1P8	VCC_HOS T_1P8
1							i					
	RF	Clock	UART	PWM	ADC	GND						
	Power	USB	SPI	JTAG	AON_GPIO	Others						

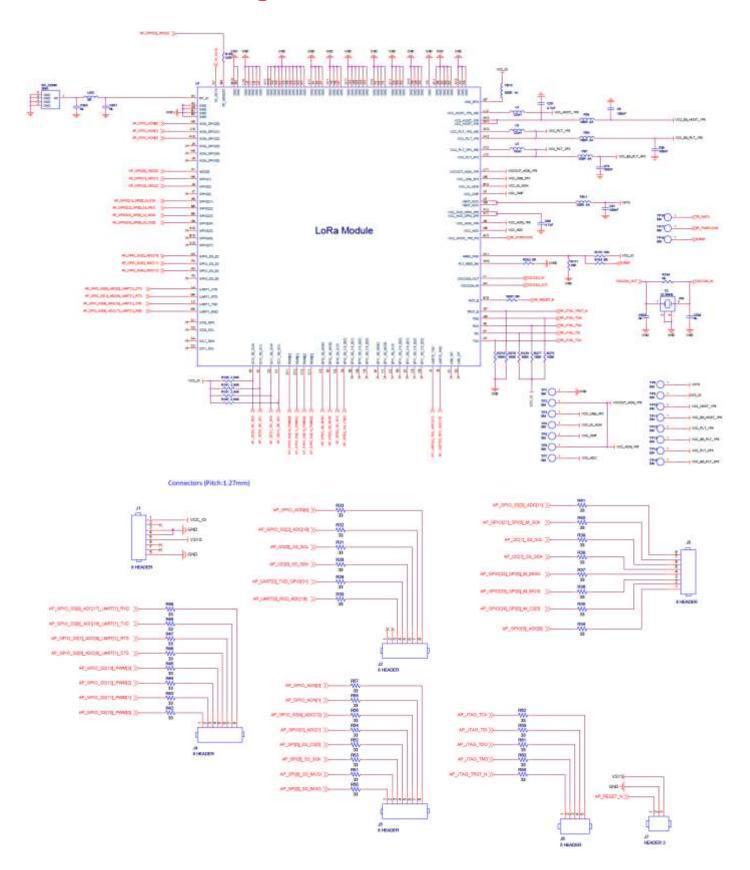


7 Recommended Footprint

Suggest on PCB: SMD (1:1)

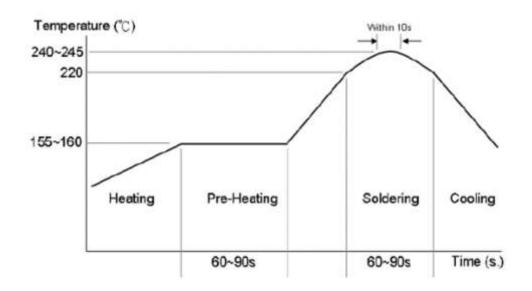


8 Reference Design Circuit





9 Recommended Reflow Profile



Profile Condition

- a. Suitable for Lead-Free solder
- b. Between 155 ~ 160°C: 60~90 sec.
- c. Above 220°C: 60 ~ 90 sec.
- d. Peak Temperature: 240 ~ 245°C (<10 sec.)

10 SiP Module Preparation

10.1 Handling

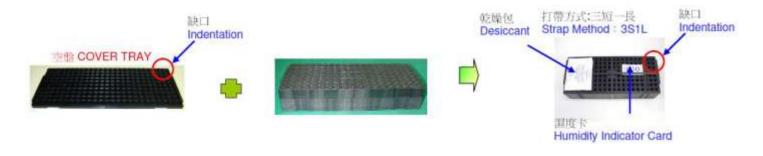
Handling the module must wear the anti-static wrist strap to avoid ESD damage. After each module is aligned and tested, it should be transport and storage with anti-static tray and packing. This protective package must be remained in suitable environment until the module is assembled and soldered onto the main board.

10.2 SMT Preparation

- 1. Calculated shelf life in sealed bag: 6 months at <40°C and <90% relative humidity (RH).
- 2. Peak package body temperature: 250°C.
- 3. After bag was opened, devices that will be subjected to reflow solder or other high temperature process must.
 - a. Mounted within: 72 hours of factory conditions <30°C /60% RH.
 - b. Stored at $\leq 10\%$ RH with N2 flow box.
- 4. Devices require baking, before mounting, if:
 - a. Package bag does not keep in vacuumed while first time open.
 - b. Humidity Indicator Card is >10% when read at 23±5°C.
 - c. Expose at 3A condition over 8 hours or Expose at 3B condition over 24 hours.
- 5. If baking is required, devices may be baked for 12 hours at 125±5°C.



11 Package Information



If the item create in barcode label, no need to fill in MSL, sealing date. Dry-pack Bag(Al-Bag) 品質檢驗章 Q.A.Inspection Passed Stamp 温度(初無資料 Temp (If no data, allow 包裝標籤 展粉紅色抗炉電氣池布 keeping empty) Indentation 1 piece of Pink Bubble Wrap Packing Label For partial bundle, please place 41PACLBW001 bubble sheet on top of the product until the product can't move inside the box. Sealing Date · 請加放41PACLBW001集出布於材料上方。直到材料







將包好的包裝盤,連同粉紅色抗靜電氣泡布放入內箱中 Put The Packaged Tray And Pink Antistatic Bubble Warp

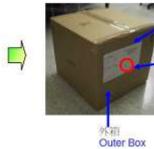


品質檢驗章 Q.A.Inspection Passed Stamp

包裝標籤貼於內意左側 Packing Label stick at the left side of inner box



外箱內未滿6盒時,請以緩衝材填塞,不須另補空盒 Please place the cushion but the empty box to fill the spare space in the outer box, if the inner box q'ty is less than 6.



ASECL Shipping Marked 品質檢驗章 Q.A.Inspection Passed Stamp

12 Document History

Date	Modifications	Version
Mar. 08, 2017	Preliminary version	1.0
Jan. 03, 2018	Add the power consumption	1.1
Feb. 27, 2018	Add low band of LoRa	1.2







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