ASSEMBLER INSTRUCTION SET SUMMARY

Mnemonic	Operands	Description
BYTE-ORIENTED FILE REGISTER OPERATIONS		
ADDWF	f, d	Add W and f
ANDWF	f, d	AND W with f
CLRF	f	Clear f
CLRW	-	Clear W
COMF	d	Complement f
DECF	f, d	Decrement f
DECFSZ	f, d	Decrement f, Skip if 0
INCF	f, d	Increment f
INCFSZ	f, d	Increment f, Skip if 0
IORWF	f, d	Inclusive OR W with f
MOVF	f, d	Move f
MOVWF	f	Move W to f
MOVFW	f	Move f to W
NOP	-	No Operation
RLF	d	Rotate Left f through Carry
RRF	f, d	Rotate Right f through Carry
SUBWF	f, d	Subtract W from f
SWAPF	f, d	Swap nibbles in f
XORWF	f, d	Exclusive OR W with f
BIT-ORIENTED FILE REGISTER OPERATIONS		
BCF	f, b	Bit Clear f
BSF	f, b	Bit Set f
BTFSC	f, b	Bit Test f, Skip if Clear
BTFSS	f, b	Bit Test f, Skip if Set
LITERAL AND CONTROL OPERATIONS		
ADDLW	k	Add literal and W
ANDLW	k	AND literal with W
CALL	k	Call subroutine
CLRWDT	-	Clear Watchdog Timer
GOTO	k	Go to address
IORLW	k	Inclusive OR literal with W
MOVLW	k	Move literal to W
RETFIE	-	Return from interrupt
RETLW	k	Return with literal in W
RETURN	-	Return from Subroutine
SLEEP	-	Go into standby mode
SUBLW	k	Subtract W from literal
XORLW	k	Exclusive OR literal with W

where   f = file register name

               k = value of literal

               b = bit number (0 - 7)

               d = result position (W or f)