

# CME 2003 Logic Design

## Experiment 2

### Preliminary Work

Find and examine the datasheet of IC 7400. Bring the datasheet (74LS00 or 74HC00) with you to the lab, 1 per each group.

### Lab Work

Design OR ( $A+B$ ) gate entirely from NAND gates.

### Truth Table for NAND Gate

| A | B | F |
|---|---|---|
| 0 | 0 | 1 |
| 0 | 1 | 1 |
| 1 | 0 | 1 |
| 1 | 1 | 0 |