

# **Intel® Math Kernel Library**

**Getting Started Tutorial: Using the Intel® Math Kernel Library for Matrix Multiplication** 

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# Overview

Discover how to incorporate core math functions from the Intel® Math Kernel Library (Intel® MKL) to improve the performance of your application.

| About This<br>Tutorial | This tutorial demonstrates how to use Intel MKL in your applications:  • Multiplying matrices using Intel MKL routines  • Measuring performance of matrix multiplication  • Controlling threading   |
|------------------------|---|
| Estimated<br>Duration  | 10-20 minutes.  |
| Learning<br>Objectives | After you complete this tutorial, you should be able to:  Use Intel MKL routines for linear algebra Compile and link your code Measure performance using support functions Understand the impact of threading on Intel MKL performance Control threading for Intel MKL functions  |
| More Resources         | This tutorial uses the C language, but the concepts and procedures in this tutorial apply regardless of programming language. A similar tutorial using a sample application in another programming language may be available at http://software.intel.com/en-us/articles/intel-software-product-tutorials/. This site also offers a printable version (PDF) of tutorials.  In addition, you can find more resources at http://software.intel.com/en-us/articles/intel-mkl/. |

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Intel® Math Kernel Library Getting Started Tutorial: Using the Intel® Math Kernel Library for Matrix Multiplication

# Introduction to the Intel® Math Kernel Library

Use the Intel Math Kernel Library (Intel MKL) when you need to perform computations with high performance. Intel MKL offers highly-optimized and extensively threaded routines which implement many types of operations.

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| Linear Algebra   | Fast Fourier<br>Transforms   | Summary<br>Statistics   | Data Fitting  | Other<br>Components   |
|--|--|---|---|---|
| BLAS LAPACK/ SCALAPACK PARDISO* Iterative sparse solvers | <ul> <li>Multi-<br/>dimensional (up<br/>to 7D) FFTs</li> <li>FFTW interfaces</li> <li>Cluster FFT</li> </ul> | <ul> <li>Kurtosis</li> <li>Variation coefficient</li> <li>Quantiles, order statistics</li> <li>Min/max</li> <li>Variance/ covariance</li> <li></li> </ul> | <ul> <li>Splines</li> <li>Interpolation</li> <li>Cell search</li> </ul> | <ul> <li>Vector Math</li> <li>Trigonometric</li> <li>Hyperbolic</li> <li>Exponential, Logarithmic</li> <li>Power/Root</li> <li>Rounding</li> <li>Vector Random Number Generators</li> <li>Congruential</li> <li>Recursive</li> <li>Wichmann-Hill</li> <li>Mersenne Twister</li> <li>Sobol</li> <li>Niederreiter</li> <li>RDRAND-based</li> <li>Poisson Solvers</li> <li>Optimization Solvers</li> </ul> |

# **Exploring Basic Linear Algebra Subprograms (BLAS)**

One key area is the Basic Linear Algebra Subprograms (BLAS), which perform a variety of vector and matrix operations. This tutorial uses the <code>dgemm</code> routine to demonstrate how to perform matrix multiplication as efficiently as possible.

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# Multiplying Matrices Using dgemm

Intel MKL provides several routines for multiplying matrices. The most widely used is the dgemm routine, which calculates the product of double precision matrices:

$$C \leftarrow \alpha A * B + \beta C$$

The dgemm routine can perform several calculations. For example, you can perform this operation with the transpose or conjugate transpose of *A* and *B*. The complete details of capabilities of the dgemm routine and all of its arguments can be found in the cblas\_?gemm topic in the *Intel Math Kernel Library Developer Reference*.

# **Use dgemm to Multiply Matrices**

This exercise demonstrates declaring variables, storing matrix values in the arrays, and calling dgemm to compute the product of the matrices. The arrays are used to store these matrices:

$$A = \begin{bmatrix} 1.0 & 2.0 & 3.0 & \cdots & 1000.0 \\ 1001.0 & 1002.0 & 1003.0 & \cdots & 2000.0 \\ 2001.0 & 2002.0 & 2003.0 & \cdots & 3000.0 \\ \vdots & \vdots & \vdots & \ddots & \vdots \\ 999001.0 & 999002.0 & 999003.0 & \cdots & 1000000.0 \end{bmatrix} \\ B = \begin{bmatrix} -1.0 & -2.0 & -3.0 & \cdots & -100000 \\ -1001.0 & -1002.0 & -1003.0 & \cdots & -2000.0 \\ -2001.0 & -2002.0 & -2003.0 & \cdots & -3000.0 \\ \vdots & \vdots & \vdots & \ddots & \vdots \\ -999001.0 & -999002.0 & -999003.0 & \cdots & -1000000.0 \end{bmatrix}$$

The one-dimensional arrays in the exercises store the matrices by placing the elements of each column in successive cells of the arrays.

### NOTE

The C source code for the exercises in this tutorial can be downloaded from https://software.intel.com/en-us/product-code-samples.

```
alpha = 1.0; beta = 0.0;
   printf (" Allocating memory for matrices aligned on 64-byte boundary for better \n"
            " performance \n\n");
   A = (double *)mkl malloc( m*k*sizeof( double ), 64 );
   B = (double *)mkl malloc( k*n*sizeof( double ), 64 );
   C = (double *)mkl malloc(m*n*sizeof(double), 64);
   if (A == NULL | | B == NULL | | C == NULL) {
     printf( "\n ERROR: Can't allocate memory for matrices. Aborting... \n\n");
     mkl free(A);
     mkl free(B);
     mkl free(C);
     return 1;
   }
   printf (" Intializing matrix data \n\n");
   for (i = 0; i < (m*k); i++) {
       A[i] = (double)(i+1);
   for (i = 0; i < (k*n); i++) {
       B[i] = (double)(-i-1);
   for (i = 0; i < (m*n); i++) {
       C[i] = 0.0;
   }
   printf (" Computing matrix product using Intel(R) MKL dgemm function via CBLAS interface \n
\n");
   cblas dgemm (CblasRowMajor, CblasNoTrans, CblasNoTrans,
                m, n, k, alpha, A, k, B, n, beta, C, n);
   printf ("\n Computations completed.\n\n");
   printf (" Top left corner of matrix A: \n");
   for (i=0; i < min(m, 6); i++) {
     for (j=0; j < min(k, 6); j++) {
       printf ("%12.0f", A[j+i*k]);
     printf ("\n");
   printf ("\n Top left corner of matrix B: \n");
   for (i=0; i < min(k, 6); i++) {
     for (j=0; j < min(n, 6); j++) {
       printf ("%12.0f", B[j+i*n]);
     printf ("\n");
   printf ("\n Top left corner of matrix C: \n");
   for (i=0; i < min(m, 6); i++) {
     for (j=0; j < min(n,6); j++) {
       printf ("%12.5G", C[j+i*n]);
     printf ("\n");
   }
   printf ("\n Deallocating memory \n\n");
```

```
mkl_free(A);
mkl_free(B);
mkl_free(C);

printf (" Example completed. \n\n");
return 0;
}
```

## **NOTE**

This exercise illustrates how to call the <code>dgemm</code> routine. An actual application would make use of the result of the matrix multiplication.

This call to the dgemm routine multiplies the matrices:

```
cblas_dgemm(CblasRowMajor, CblasNoTrans, CblasNoTrans, m, n, k, alpha, A, k, B, n, beta, C, n);
```

| The arguments provide options for how Intel MKL performs the operation. In this case: |   |  |  |
|---|---|--|--|
| CblasRowMajor   | Indicates that the matrices are stored in row major order, with the elements of each row of the matrix stored contiguously as shown in the figure above.  |  |  |
| CblasNoTrans  | Enumeration type indicating that the matrices $\boldsymbol{A}$ and $\boldsymbol{B}$ should not be transposed or conjugate transposed before multiplication.   |  |  |
| m, n, k   | Integers indicating the size of the matrices:   |  |  |
|   | • A: m rows by k columns  |  |  |
|   | • B: k rows by n columns  |  |  |
|   | • C: m rows by n columns  |  |  |
| alpha   | Real value used to scale the product of matrices A and B.   |  |  |
| A   | Array used to store matrix A.   |  |  |
| k   | Leading dimension of array $\mathbb{A}$ , or the number of elements between successive rows (for row major storage) in memory. In the case of this exercise the leading dimension is the same as the number of columns. |  |  |
| В   | Array used to store matrix B.   |  |  |
| n   | Leading dimension of array $\ B$ , or the number of elements between successive rows (for row major storage) in memory. In the case of this exercise the leading dimension is the same as the number of columns.        |  |  |

beta Real value used to scale matrix C.

C Array used to store matrix *C*.

Leading dimension of array C, or the number of elements between successive rows (for row major storage) in memory. In the case of this exercise the leading dimension is the same as the number of columns.

# **Compile and Link Your Code**

Intel MKL provides many options for creating code for multiple processors and operating systems, compatible with different compilers and third-party libraries, and with different interfaces. To compile and link the exercises in this tutorial with Intel® Parallel Studio XE Composer Edition, type

- Windows\* OS: icl /Qmkl src\dgemm example.c
- Linux\* OS, macOS\*: icc -mkl src/dgemm example.c

Alternatively, you can use the supplied build scripts to build and run the executables.

Windows\* OS:

```
build
build run_dgemm_example
```

Linux\* OS, macOS\*:

```
make run_dgemm_example
```

For the executables in this tutorial, the build scripts are named:

| Example                          | Executable                         |  |  |
|----------------------------------|------------------------------------|--|--|
| dgemm_example.c                  | run_dgemm_example                  |  |  |
| dgemm_with_timing.c              | run_dgemm_with_timing              |  |  |
| matrix_multiplication.c          | run_matrix_multiplication          |  |  |
| dgemm_threading_effect_example.c | run_dgemm_threading_effect_example |  |  |

## **NOTE**

This assumes that you have installed Intel MKL and set environment variables as described in .

For other compilers, use the Intel MKL Link Line Advisor to generate a command line to compile and link the exercises in this tutorial: http://software.intel.com/en-us/articles/intel-mkl-link-line-advisor/.

After compiling and linking, execute the resulting executable file, named dgemm\_example.exe on Windows\* OS or a.out on Linux\* OS and macOS\*.

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# Measuring Performance with Intel® MKL Support Functions

Intel MKL provides functions to measure performance. This provides a way of quantifying the performance improvement resulting from using Intel MKL routines in this tutorial.

# **Measure Performance of dgemm**

Use the dsecnd routine to return the elapsed CPU time in seconds.

#### **NOTE**

The quick execution of the <code>dgemm</code> routine makes it difficult to measure its speed, even for an operation on a large matrix. For this reason, the exercises perform the multiplication multiple times. You should set the <code>value</code> of the <code>LOOP\_COUNT</code> constant so that the total execution time is about one second.

## **Measure Performance Without Using dgemm**

In order to show the improvement resulting from using <code>dgemm</code>, perform the same measurement, but use a triply-nested loop to multiply the matrices.

```
for (r = 0; r < LOOP_COUNT; r++) {
    for (i = 0; i < m; i++) {
        sum = 0.0;
        sum + 0.0;
        for (1 = 0; 1 < k; 1++)
            sum + A[k*i+1] * B[n*1+j];
        C[n*i+j] = sum;
        }
    }
}
s_elapsed = (dsecnd() - s_initial) / LOOP_COUNT;

printf (" == Matrix multiplication using triple nested loop completed == \n"
        " == at %.5f milliseconds == \n\n", (s_elapsed * 1000));</pre>
```

Compare the results in the first exercise using dgemm to the results of the second exercise without using dgemm.

You can find more information about measuring Intel MKL performance from the article "A simple example to measure the performance of an Intel MKL function" in the Intel Math Kernel Library Knowledge Base.

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# Measuring Effect of Threading on dgemm

By default, Intel MKL uses n threads, where n is the number of physical cores on the system. By restricting the number of threads and measuring the change in performance of dgemm, this exercise shows how threading impacts performance.

# **Limit the Number of Cores Used for dgemm**

This exercise uses the mkl\_set\_num\_threads routine to override the default number of threads, and mkl\_get\_max threads to determine the maximum number of threads.

```
/* C source code is found in dgemm threading effect example.c */
   printf (" Finding max number of threads Intel(R) MKL can use for parallel runs \n\n");
   max threads = mkl get max threads();
   printf (" Running Intel(R) MKL from 1 to %i threads \n\n", max threads);
   for (i = 1; i <= max threads; i++) {
       for (j = 0; j < (m*n); j++)
           C[j] = 0.0;
       printf (" Requesting Intel(R) MKL to use %i thread(s) \n\n", i);
       mkl set num threads(i);
       printf (" Making the first run of matrix product using Intel(R) MKL dgemm function \n"
                " via CBLAS interface to get stable run time measurements \n\n");
       cblas dgemm(CblasRowMajor, CblasNoTrans, CblasNoTrans,
                   m, n, k, alpha, A, k, B, n, beta, C, n);
       printf (" Measuring performance of matrix product using Intel(R) MKL dgemm function \n"
                " via CBLAS interface on %i thread(s) \n\n", i);
       s initial = dsecnd();
       for (r = 0; r < LOOP COUNT; r++) {
           cblas dgemm (CblasRowMajor, CblasNoTrans, CblasNoTrans,
                       m, n, k, alpha, A, k, B, n, beta, C, n);
       s elapsed = (dsecnd() - s initial) / LOOP COUNT;
       printf (" == Matrix multiplication using Intel(R) MKL dgemm completed ==\n"
                " == at %.5f milliseconds using %d thread(s) ==\n'', (s elapsed * 1000), i);
    }
```

Examine the results shown and notice that time to multiply the matrices decreases as the number of threads increases. If you try to run this exercise with more than the number of threads returned by  $mkl_get_max_threads$ , you might see performance degrade when you use more threads than physical cores.

#### NOTE

You can see specific performance results for dgemm at the Details tab at http://software.intel.com/en-us/articles/intel-mkl.

# **See Also**

Intel MKL Documentation for additional Intel MKL documentation, including the *Intel MKL Developer Reference* and the *Intel MKL Developer Guide*.

Intel Math Kernel Library Knowledge Base for articles describing usage of Intel MKL functionality. Click here for more Getting Started Tutorials

# Other Areas to Explore

The exercises so far have given the basic ideas needed to get started with Intel MKL, but there are plenty of other areas to explore. The following are some controls, interfaces, and topics which you might find worth investigating further.

### Support functions

The second exercise shows how to use the timing functions and the third exercise shows the use of threading control functions. Acquaint yourself with other support functions by referring to the "Support functions" chapter of the *Intel MKL Developer Reference*:

- Support functions for Conditional Numerical Reproducibility (CNR)
  - These functions provide the means to balance reproducibility with performance in certain conditions.
- Memory functions

These functions provide support for allocating and freeing memory. The allocation functions allow proper alignment of memory to ensure reproducibility when used together with CBWR functions.

• Error handling functions

The xerbla function is used by BLAS, LAPACK, VML, and VSL to report errors.

## Linking and interfaces

The ILP64 interface

Most users call the interface of Intel MKL that takes 32-bit integers for size parameters, but increased memory and also some legacy code requires 64-bit integers. Read more about the ILP64 interface and the libraries and functions supporting it in the *Intel MKL Developer Guide*.

• Single Dynamic Library (SDL) linking model

Intel MKL has two ways to link to dynamic libraries. The newest of these models is the best option for those calling Intel MKL from managed runtime libraries and is easy to link, but requires some functions calls to use non-default interfaces (for example, ILP64). See the *Intel MKL Developer Guide* for more information on Intel MKL linking models.

### Miscellaneous

• Environment variables

Many controls in Intel MKL have both environment variables and functional versions. In all cases the function overrides the behavior of the environment variable. If you do not want the behavior to change based on an environment variable in a particular case, use the function call to ensure the desired setting. See the *Intel MKL Developer Guide* for descriptions of the environment variables used by Intel MKL.

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