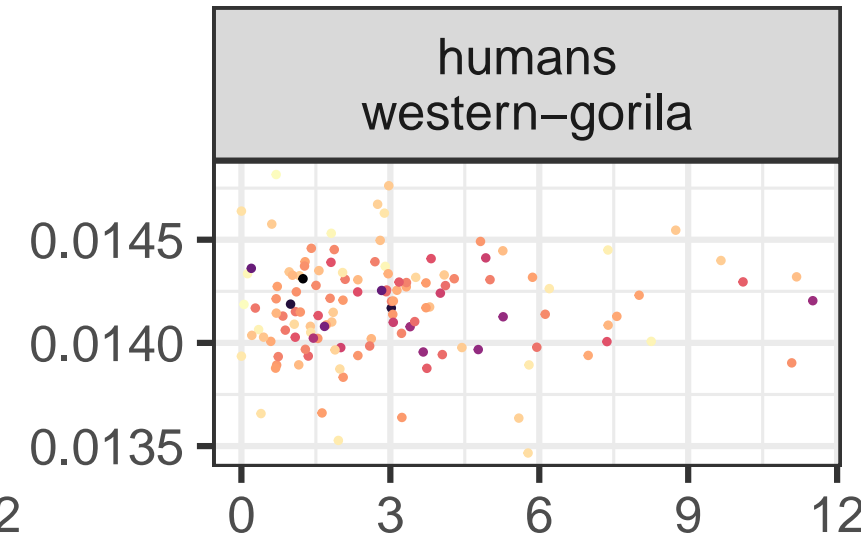rep_0_stat–win–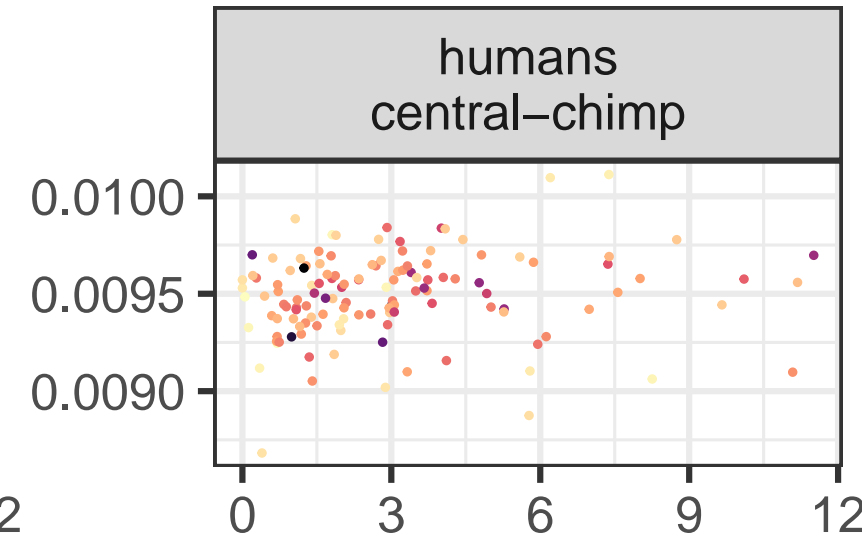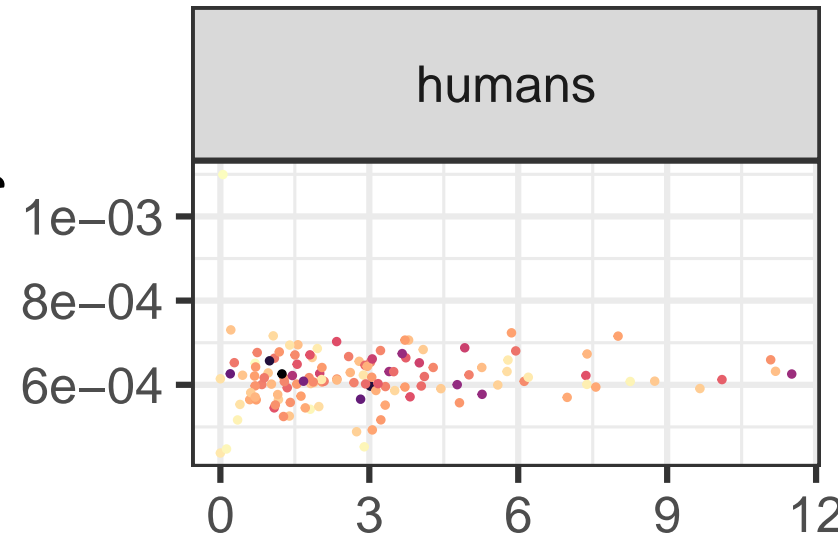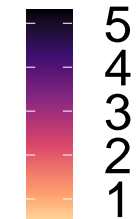size_1000000_resc_1_sim–win–len_–1_pad_0_del–rate_0_del–s_0_pos–rate_0_pos–s_0