

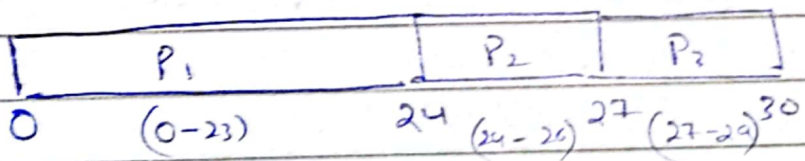
Date _____

NUMERICALS ::

Process	Burst Time
P_1	24
P_2	3
P_3	3

* Suppose the processes arrive in the same order as given.

Suppose Arrival time is 0



Completion time CS	Turn Around time TAT = CS - AT	WT
23	$= 23 - 0 = 23$	0
26	$= 26 - 24 = 2$	23
29	$= 29 - 27 = 2$	26

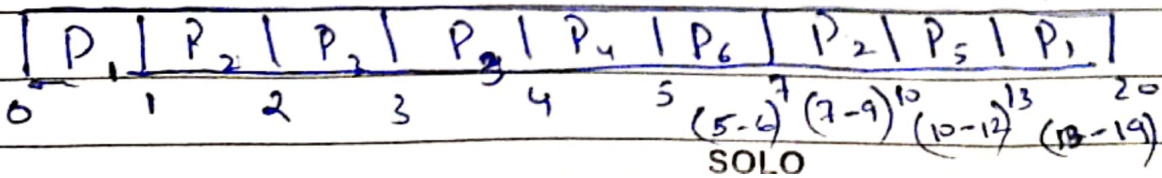
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Shortest Remaining Time First (SRTF) :-
(preemptive version)

Assume $AT = 0$.

Process	AT	BT	CT	TAT = CT - AT	WT
P ₁	0	8	17	17 - 0 = 17	10
P ₂	1	4	5	5 - 1 = 4	2
P ₃	2	9	25	25 - 2 = 23	17
P ₄	3	3	10	10 - 3 = 7	5

Process	AT	BT	CT	TAT = CT - AT	WT
P ₁	0	8			
P ₂	1	4			
P ₃	2	2			
P ₄	3	1			
P ₅	4	3			
P ₆	5	2			



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Assume $HS = 0$.

Process	AT	BT
P_1	0	7 0
P_2	1	8 0
P_3	2	3 0
P_4	3	4 0
P_5	4	2 0
P_6	5	1 0

First Come First Serve (FCFS) ::

P_1	P_2	P_3	P_4	P_5	P_6
0	7	12	15	16	18
(0-0)	(7-11)	(12-14)		(16-17)	

Shortest Job First (SJF) :: Assume $AT = 0$

P_1	P_2	P_3	P_4	P_5	P_6	P_3	P_5	P_2	P_1
0	1	2	3	4					

P_1	P_4	P_6	P_5	P_3	P_2
0	7	8	9	11	14
(0-4)			(9-10)	(11-13)	(14-18)

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(SRTF)

P_1	P_2	P_3	P_4	P_3	P_6	P_5	P_2	P_1
0	1	2	3	4	6	7	9	13
				(4-3)		(7-2)	(9-12)	(13-13)

P_1	P_2	P_3	P_4	P_5	P_6	P_5	P_2	P_2	P_1
0	1	2	3	4	5	6	7	9	13

				19	CT	TAT	WT
P_1	0	4	6	19	19	19	19
P_2	1	3	4	13	13	13	13
P_3	2	3	3	6	6	6	6
P_4	3	4	6	4	4	4	4
P_5	4	2	4	9	9	9	9
P_6	5	4	0	7	7	7	7

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Priority Program ~~Cover~~ Allocation:-

Process	P	AT	BT	(CT-AT)	(TAT-BT)
				TAT	WT
P ₁	2	0	4 3	0	
P ₂	4	1	2 1	0	
P ₃	6	2	3 2 0		
P ₄	10	3	3 7 0		
P ₅	8	4	1 0		
P ₆	12 (high)	5	4 0		
P ₇	9	6	6 0		

P_1	P_2	P_3	P_4	P_6	P_5	P_7	P_5	P_3	P_2	P_1	
0	1	2	3	5	9	12	18	19	21	22	25

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Round Robin :: (Time Quanta / Time Slice) :

Time Quanta = 2.

Process	AT	BT	CT	#	WT	TAT
P ₁	0	8 2	14		9	14
P ₂	1	8 1	12		10	13
P ₃	2	1 0	5		2	3
P ₄	3	2 0	7		2	4
P ₅	4	2 0	13		6	9

P_1	P_2	P_3	P_4	P_5	P_1	P_2	P_3	P_1	
0	2	4	5	7	9	11	12	13	14

Time Quanta = 4.

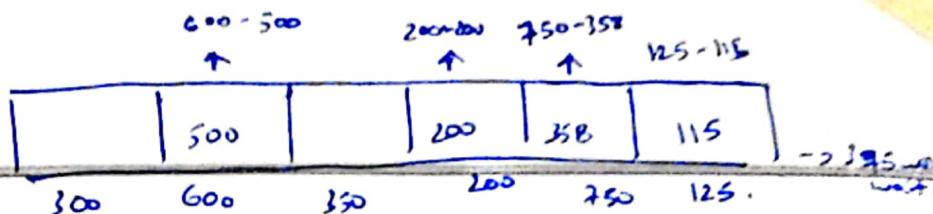
P	AT	BT	CT	TAT	WT
P ₁	0	10 2			
P ₂	1	9 1			
P ₃	2	12 0			
P ₄	3	6 0			

P ₁	P ₂	P ₃	P ₄	P ₁	P ₂	P ₃	P ₄	P ₁	P ₂	P ₃
0	4	8	12	16	20	24	28	32	36	40

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FF 569
RF 483
WF 659

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Memory Partitions: 300KB, 600KB, 350KB, 200KB, 750KB, 125KB
Process Size: 115KB, 500KB, 358KB, 200KB, 375KB

First Fit

Worst Fit

Best Fit

115KB allocated to 300KB
 500KB allocated to 600KB
 358KB & 750KB = 750-358
 200KB & 350KB = 350-200
 375KB waiting...

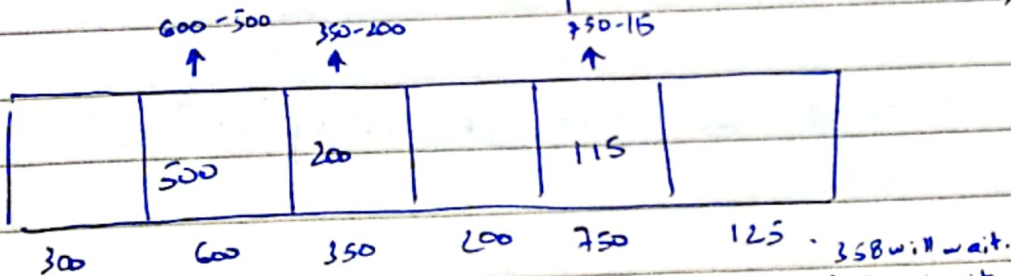
115KB allocated to 750KB
 500KB & 600KB = 600-500
 358KB waiting...
 200KB & 350KB = 350-200
 375KB waiting...

115KB allocated to 125KB
 500KB & 600KB = 600-500
 358KB & 750KB = 750-358
 200KB & 200KB = 200-200
 375KB waiting...

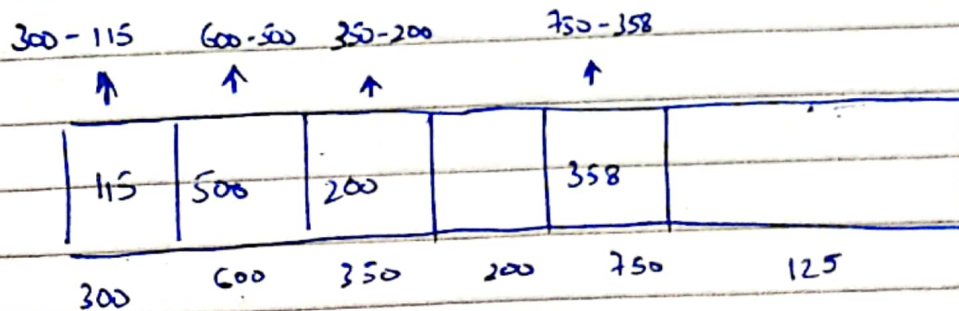
829KB
 ←
 wastage

825KB

502KB



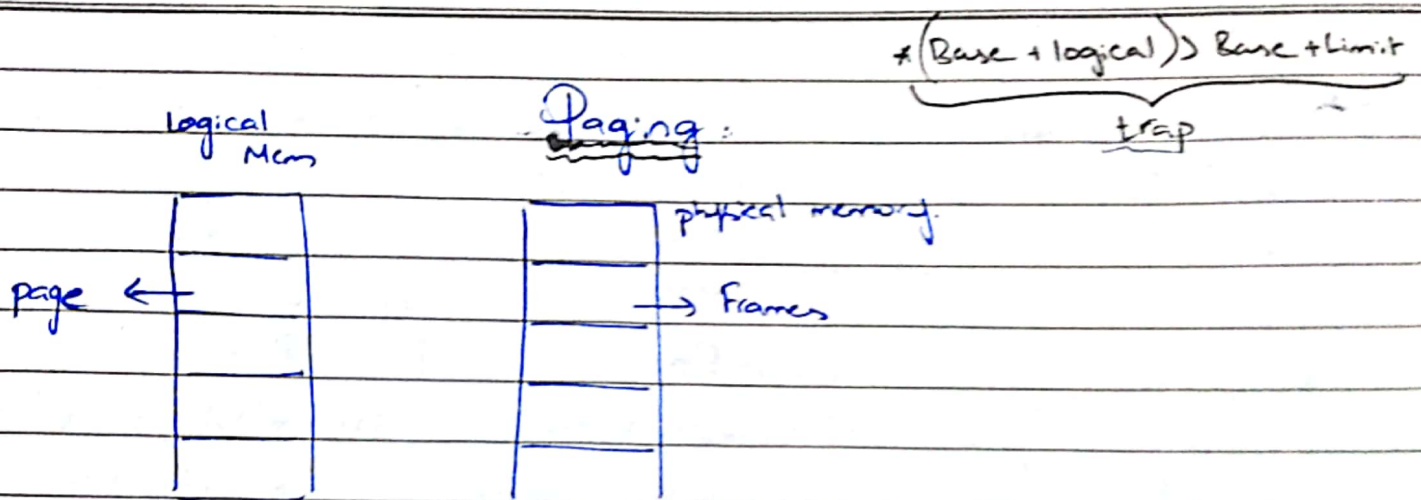
115KB	→ 125KB
358KB	→ 750KB
200KB	→ 200KB
	→ 350KB
500KB	→ 600KB
	→ 300KB



375KB waiting

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$$\# \text{ of pages} = \frac{\text{Process size}}{\text{Page size}}$$

Example:

page size = 2,048 bytes

process size = 72,766 bytes.

$$\# \text{ of pages} = \frac{72,766}{2048} \Rightarrow 35 \text{ pages} + 1086 \text{ bytes. (can't be in decimal)}$$

Internal fragmentation of $2048 - 1086 \Rightarrow 962$ Bytes. (wastage).
↓
internal fragmentation.

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Structure of Page table ::

(page size all numbers should be in 2^n (2^{10} power))

1) Hierarchical ::

logical Address = 2^{32} bit, page size = $4K \Rightarrow 12$

$P = ?$, $d = ?$

(m-n) $P = 2^{20} \Rightarrow 20$ bits. $2^{12} \rightarrow$ page size.

$P = 20$ bits.
 $d = \text{logical add} - \text{page size}$
 $d = 32 - 20 \Rightarrow 12$ bits.

2) Hash page table ::

PN # 02.

(if given indecimal. Then.
for $P = \text{given} / \text{page size}$
 $d = \text{given mod } 1024$.)

pg table \rightarrow hash value
cluster of pages.

3) Inverted page table

page \rightarrow pin

PN # 03.

Q2 Consider a ~~logical~~ logical/virtual address of 64 pages of 1024 words each mapped onto physical memory of 32 frames.

a) Bits in logical address = ?

$= (6 + 10) \Rightarrow 16 \Rightarrow 2^{16}$

page table entries = $\frac{2^{16}}{2^{10}} \Rightarrow 2^6 \Rightarrow 64$ entries.

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b) Bits in physical address = ?

$$\Rightarrow (5 + 10) = 15 \Rightarrow 2^{15}$$

$$\text{Page table entries} = \frac{2^{15}}{2^{10}} \Rightarrow 2^5$$

$$\text{logical Add.} = 16 \text{ bits. } (2^{16}) \Rightarrow 2^m$$

$$\text{page size} = 5 \text{ bits. } (2^5) \Rightarrow 2^n$$

$$p = ? , d = ?$$

$$p = m - n , d = \text{logical Address} - p =$$

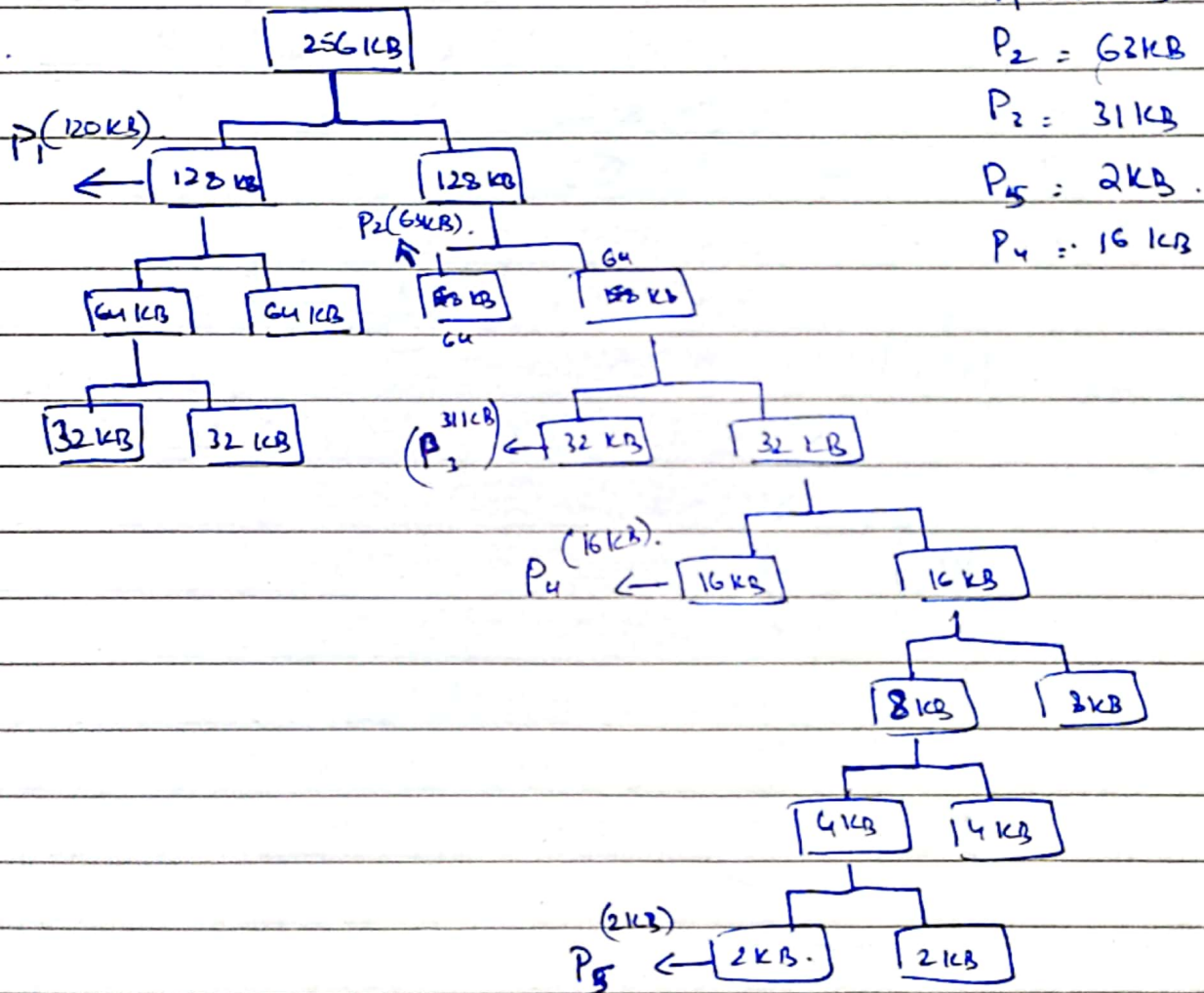
$$p = 11 , d = 16 - 11 \Rightarrow d = 5.$$

$$\text{Page entries} = \frac{LA \Rightarrow}{PS \Rightarrow}$$

$$= \frac{PA}{PS} \Rightarrow \text{Frames} \quad \text{Heavy}$$

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Buddy System Allocator ::



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Resource Allocation Graph.

1

One instance of R_1

2 \leftarrow R_2

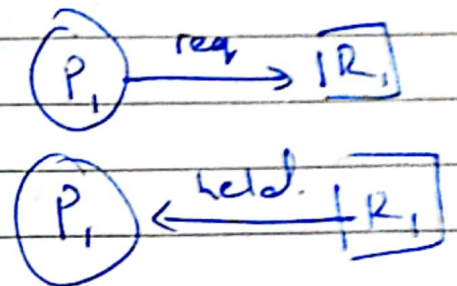
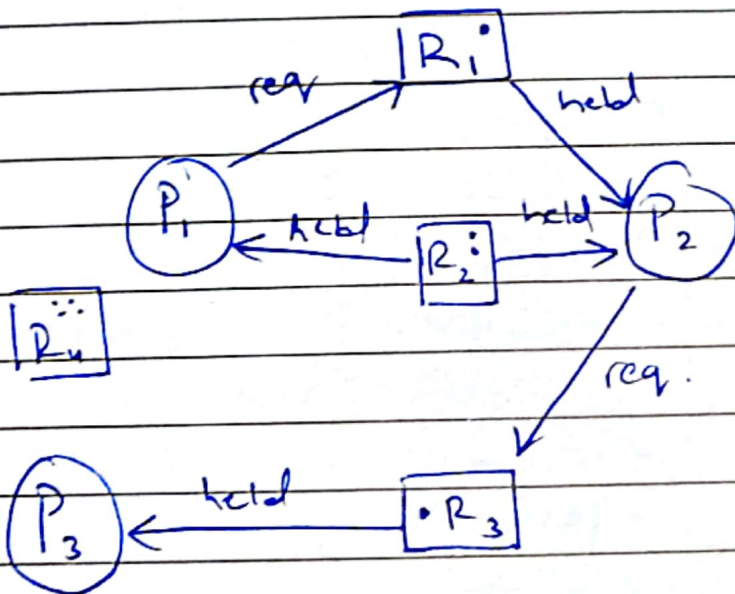
1 \leftarrow R_3

4 \leftarrow R_4

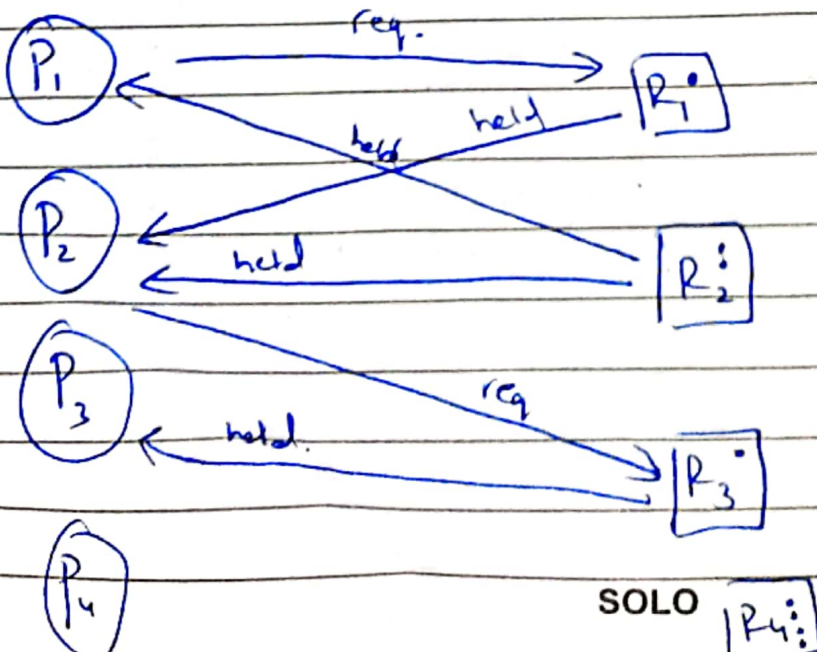
P_1 holding R_2 & waiting for R_1

P_2 R_1 & R_2 \leftarrow R_3

P_3 \leftarrow R_3



OR

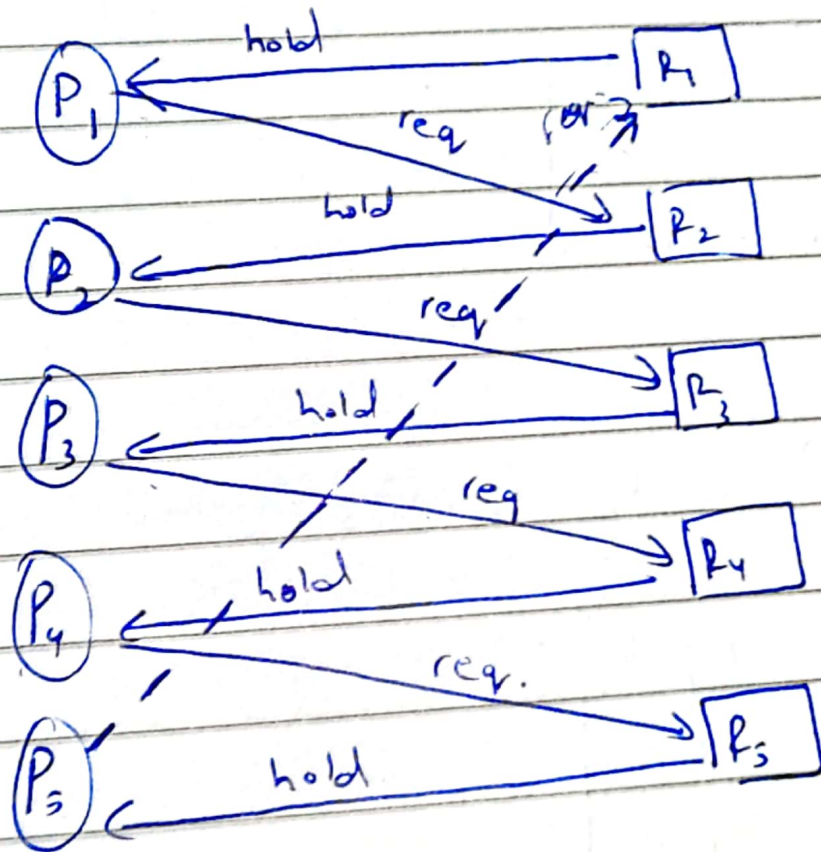


SOLO P_4

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Dining Philosopher's Problem.
(RAG).

(5 philosopher, 5 Chopsticks(res)).



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FIFO, LRU, OPTIMAL.

FIFO : $\downarrow \downarrow \downarrow H \downarrow \downarrow \downarrow$
 1, 3, 0, 3, 5, 6, 3. \Rightarrow 6 page fault.

2	0	2	0	2	3
1	3	1	3	1	6
0	1	0	5	0	5

LRU : $\downarrow \downarrow \downarrow H \downarrow \downarrow H$
 1, 3, 0, 3, 5, 6, 3 \Rightarrow 5 page fault

2	0	2	0	2	6
1	3	1	3	1	3
0	1	0	5	0	5

Optimal : ~~1, 3, 0, 3, 5, 6, 3~~ $\downarrow \downarrow \downarrow \downarrow H \downarrow H \downarrow H H H H H H$
 7, 0, 1, 2, 0, 3, 0, 4, 2, 3, 0, 3, 2, 3.

3	2	3	2	3	2
2	1	2	1	2	4
1	0	1	0	1	0
0	7	0	3	0	3

5 page fault.