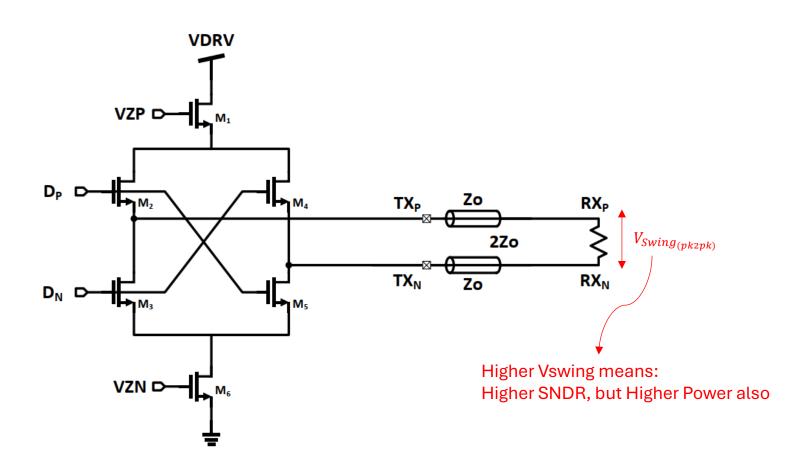
# Tx Driver - VM

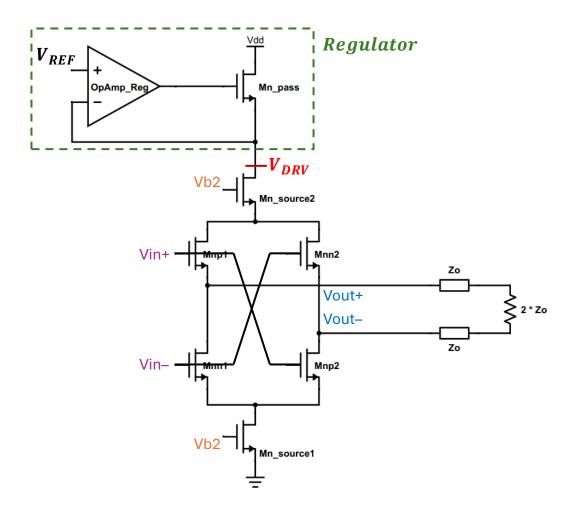
1 Gbps

Muhammad Aldacher

## Voltage-Mode Driver (VM)



### 1) Without Replica



$$V_{Swing_{(pk2pk)}} = V_{DRV} = V_{REF}$$

For 
$$V_{Swing_{(pk2pk)}} = 500 \text{ mV}$$
:  
 $V_{REF} = V_{DRV} = \mathbf{500 mV}$ 

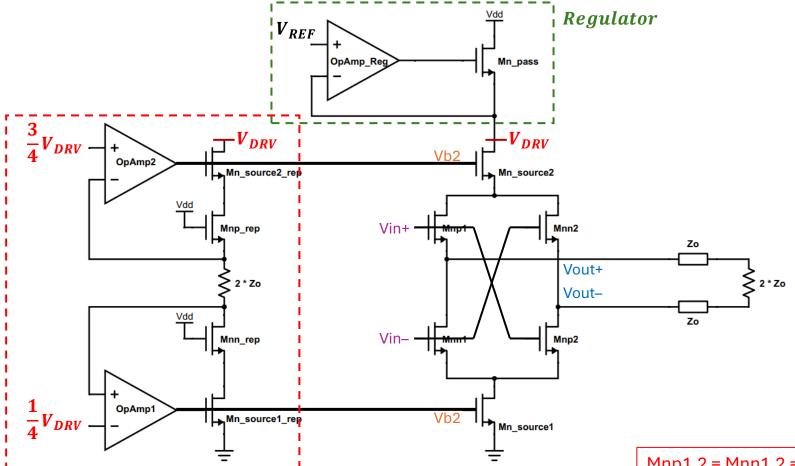
#### **Device Sizing:**

- ► Input devices (Mnp1,2 & Mnn1,2) act as switches, so we should increase W (reducing Ron) to improve headroom.
- Bias devices (Mn\_source1,2) should have their W & gate voltages Vb1,2 adjusted so that the output voltages would be at ¾ VDRV & ¼ VDRV.

```
Mnp1,2 = Mnn1,2 : L = L_{MIN} , m=100x Mn_source1,2 : L = L_{MIN} , m=200x
```

## 2) With Replica

$$V_{Swing_{(pk2pk)}} = V_{DRV} = V_{REF}$$



For the L of the Bias devices (Mn\_source1,2), We can use minimum L, because the feedback loops in the replica branch will adjust the currents to give the correct output swing anyway.

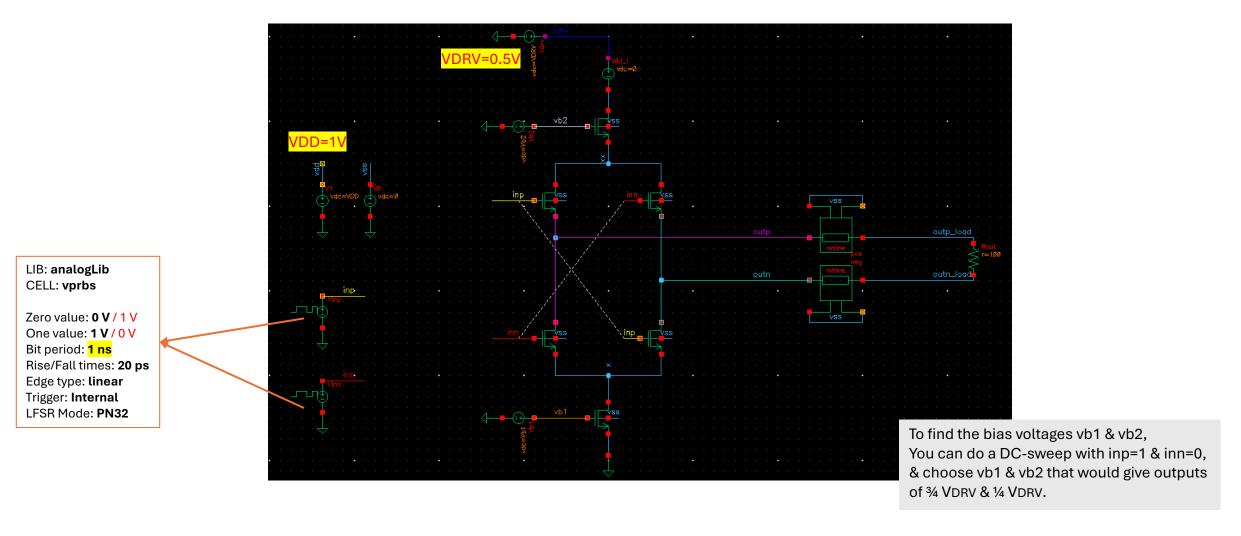
```
Mnp1,2 = Mnn1,2 = Mnp1,2_rep = Mnn1,2_rep : L = L_{MIN} , m=100x Mn_source1,2 = Mn_source1,2_rep : L = L_{MIN} , m=200x
```

# Testbenchs & Setups

- VDD = 1V
- Data-Rate = 1 Gb/s
- VSWING = 0.5V (pk2pk)

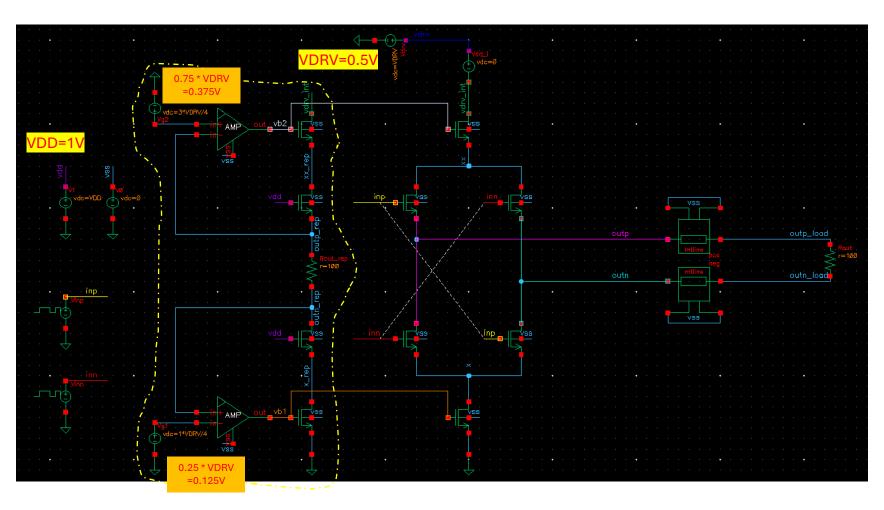
### Testbench

## A) Without Replica



### Testbench

## B) With Replica

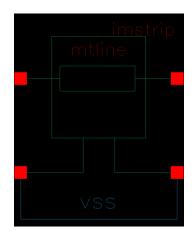


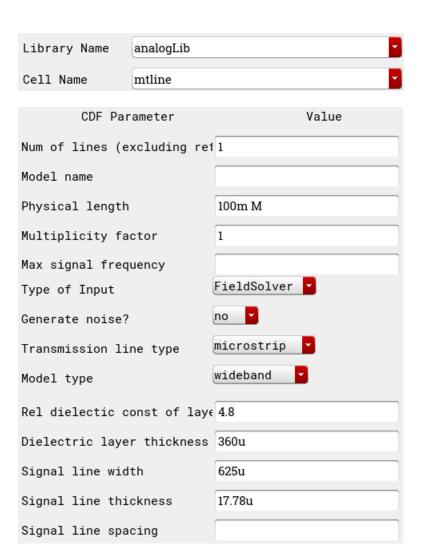
## Channel Settings (mtline)

#### → For Zo = 50 Ohms:

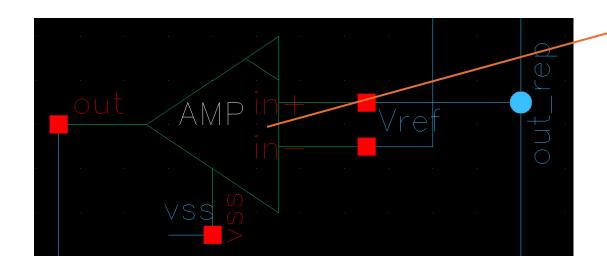
$$Z_0 \approx \frac{87}{\sqrt{\varepsilon_r + 1.41}} \ln \left( \frac{5.98 \, H}{0.8 \, W + T} \right)$$

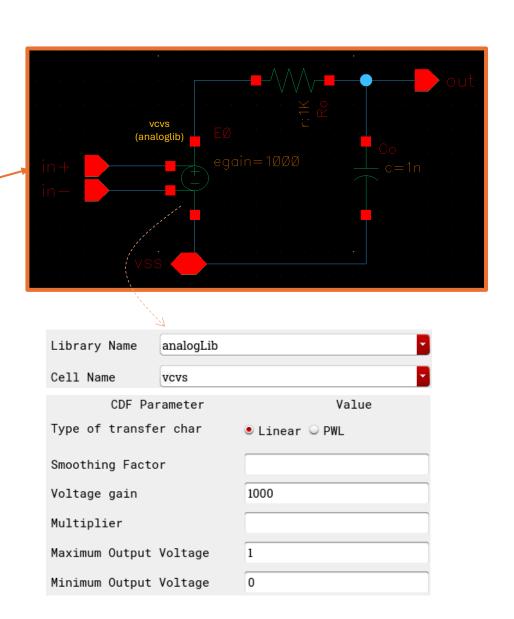
Dielectric\_const =  $\varepsilon_r$  = 4.8 Dielectric\_thickness = H = 360u Line\_width = W = 625u Line\_thickness = T = 17.78u



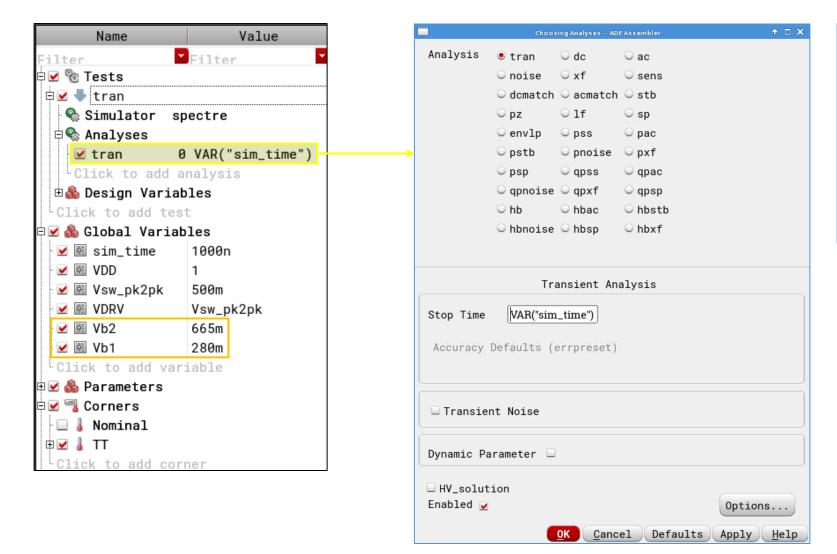


## Ideal OpAmp





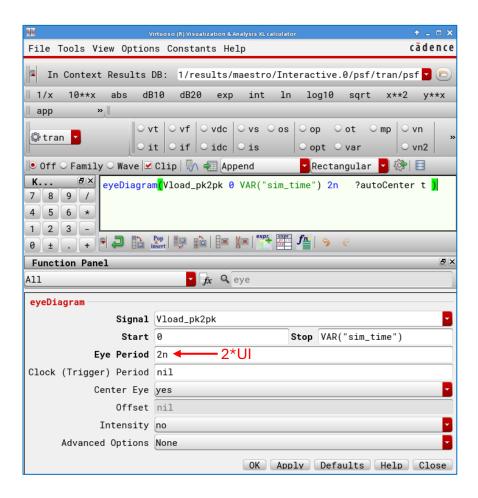
### Simulation Setup



Time Step	Algorithm	State	File	Output	EM/IR	Output		
	<u> </u>							
SIMULATION INTERVAL PARAMETERS								
start								
	_							
outputstart								
TIME STEP PARAMETERS								
step								
	_							
maxstep		1p						
	_							
minstep								

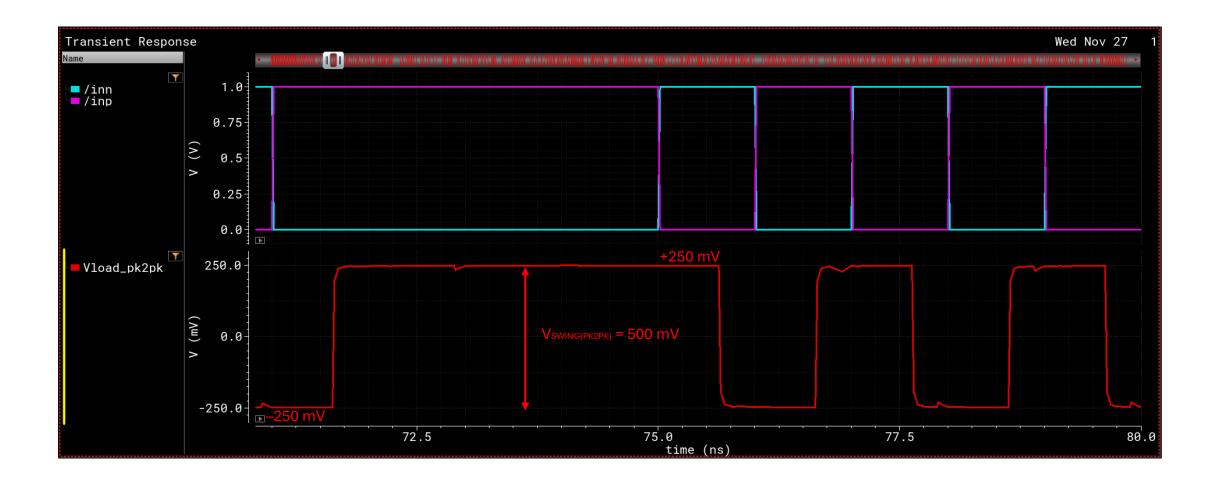
### Measurements

Name	Туре	Details	EvalType
	Filter 🔼	Filter	Filter
*** Voltages ***	expr		point
	signal	/inp	point
	signal	/inn	point
	signal	/outp	point
	signal	/outn	point
	signal	/outp_load	point
	signal	/outn_load	point
	signal	/x	point
	signal	/xx	point
	signal	/vdrv	point
	signal	/vb2	point
	signal	/vb1	point
	signal	/x_rep	point
	signal	/xx_rep	point
	signal	/outp_rep	point
	signal	/outn_rep	point
*** Currents ***	expr		point
/Vdd_I/PLUS_I	signal	/Vdd_I/PLUS	point
/Rout/PLUS_I	signal …	/Rout/PLUS	point
*** Outputs ***	expr		point
Vtxout_pk2pk	expr	(VT("/outp") - VT("/outn"))	point
Vtxout_eye	expr	<pre>eyeDiagram(Vtxout_pk2pk 0 VAR("sim_time") 2e-09 ?autoCenter t)</pre>	point
Vload_pk2pk	expr	(VT("/outp_load") - VT("/outn_load"))	point
Vload_eye	expr	eyeDiagram(Vload_pk2pk 0 VAR("sim_time") 2e-09 ?autoCenter t)	point

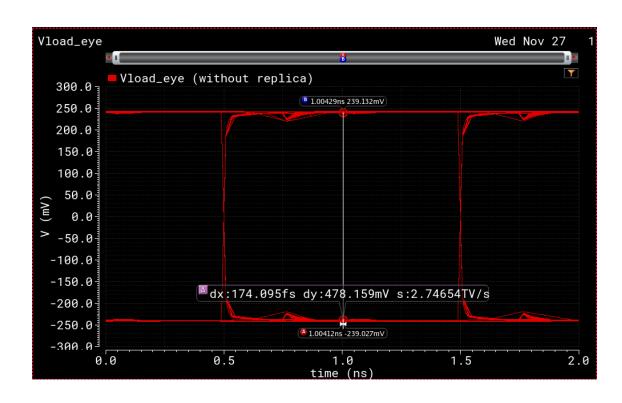


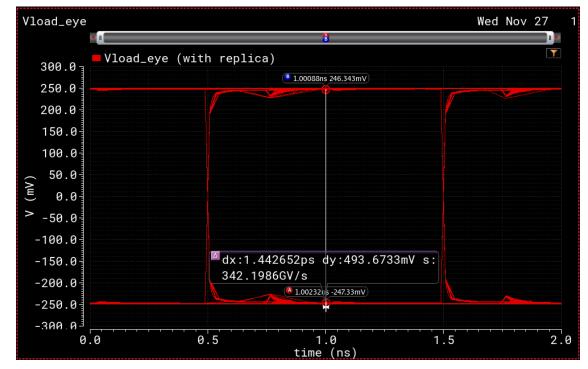
# Simulations & Results

### Waveforms



### Waveforms (Eye-Diagrams)





#### No Replica:

 $V_{SW(pk2pk)} = 478.16 \text{ mV}$ 

#### With Replica:

 $V_{SW(pk2pk)} = 493.67 \text{ mV}$ 

→ More Accurate Swing, at the expense of additional power drawn by an extra branch & the OpAmps

### **Current Consumption**

• Total current drawn = 5 mA

(2.5 mA from main driver + 2.5 mA from Replica branch) (OpAmps not included)