EE 538B CMOS RF IC DESIGN

Midterm Examination No. 2: May 24, 2004

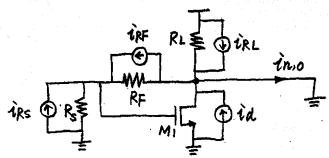
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Student Name:	Solution	
UW Student ID #:	1234567	

You are allowed two sheets of notes. Write legibly. Show all work. State assumptions.

	Problem #	Points Possible	Points
	1	15	
	2	15	
- '	3	30	
	4	25	
	5	15	

1. (15 points) Determine the noise factor of the circuit shown with respect to the source resistance R_S . Neglect channel length modulation effects, all parasitic capacitors and induced gate noise of the MOSFETs. Assume that all the MOSFETs operate in the saturation region.



There are four noise sources:

$$\frac{1}{\ln^2 Rs} = \frac{1}{\ln^2 Rs} \cdot \left(\frac{RFRs}{RF+Rs}\right)^2 \cdot \left(\frac{1}{RF} - \frac{g_m}{r}\right)^2$$

$$i_{x,F}: v_{x} = i_{x}F \cdot \left(\frac{RFRS}{RF+RS}\right)$$

$$i_{x} = i_{x}F \cdot \left(\frac{RS}{RS+RF}\right)$$

$$in, RF = ix - iRF - 9mVX$$

$$= iRF \cdot \frac{Rs}{Rs+RF} - iRF - gm \cdot iRF \cdot \frac{RFRs}{RF+Rs}$$

$$= -iRF \cdot \frac{RF}{RF+Rs} \cdot (1+g_mRs)$$

$$\overline{\hat{l}_{n,RF}^2} = \overline{\hat{l}_{RF}^2} \cdot \left(\frac{RF}{RF+RS}\right)^2 (1+9mRS)^2$$

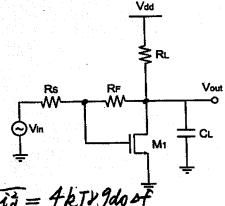
$$\vec{in}_{,RL}^2 = \frac{4kT}{RL}\Delta f$$

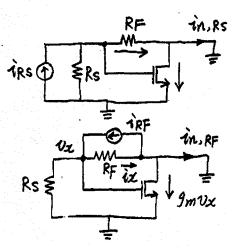
$$F = 1 + \frac{i\hat{n}_{,RF} + i\hat{n}_{,RL} + i\hat{n}_{,d}}{i\hat{n}_{,RS}}$$

$$=1+\frac{4kI}{RF}\Delta f\left(\frac{RF}{RF+RS}\right)^{2}(1+g_{m}R_{S})^{2}+\frac{4kI}{RL}\Delta f+4kIYgdo\Delta f$$

$$\frac{4kI}{RS}\Delta f\cdot\left(\frac{RFRS}{RF+RS}\right)^{2}\cdot\left(g_{m}-\frac{1}{RF}\right)^{2}$$

$$\Rightarrow F = 1 + \frac{RF}{RS} \cdot \frac{(1 + g_m R_S)^2}{(1 - g_m R_F)^2} + \frac{1}{RS} \left(\frac{1}{RL} + \gamma g_{do}\right) \cdot \frac{(RF + RS)^2}{(1 - g_m R_F)^2}$$





2. (15 points) A double balanced passive mixer is shown below. The IF port is terminated by a resistance $R_L=R_S$. What is the conversion gain? Assume the LO is driven by square wave the switching is infinitely fast. The RF signal is sinusoidal with peak value A and the LO square wave has peak value B.

LO port is driven by ideal square wave.

i) If $LO(t) > \overline{LO}(t)$: $R_{S/2} = V_{RF} \Leftrightarrow V_{IF} \Leftrightarrow V_{I$

ii) If Lo(t) < Lo(t):

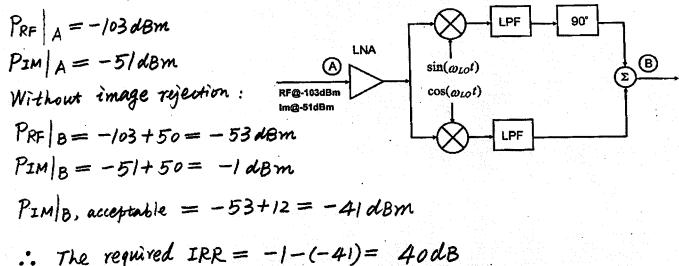
Therefore, $V_{IF}(t) = \frac{1}{2} V_{KF}(t) \cdot \frac{4\pi}{\pi} \left[\cos \omega_{Lot} + \frac{1}{3} \omega_{S} \omega_{Lot} + \frac{1}{5} \omega_{S} 5 \omega_{Lot} + \cdots \right]$ $= \frac{1}{2} A \omega_{S} \omega_{KF} t \cdot \frac{4\pi}{\pi} \left[\omega_{S} \omega_{Lot} + \frac{1}{3} \omega_{S} 3 \omega_{Lot} + \frac{1}{5} \omega_{S} 5 \omega_{Lo} t + \cdots \right]$

Desired IF output term 的 主.A. #. 主 ws(wef-who)t

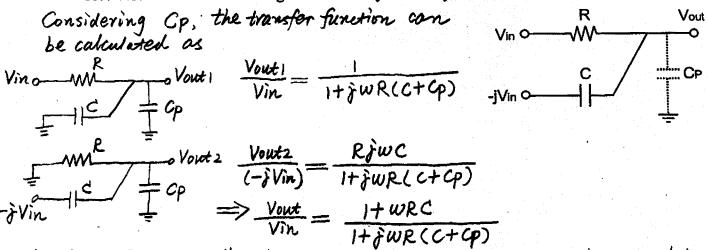
 \Rightarrow Conversion Gain $G_c = \frac{\pm A \cdot \frac{4B}{K} \cdot \pm 1}{A}$

 $\therefore G_{c} = \frac{B}{\pi}$

3. (30 points) (a) A Hartley receiver is shown below. The RF signal power (@ node A) is -103dBm. The power of image interferer (also @ node A) is as high as -51dBm. The gain from node A to node B is 50dB. After down-conversion, the acceptable interference from image frequency can be 12dB higher than the desired signal. Determine the required image reject ratio (IRR).



(b) Suppose the phase shift of 90° is realized by the one stage polyphase filter, as shown below. That is, -45° is realized in one branch and +45° is realized in the other branch. The parasitic capacitance at the output node is C_P =0.1pF. R=1K Ω , C=1pF. What is the IRR of the Hartley receiver? Assume that IRR degradation is only caused by the existence of C_P .



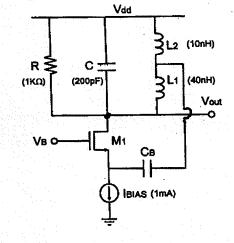
Therefore, Cp affects the phase of transfer function, not the amplitude. At $\omega = -\frac{1}{RC}$, |H(f w o)| = 0. So image signal is suppressed.

=> IRR = ∞ dB. phose errors couse IRR degradation too!

4. (25 points) For the Hartley oscillator shown below, calculate (a) frequency of oscillation, (b) peak output amplitude, (c) loaded and unloaded Qs of the tank, and (d) minimum transconductance required for startup (assume a safety margin of 3X to ensure start-up).

(a)
$$Leq = Li + Lz = 50nH$$

 $wo = \frac{1}{\sqrt{Leq \cdot C}} = \frac{1}{\sqrt{50 \times 10^{-9} \times 200 \times 10^{-12}}}$
 $= 3.16 \times 10^{8} \text{ rad/s}$
 $fo = \frac{wo}{2\pi} = 50.3 \text{ MHz}$



(b)
$$V tank \approx 2 I BIAS R (1-n)$$

$$n = \frac{L_2}{L_1 + L_2} = \frac{10}{10 + 40} = 0.2$$

Vtank = 2×10-3×103×(1-0.2)

: Vtank
$$\approx 1.6 V$$

(c)
$$Gm = \frac{2I_{BIAS}}{V_I}$$
, $V_I = n \cdot V_{tank} = 0.2 \times 1.6 = 0.32 V$
 $Gm = \frac{2 \times 10^{-3}}{0.32} = 6.25 \text{ mS}$
 $Req = R II \frac{1}{n^2 Gm} = 800 \Omega$

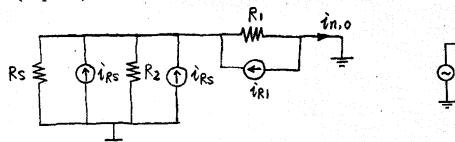
Qualoaded =
$$WoRC = 63.25$$

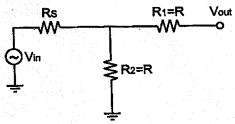
Quaded = $WoReq C = 50.60$

(d)
$$9m \ge \frac{1}{R(n-n^2)} = \frac{1}{10^3 \times (0.2-0.2^2)} = 6.25 ms$$

 $9m, \min = 3 \times 6.25 = 18.75 ms$

5. (15 points) A resistive network is shown below:





(a) What is the noise factor of the above circuit with respect to source resistance R_S ?

There are three moise sources:
$$\frac{1}{R_s^2} = \frac{4kT}{R_s} \Delta f, \quad \frac{1}{1R_1^2} = \frac{4kT}{R_1} \Delta f, \quad \frac{1}{1R_2^2} = \frac{4kT}{R_2} \Delta f$$

$$\frac{1}{1R_s^2} = \frac{4kT}{1R_s} \Delta f, \quad \frac{1}{1R_1^2} = \frac{4kT}{R_s} \Delta f, \quad \frac{1}{1R_2^2} = \frac{4kT}{R_s} \Delta f, \quad \frac{1}{1R_s^2} \Delta f$$

$$\frac{1}{1R_s^2} = \frac{1}{1R_s^2} \cdot \left(\frac{1}{R_s} + \frac{1}{1R_s} + \frac{1}{1R_s}\right)^2 = \frac{4kT}{R} \Delta f, \quad \frac{1}{1R_s} + \frac{1}{1R_s} \Delta f \cdot \left(\frac{R_s + R}{2R_s + R}\right)^2$$

$$\therefore F = 1 + \frac{1}{1R_s^2} + \frac{1}{1R_s^2} + \frac{1}{1R_s^2} = 1 + \frac{R_s}{R} + \frac{R_s}{R} \cdot \left(\frac{R_s + R}{R_s}\right)^2$$

$$\Rightarrow F = 3 + 2 \cdot \frac{R_s}{R} + \frac{R}{R_s}$$

(b) What value of R minimizes the noise factor F?

$$\frac{\partial F}{\partial R} = -2\frac{Rs}{R^2} + \frac{1}{Rs} = 0$$

$$\Rightarrow Ropt = \sqrt{2}Rs$$