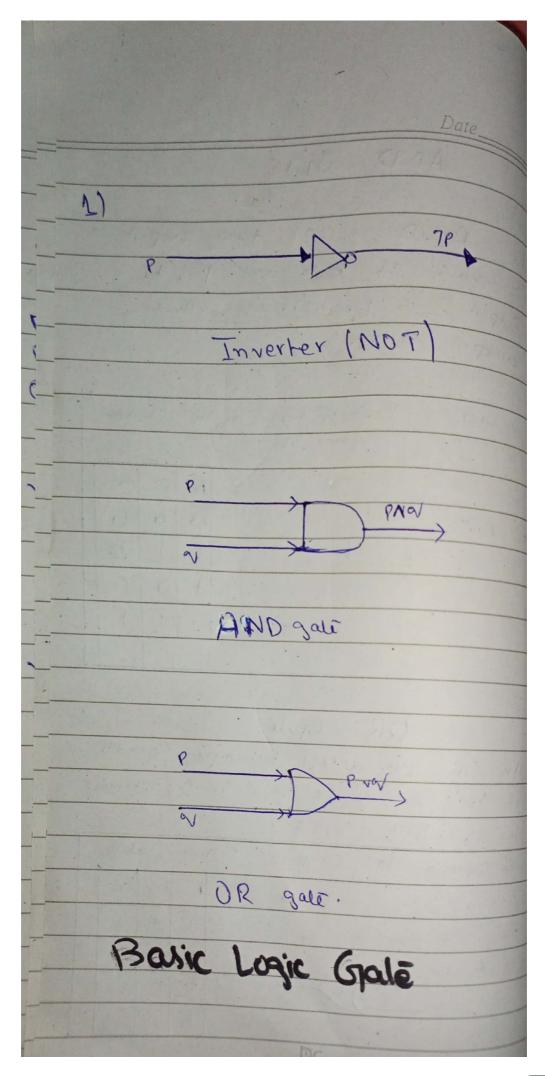
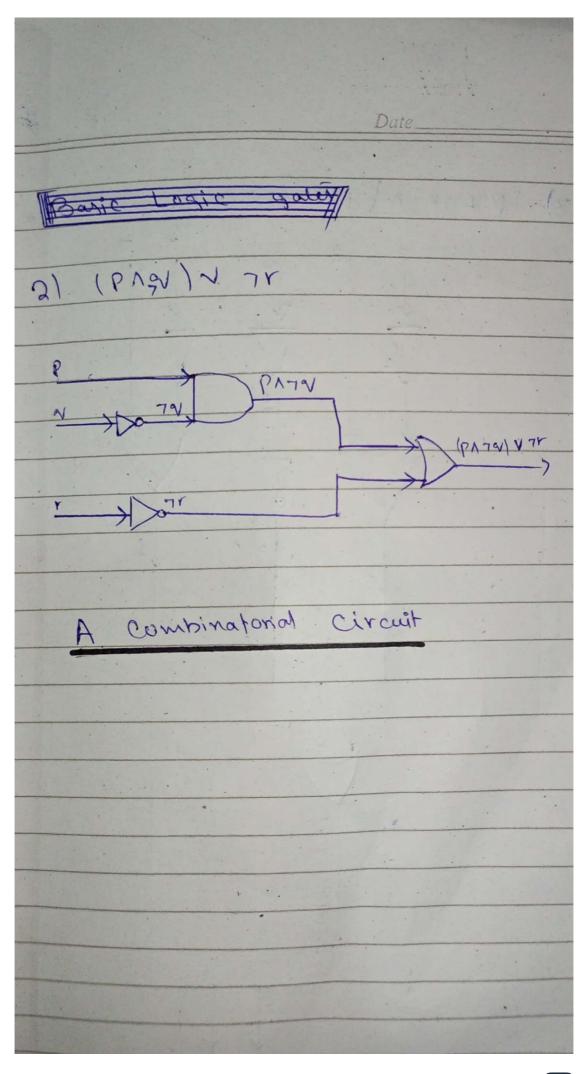
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Logic Gales
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A device that perform a
basic operation on electrical signals.
Circuits.
Crater combine to perform
more complicated tenk.
Boolean expressions
ne pogeon glospha, or
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tono rapido 102/c.
The state of the s
Logic diagrams
A graphical representation
of a circuit each gate has it
own symbol.
Truin Table
A table showing all
input nable and the associated
ostput valuer.
$\mathbb{R}^{C_{j}}$

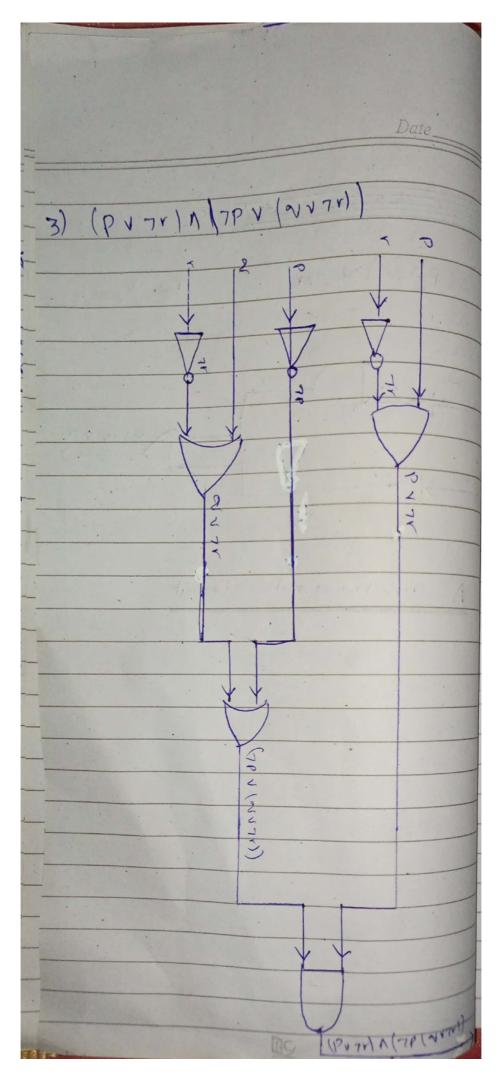
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the opposite signal	as	autout.	
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Boolean Expression Logic Diagram	Trul	Le Table	7
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Signals of bolk are I, the
output is 1, otherwise the
601POF 15 0.
Boolean Logic Diagram Truth Table
Exbrangen Sampol
ABX
X = A.B A 0 0 0
8. 100
THE PROPERTY OF THE PARTY OF TH
DR Gali
An OR gate accept two import Sognals
if both are 0, the output is 0; otherwise
The output is 1.
Booken Expression logic Diagram Trush Table.
Colmos . H B N .
X=A+BA
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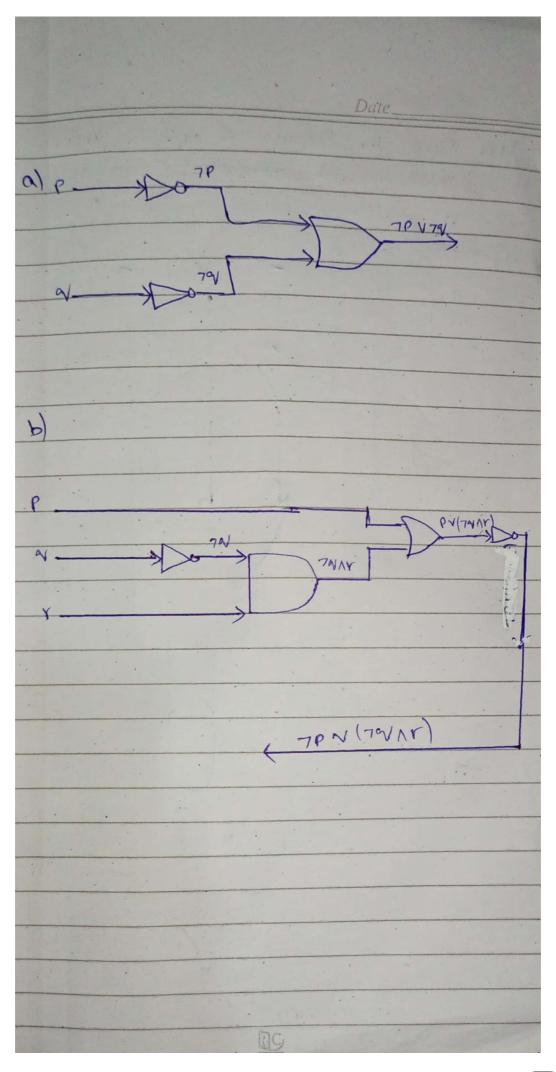




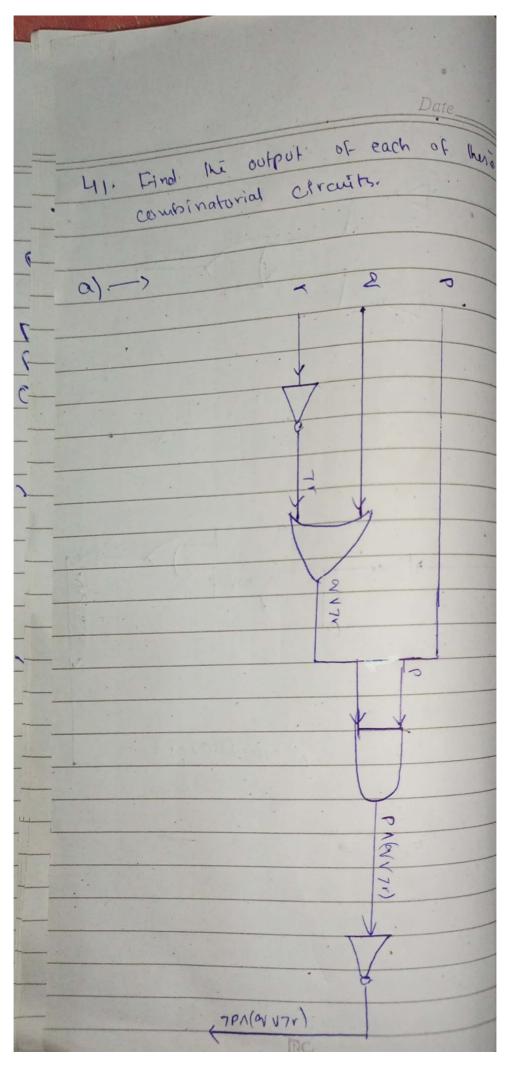


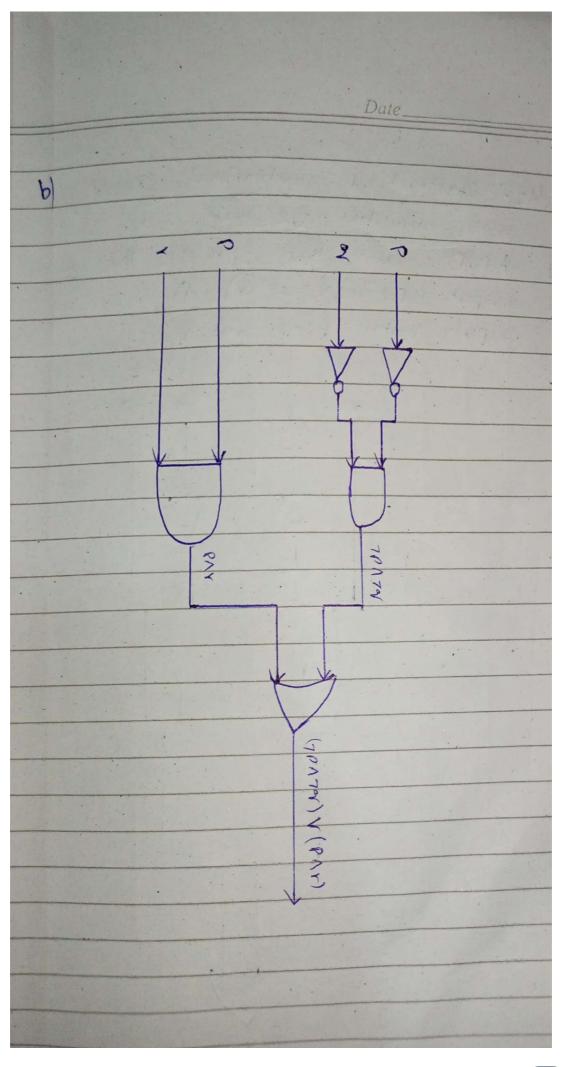




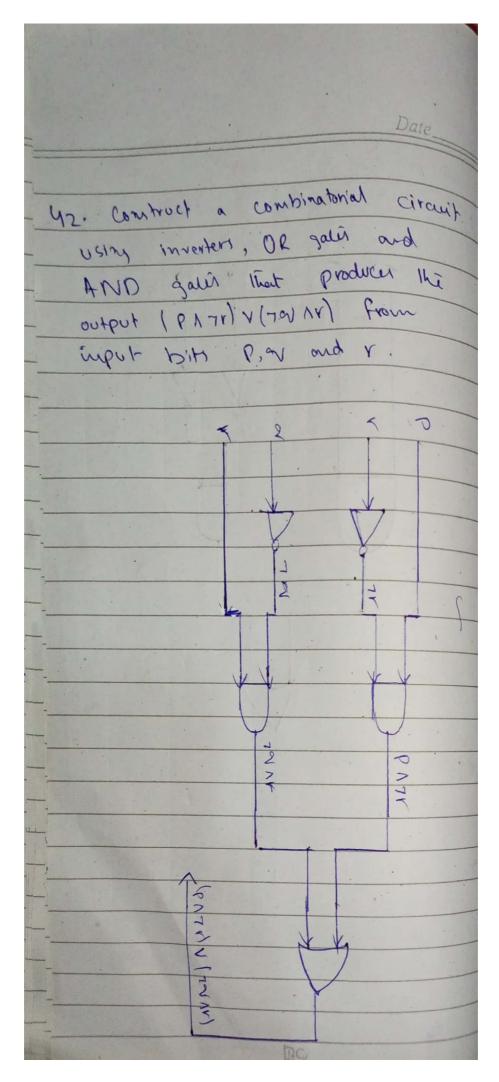


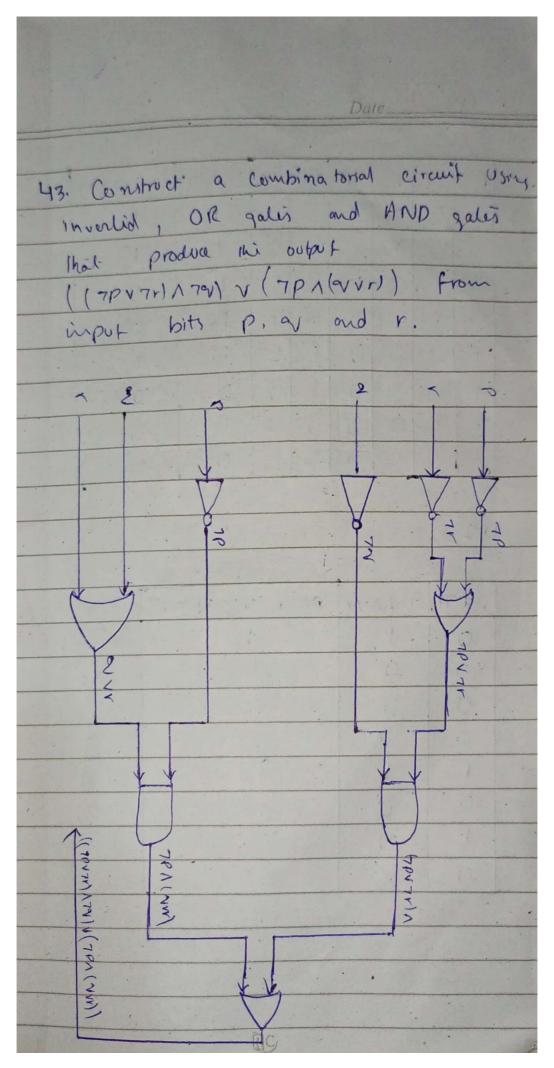












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