Multiply 0x011 & 0x0AB

# **mul 0x011, 0x0AB**

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| First Operand | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
| 0 | 0 | 0 | 1 | 0 | 0 | 0 | 1 |
| Hexa-Decimal Value | 0x11 | | | **Decimal Value to mul** | 17 | | | |
| Second Operand | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
| 1 | 0 | 1 | 0 | 1 | 0 | 1 | 1 |
| Hexa-Decimal Value | 0xAB | | | **Decimal Value to mul** | 171 | | | |
| R1 | Bit **7** | Bit **6** | Bit **5** | Bit **4** | Bit **3** | Bit **2** | Bit **1** | Bit **0** |
| 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 |
| R0 | Bit **7** | Bit **6** | Bit **5** | Bit **4** | Bit **3** | Bit **2** | Bit **1** | Bit **0** |
| 0 | 1 | 0 | 1 | 1 | 0 | 1 | 1 |
| Hexa-Decimal Value | 0x5B0B | | | **Decimal Value** | 2907 | | | |
| Control Status Register | **I** | **T** | **H** | **S** | **V** | **N** | **Z** | **C** |
| 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| Alterations |  |  |  |  |  |  |  |  |
| Reason for alterations |  |  |  |  |  |  |  |  |

# **muls 0x011, 0x0AB**

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| First Operand | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
| 0 | 0 | 0 | 1 | 0 | 0 | 0 | 1 |
| Hexa-Decimal Value | 0x11 | | | **Decimal Value to mul** |  | | | |
| Second Operand | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
| 1 | 0 | 1 | 0 | 1 | 0 | 1 | 1 |
| Hexa-Decimal Value | 0xAB | | | **Decimal Value to mul** |  | | | |
| R1 | Bit **7** | Bit **6** | Bit **5** | Bit **4** | Bit **3** | Bit **2** | Bit **1** | Bit **0** |
| 1 | 1 | 1 | 1 | 1 | 0 | 1 | 0 |
| R0 | Bit **7** | Bit **6** | Bit **5** | Bit **4** | Bit **3** | Bit **2** | Bit **1** | Bit **0** |
| 0 | 1 | 0 | 1 | 1 | 0 | 1 | 1 |
| Hexa-Decimal Value | 0XFA5B | | | **Decimal Value** |  | | | |
| Control Status Register | **I** | **T** | **H** | **S** | **V** | **N** | **Z** | **C** |
| 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 |
| Alterations |  |  |  |  |  |  |  |  |
| Reason for alterations |  |  |  |  |  |  |  |  |

# **mulsu 0x011, 0x0AB**

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| First Operand | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
| 0 | 0 | 0 | 1 | 0 | 0 | 0 | 1 |
| Hexa-Decimal Value | 0x11 | | | **Decimal Value to mul** | 17 | | | |
| Second Operand | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
| 1 | 0 | 1 | 0 | 1 | 0 | 1 | 1 |
| Hexa-Decimal Value | 0xAB | | | **Decimal Value to mul** | 171 | | | |
| R1 | Bit **7** | Bit **6** | Bit **5** | Bit **4** | Bit **3** | Bit **2** | Bit **1** | Bit **0** |
| 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 |
| R0 | Bit **7** | Bit **6** | Bit **5** | Bit **4** | Bit **3** | Bit **2** | Bit **1** | Bit **0** |
| 0 | 1 | 0 | 1 | 1 | 0 | 1 | 1 |
| Hexa-Decimal Value | 0x5B0B | | | **Decimal Value** |  | | | |
| Control Status Register | **I** | **T** | **H** | **S** | **V** | **N** | **Z** | **C** |
| 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| Alterations |  |  |  |  |  |  |  |  |
| Reason for alterations |  |  |  |  |  |  |  |  |