# IMPLEMENTATION OF BOOLEAN LOGIC

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***Abstract*—xyz**

***Keywords*—IEEE, plantilla, LaTeX, ecuaciones,**

## I. Introduction

ONCE . .

## II. concept,Methodology

### A. Definition

### B. Definition 2

### C. XYZ

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## III. Solution proposal

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### A. xyz LaTeX

$$I_D = \frac{qN_A n_i^2}{N_D}\left(\frac{\alpha V_{GS}^2}{\mu_o}\right)^3 \tag{1}$$

$$V_o \approx \int e^X dX \tag{2}$$

(**??**) (**??**) $I_D$ $V_o$ .

$$i = \frac{v}{R} \Longrightarrow i = \frac{5}{500} = 10mA$$

## IV. Simulation and Results

### A. Figures en LaTeX

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asdfg
[**?**].

Figure 1. And and or gate

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**??** $I_1$ contra $V_1$. *DIA* [**?**]

%includegraphics[scale=0.55]lvdt4

Figure 2. Diagrm integrated AD598.

## V. Implementation and solution

PCB (*Printed Circuit Board*) *scripts*

## VI. Results

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### A. *Tablas en LaTeX*

Table I
NOMBRE DE LA TABLA

| Símbolo | Nombre | Código Latex |
|---|---|---|
| $\alpha$ | alpha | `\alpha` |
| $\mu$ | mu | `\mu` |
| $\beta$ | beta | `\beta` |
| $\Omega$ | Omega | `\Omega` |

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## VII. CONCLUSION

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