Mukesh Tiwari

Education

2016–2020 **PhD, Computer Science**, *Australian National University*, Canberra, Australia.
Thesis Submitted

2004–2009 **Integrated Post Graduate**, *Indian Institute of Information Technology & Management*, Gwalior, India.

PhD thesis

Title *Formally Verified Verifiable Electronic Voting Scheme*

Supervisor Prof. Dirk Pattinson

Description We focussed on the three main concerns posed by electronic voting, i.e. correctness, privacy, and verifiability. We addressed the correctness concern by using a theorem prover to implement the vote counting algorithm, privacy concern by using homomorphic encryption, and verifiability concern by generating a independently checkable scrutiny sheet (certificate). Our work has been carried out in Coq theorem prover.

Experience

2020– **Research Fellow**, *University of Melbourne*, Melbourne, Australia.

2013–2015 **Lecturer**, *International Institute of Information Technology*, Bhubaneswar, India.

2012–2013 **Haskell Developer**, *Parallel Scientific*, Colorado, USA.

2009–2012 **Technical Assistant**, *Government of India*, Kolkata, India.

2008–2008 **Summer Intern**, *Arcelor Mittal, Research & Development Technological Centre*, Avilés, Spain.

Skills

Coding Coq, Haskell, OCaml, Idris, Racket, Clojure, Isabelle, Python, C, Java

Language Hindi, English

Awards

HDR Fee Remission Merit Scholarship

ANU PhD Scholarship (International)

Full Scholarship to attend DeepSpec Summer School 2018, Princeton

Travel Scholarship to attend Marktoberdorf Summer School 2019

References

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- Prof. Dirk Pattinson, Research School of Computer Science, Australian National University, Canberra, dirk.pattinson@anu.edu.au

- Prof. Rajeev Goré, Research School of Computer Science, Australian National University, Canberra, rajeev.gore@anu.edu.au

- Thomas Haines, Department of Mathematical Sciences, Norwegian University of Science and Technology, Gløshaugen, thomas.haines@ntnu.no

Publications

[1] Lyria Bennett Moses, Rajeev Goré, Ron Levy, Dirk Pattinson, and Mukesh Tiwari. No more excuses: Automated synthesis of practical and verifiable vote-counting programs for complex voting schemes. In Robert Krimmer, Melanie Volkamer, Nadja Braun Binder, Norbert Kersting, Olivier Pereira, and Carsten Schürmann, editors, *Electronic Voting*, pages 66–83, Cham, 2017. Springer International Publishing.

[2] Milad K. Ghale, Rajeev Goré, Dirk Pattinson, and Mukesh Tiwari. Modular formalisation and verification of stv algorithms. In Robert Krimmer, Melanie Volkamer, Véronique Cortier, Rajeev Goré, Manik Hapsara, Uwe Serdült, and David Duenas-Cid, editors, *Electronic Voting*, pages 51–66, Cham, 2018. Springer International Publishing.

[3] Thomas Haines, Rajeev Goré, and Mukesh Tiwari. Verified verifiers for verifying elections. In *Proceedings of the 2019 ACM SIGSAC Conference on Computer and Communications Security*, CCS '19, page 685–702, New York, NY, USA, 2019. Association for Computing Machinery.

[4] Thomas Haines, Dirk Pattinson, and Mukesh Tiwari. Verifiable homomorphic tallying for the schulze vote counting scheme. In *Verified Software: Theories, Tools, and Experiments*. Springer, 2019. to appear.

[5] Dirk Pattinson and Mukesh Tiwari. Schulze voting as evidence carrying computation. In Mauricio Ayala-Rincón and César A. Muñoz, editors, *Interactive Theorem Proving*, pages 410–426, Cham, 2017. Springer International Publishing.