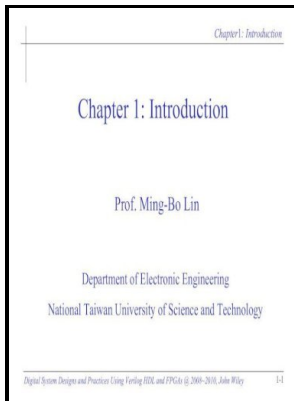# Digital systems design and practices - using Verilog HDL and FPGAs

J. Wiley & Sons - Digital Systems Design and Practice: Using Verilog HDL and FPGAs: Lin, Ming

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Verilog (Computer hardware description language)

Field programmable gate arrays

Digital electronicsDigital systems design and practices - using Verilog HDL and FPGAs

-Digital systems design and practices - using Verilog HDL and FPGAs

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To design and implement an SoC-based product, it proves necessary to totally or partly rely on the hardware description language HDL synthesis flow and field programmable gata array FPGA devices or cell libraries.

DIGITAL DESIGN AND VERILOG HDL FUNDAMENTALS

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