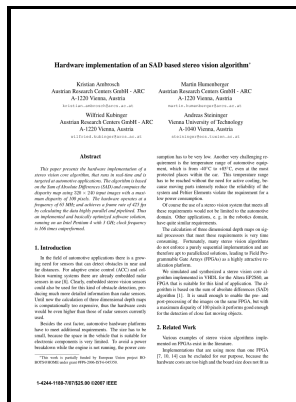


Video-rate stereo vision on reconfigurable hardware.

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Description: -

-Video-rate stereo vision on reconfigurable hardware.

- Canadian theses = -- Thèses canadiennes Video-rate stereo vision on reconfigurable hardware.

Notes: Thesis (M.A.Sc.) -- University of Toronto, 2003.

This edition was published in 2003



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Tags: #Stereo #vision #algorithms #in #reconfigurable #hardware #for #robotics #applications

Video

His present research interests concern the design of digital circuits for image processing, embedded systems, system on programmable chips, high performance advanced FPGA design, High-Performance Reconfigurable Computing, fault-tolerant computing. Below is available a collection of most popular stereo camera sensors with different specifications and designed for different applications.

Hybrid algorithms for hardware/software partitioning and scheduling on reconfigurable devices

The same application controls the USB 2. Stereo matching is a fundamental problem of computer vision, and emerging applications, such as 3-D gesture recognition and automotive navigation, demand fast and high-quality stereo matching.

What hardware and software do I need for hardware stereo display using Maestro?

With a set of configuration interfaces, the machine can process many different sizes of stereo pair images. Related Work Different authors have proposed architectures to accelerate the computation of dense disparity maps in real-time applications, trying to maintain the accuracy of the results.

ChipVision2

Hence, the creation flow should as. As shown in Figure 10, bad matches are in average lower than 10%.

Related Books

- [François Morellet - \[exposition\] du 17 mai 1973 au 2 septembre 1973, Ville de Nantes, Musée des beau](#)
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