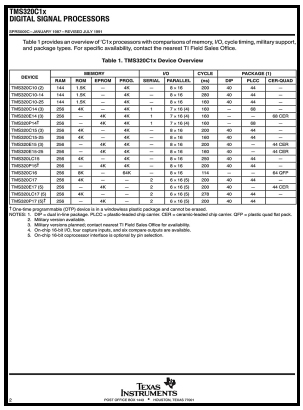
TMS320C1X

Texas Instruments - Texas Instruments TMS320



TMS320C1x
DIGITAL SIGNAL PROCESSORS

SPRS005C—JANUARY 1987—REVISED JULY 1991

Table 1 provides an overview of 'C1x processors with comparisons of memory, I/O, cycle timing, military support, and package types. For specific availability, contact the nearest TI Field Sales Office.

Table 1. TMS320C1x Device Overview

DEVICE	MEMORY				I/O		CYCLE	PACKAGE (1)		
	RAM	ROM	EPROM	PROG.	SERIAL	PARALLEL	(ns)	DIP	PLCC	CER-QUAD
TMS320C10 (2)	144	1.5K	—	4K	—	8 × 16	390	40	44	—
TMS320C10-14	144	1.5K	—	4K	—	8 × 16	280	40	44	—
TMS320C10-25	144	1.5K	—	4K	—	8 × 16	160	40	44	—
TMS320C14 (3)	256	4K	—	4K	1	7 × 16 (4)	160	—	68	—
TMS320E14 (3)	256	—	4K	4K	1	7 × 16 (4)	160	—	—	68 CER
TMS320P14†	256	—	4K	4K	1	7 × 16 (4)	160	—	68	—
TMS320C15 (3)	256	4K	—	4K	—	8 × 16	200	40	44	—
TMS320C15-35	256	4K	—	4K	—	8 × 16	160	40	44	—
TMS320E15 (3)	256	—	4K	4K	—	8 × 16	200	40	—	44 CER
TMS320E15-35	256	—	4K	4K	—	8 × 16	160	40	—	44 CER
TMS320C16	256	4K	—	4K	—	8 × 16	350	40	44	—
TMS320P16†	256	—	4K	4K	—	8 × 16	200	40	44	—
TMS320C19	256	8K	—	8K	—	8 × 16	114	—	—	64 QFP
TMS320C17	256	4K	—	—	2	6 × 16 (5)	200	40	44	—
TMS320E17 (3)	256	—	4K	—	2	6 × 16 (5)	200	40	—	44 CER
TMS320LC17 (3)	256	4K	—	—	2	6 × 16 (5)	278	40	44	—
TMS320P17 (3)†	256	—	4K	—	2	6 × 16 (5)	200	40	44	—

† One-time programmable (OTP) device is in a windowless plastic package and cannot be erased.
NOTES: 1. CER = ceramic-leaded chip carrier; PLCC = plastic-leaded chip carrier; CER = ceramic-leaded chip carrier; QFP = plastic quad flat pack.
2. Military versions available.
3. Military versions planned; contact nearest TI Field Sales Office for availability.
4. On-chip 16-bit I/O, four capture inputs, and six compare outputs are available.
5. On-chip 16-bit coprocessor interface is optional by pin selection.

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5 Assembler Directives

In this case, each member must have a unique name. With this directive, you can define up to 32 local macro substitution symbols including parameters per macro.

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The types created are analogous to the struct and union types of the C language. The value is stored in the substitution symbol table.

5 Assembler Directives

The expanded line appears below the actual source line. It cannot be used when defining a nested union.

Source code to an IDA processor module: TMS320C10/TMS320C1X

These C1x devices utilize a modified Harvard architecture to optimize speed and flexibility, implementing functions in hardware that other processors implement through microcode or software. The optional count operand, if used, must be a well-defined integer expression.

5 Assembler Directives

It can be used in macro definitions to suppress the listing of the macro expansion. TMS320C2000 COFF Assembler Version x.

5 Assembler Directives

Local labels are temporary labels that can be used as operands for jump instructions. This example shows how the.

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