

TMS320C1X

Texas Instruments - Texas Instruments TMS320

TMS320C1X DIGITAL SIGNAL PROCESSORS

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Table 1 provides an overview of C1x processors with components of memory, I/O, cycle timing, military support, and package types. For specific availability, contact the nearest TI Field Sales Office.

Table 1. TMS320C1x Device Overview

DEVICE	YEAR	MEMORY	PERIPHERALS	IO	CLOCK	PACKAGE (1)
TMS320C10	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C11	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C12	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C13	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C14	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C15	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C16	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C17	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C18	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C19	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C20	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C21	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C22	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C23	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C24	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C25	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C26	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C27	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C28	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C29	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C30	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C31	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C32	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C33	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C34	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C35	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C36	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C37	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C38	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C39	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C40	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C41	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C42	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C43	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C44	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C45	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C46	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C47	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C48	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C49	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C50	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C51	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C52	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C53	1984	1 Kbit	—	40	—	28-pin DIP
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TMS320C58	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C59	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C60	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C61	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C62	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C63	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C64	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C65	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C66	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C67	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C68	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C69	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C70	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C71	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C72	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C73	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C74	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C75	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C76	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C77	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C78	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C79	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C80	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C81	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C82	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C83	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C84	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C85	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C86	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C87	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C88	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C89	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C90	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C91	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C92	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C93	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C94	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C95	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C96	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C97	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C98	1984	1 Kbit	—	40	—	28-pin DIP
TMS320C99	1984	1 Kbit	—	40	—	28-pin DIP

1. Only use these packages for C1x processors. For a comprehensive package catalog, see the TI website at www.ti.com.
 2. Military version available.
 3. Military version available.
 4. Military version available.
 5. Military version available.
 6. Military version available.
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 99. Military version available.
 100. Military version available.

Description: -
-TMS320C1X

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5 Assembler Directives

In this case, each member must have a unique name. With this directive, you can define up to 32 local macro substitution symbols including parameters per macro.

TMS320C1X datasheet

The types created are analogous to the struct and union types of the C language. The value is stored in the substitution symbol table.

5 Assembler Directives

The expanded line appears below the actual source line. It cannot be used when defining a nested union.

Source code to an IDA processor module: TMS320C10/TMS320C1X

These C1x devices utilize a modified Harvard architecture to optimize speed and flexibility, implementing functions in hardware that other processors implement through microcode or software. The optional count operand, if used, must be a well-defined integer expression.

5 Assembler Directives

It can be used in macro definitions to suppress the listing of the macro expansion. TMS320C2000 COFF Assembler Version x.

5 Assembler Directives

Local labels are temporary labels that can be used as operands for jump instructions. This example shows how the.

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