

# Uccayute nilal.

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Description: -  
-Uccayute nilal.  
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Notes: Romanized.  
This edition was published in 1976



Filesize: 64.65 MB

Tags: #[PATCH #v3 #11/14] #scsi: #lpfc: #Use #irq\_set\_affinity

**Re: [PATCH v3 06/14] RDMA/irdma: Use irq\_set\_affinity\_and\_hint**

We would like to create devices with correct affinity from the beginning.

**Re: [PATCH v2 00/14] genirq: Cleanup the usage of irq\_set\_affinity\_hint**

In any case, I will make the change and re-post. Hence, there is no need for the drivers to enforce their own affinity for the spreading of interrupts.

**Re: [PATCH v3 06/14] RDMA/irdma: Use irq\_set\_affinity\_and\_hint**

. Currently, netdev users who don't want irqbalance are digging into their procs, reconfigure affinity on already existing devices and hope for the best.

**Question 2 (a) Design a causal FIR lowpass filter,**

Testing ----- In terms of testing, I have performed some basic testing on x86 to verify things such as the interrupts are evenly spread on all CPUs, hint mask is correctly set etc. I got a little confused from our last conversation about mlx5.

**Re: [PATCH v2 00/14] genirq: Cleanup the usage of irq\_set\_affinity\_hint**

Not sure about that, but I think that only mlx5 implements SIOV model. In this patch-set, I have done that for a few drivers and will hopefully try to move the remaining of them in the coming days.

**Re: [PATCH v3 06/14] RDMA/irdma: Use irq\_set\_affinity\_and\_hint**

Hence, there is no need for the drivers to enforce their own affinity for the spreading of interrupts.

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