

High sample-rate Givens rotations for recursive least squares

typescript - Analysis of a recursive least squares hyperbolic rotation algorithm for signal processing



Description: -

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High sample

In addition, we have proposed sum, delay, and product relaxed look-ahead techniques to design inherently pipelined adaptive digital filters. An estimated sample-rate of 136 MHz could be achieved using a standard cell approach and O. The proposed pipelined architectures can be operated at arbitrarily high sample rate without degrading the filter convergence behavior.

On Pipelined Implementations of QRD

Past Research Interests of Prof. At this rate, the QRD-RLS systolic array can converge and output filter weights in 5.

British Library EThOS: High sample

An estimation interpretation of the adaptive control problem is introduced first. We have proposed an adder architecture which was proved to be faster than the previously known fastest Brent-Kung binary-look-ahead adder. In our group, research work on both soft-decision and hard-decision error control codec has been extensively carried out, which includes the recently discovered powerful Convolutional Turbo Codes, Block Turbo Codes, LDPC Codes a re-discovery , conventional Convolutional Codes and Reed-Solomon Codes.

Order

Yuen, Kenneth Abend and Raymond S.

Recursive Least

As a result, the exhaustive-search-based root computation from the second iteration of the factorization step is circumvented with more than 99% probability. In order to attain high performance, parallel implementation strategies such as systolic methods have been applied.

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